

20 V, 3.5 A PNP low VCEsat transistor

15 September 2023

### 1. General description

PNP low  $V_{\text{CEsat}}$  transistor in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4021NT

### 2. Features and benefits

- Very low collector-emitter saturation voltage V<sub>CEsat</sub>
- + High collector current capability  ${\rm I}_{\rm C}$  and  ${\rm I}_{\rm CM}$
- + High collector current gain ( $h_{FE}$ ) at high  $I_C$
- High energy efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

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- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

### 4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-20	V
I <sub>C</sub>	collector current		-	-	-3.5	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	-8	А
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C$ = -4 A; $I_B$ = -400 mA; pulsed; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	55	82.5	mΩ

# nexperia

# 5. Pinning information

Table 2	. Pinning info	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	E	emitter		J
3	С	collector		B
			1 2 SOT23	sym013

# 6. Ordering information

### Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBSS4021PT-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

# 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PBSS4021PT-Q	%BJ

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5. Limiting values

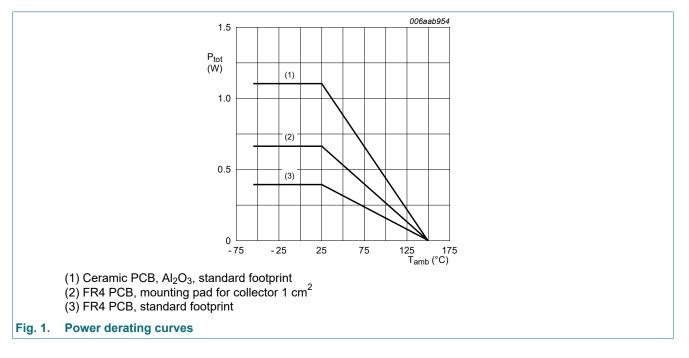
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-20	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-20	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
l <sub>C</sub>	collector current			-	-3.5	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-8	А
I <sub>B</sub>	base current			-	-1	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	390	mW
			[2]	-	660	mW
			[3]	-	1100	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



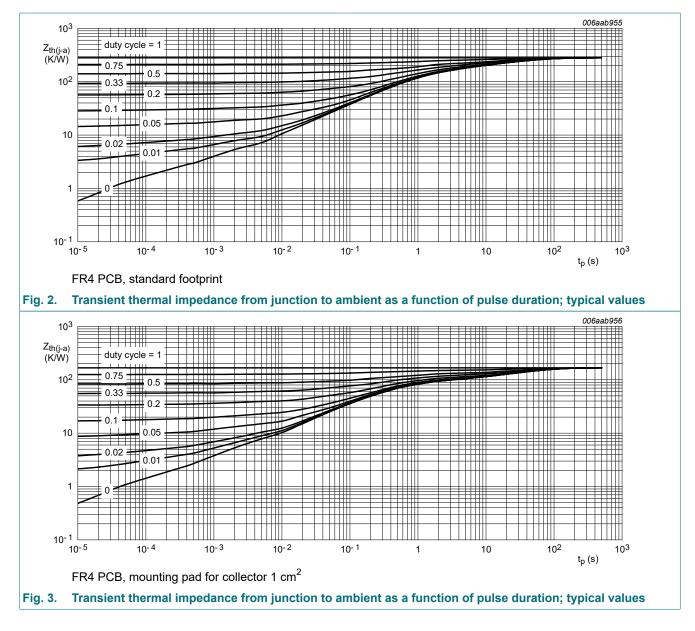
# 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1]	-	-	320	K/W
			[2]	-	-	190	K/W
			[3]	-	-	115	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	62	K/W

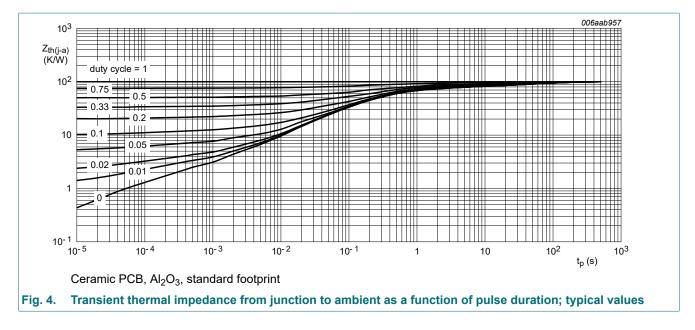
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



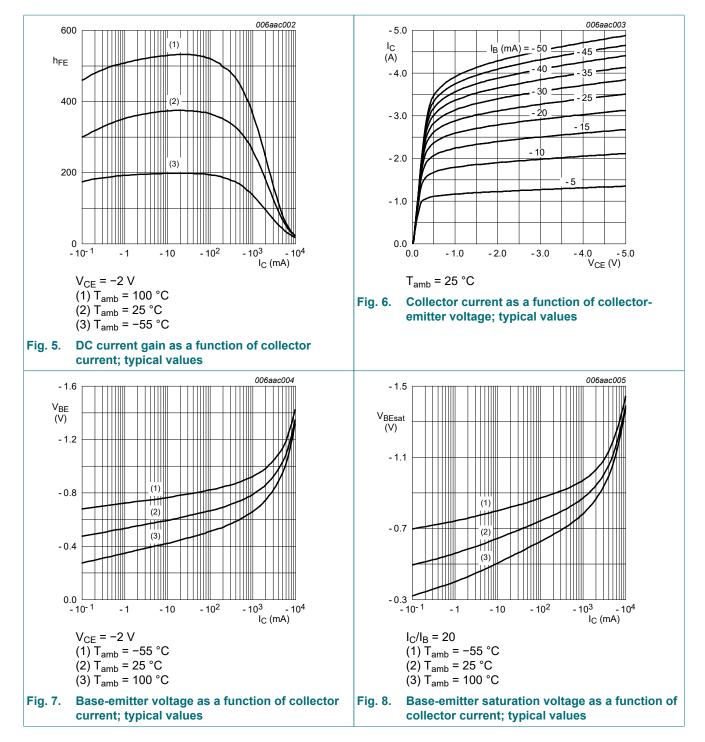
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# **10. Characteristics**

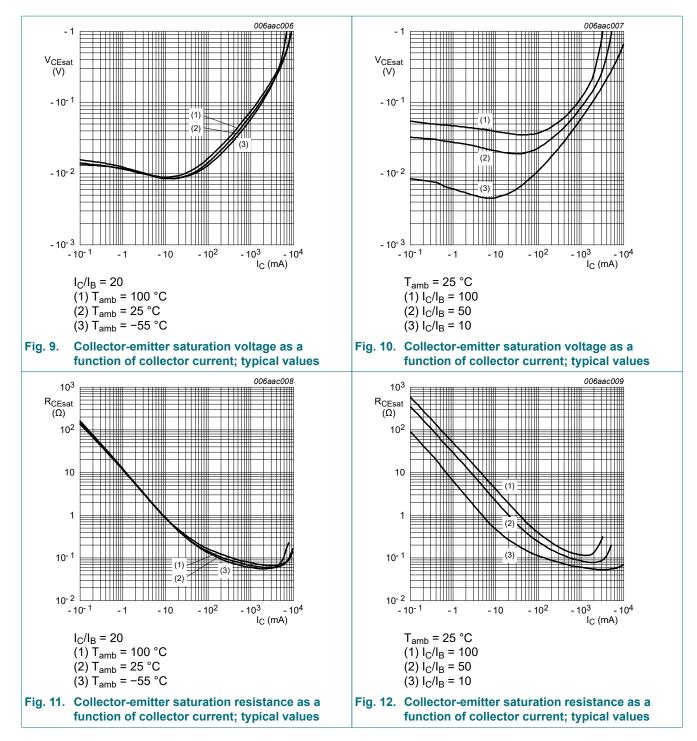
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
	current	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-55	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
I <sub>CES</sub>	collector-emitter cut-off current	$V_{CE}$ = -16 V; $V_{BE}$ = 0 V; $T_{amb}$ = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	250	400	-	
		$V_{CE}$ = -2 V; I <sub>C</sub> = -1 A; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	200	320	-	
		$V_{CE}$ = -2 V; $I_C$ = -2 A; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02; $T_{amb}$ = 25 °C	140	220	-	
		$V_{CE}$ = -2 V; $I_{C}$ = -4 A; pulsed; $t_{p} \le$ 300 µs; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	60	100	-	
		$V_{CE}$ = -2 V; $I_C$ = -6 A; pulsed; $t_p \le$ 300 µs; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	30	50	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -1 A; I <sub>B</sub> = -50 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-70	-105	mV
		I <sub>C</sub> = -1 A; I <sub>B</sub> = -10 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-115	-170	mV
		I <sub>C</sub> = -2 A; I <sub>B</sub> = -40 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-160	-240	mV
		I <sub>C</sub> = -4 A; I <sub>B</sub> = 200 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-250	-375	mV
		$I_{C}$ = -4 A; $I_{B}$ = -400 mA; pulsed; $t_{p}$ ≤	-	-220	-330	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	55	82.5	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = -1 A; I <sub>B</sub> = -100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-0.89	-1	V
		I <sub>C</sub> = -4 A; I <sub>B</sub> = -400 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-1.1	-1.2	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$V_{CE}$ = -2 V; I <sub>C</sub> = -2 A; pulsed; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-0.83	-0.9	V
t <sub>d</sub>	delay time	V <sub>CC</sub> = -12.5 V; I <sub>C</sub> = -1 A; I <sub>Bon</sub> = -0.05 A;	-	10	-	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = 0.05 A; T <sub>amb</sub> = 25 °C	-	60	-	ns
t <sub>on</sub>	turn-on time		-	70	-	ns
t <sub>s</sub>	storage time		-	340	-	ns
t <sub>f</sub>	fall time		-	120	-	ns
t <sub>off</sub>	turn-off time		-	460	-	ns
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -10 V; I <sub>C</sub> = -100 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	-	155	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	40	-	pF

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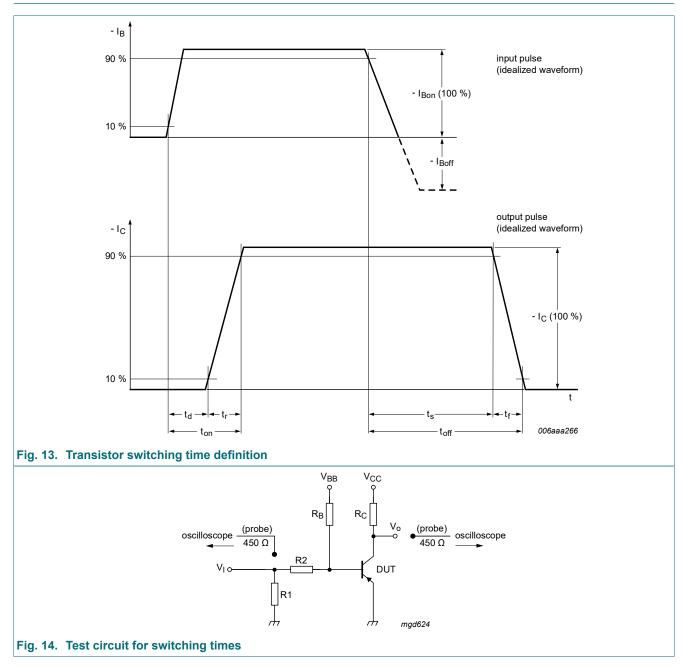


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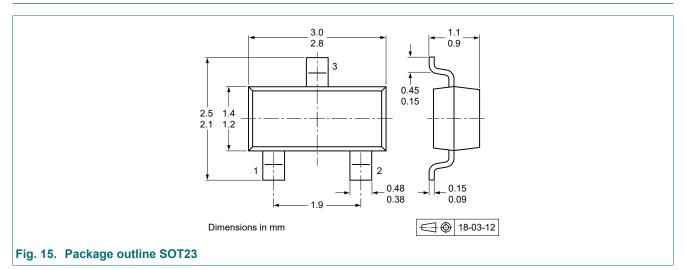
# **11. Test information**



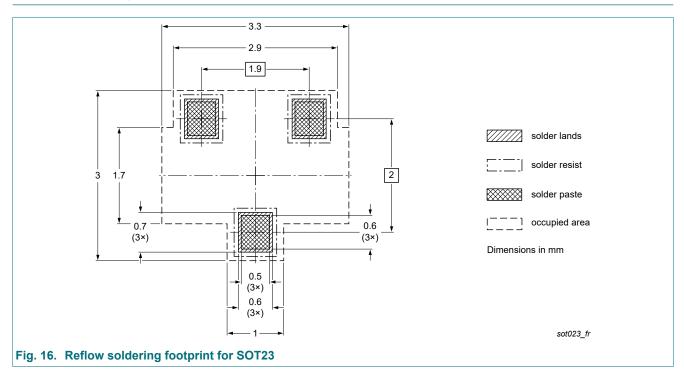
### **Quality information**

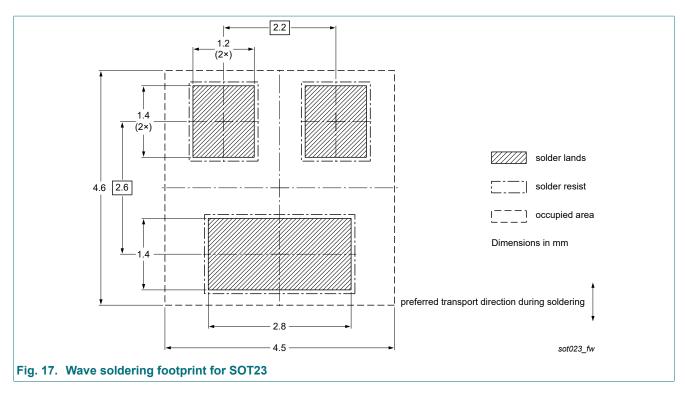
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



### 13. Soldering





# 14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PBSS4021PT-Q v.1	20230915	Product data sheet	-	-			

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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