

# PBSS305PZ-Q

80 V, 4.5 A PNP low V<sub>CEsat</sub> transistor

29 August 2024

Product data sheet

## 1. General description

PNP low V<sub>CEsat</sub> transistor in a SOT223 (SC-73) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS305NZ-Q

## 2. Features and benefits

- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High collector current gain (h<sub>FE</sub>) at high I<sub>C</sub>
- High efficiency due to less heat generation
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- High-voltage DC-to-DC conversion
- High-voltage MOSFET gate driving
- High-voltage motor control
- High-voltage power switches (e.g. motors, fans)
- Automotive applications

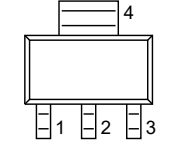
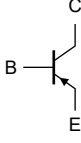
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-80	V
I <sub>C</sub>	collector current		-	-	-4.5	A
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	-9	A
R <sub>CEsat</sub>	collector-emitter saturation resistance	I <sub>C</sub> = -2 A; I <sub>B</sub> = -40 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	100	140	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-73 (SOT223)	 sym028
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">PBSS305PZ-Q</a>	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	<a href="#">SOT223</a>

7. Marking

Table 4. Marking codes

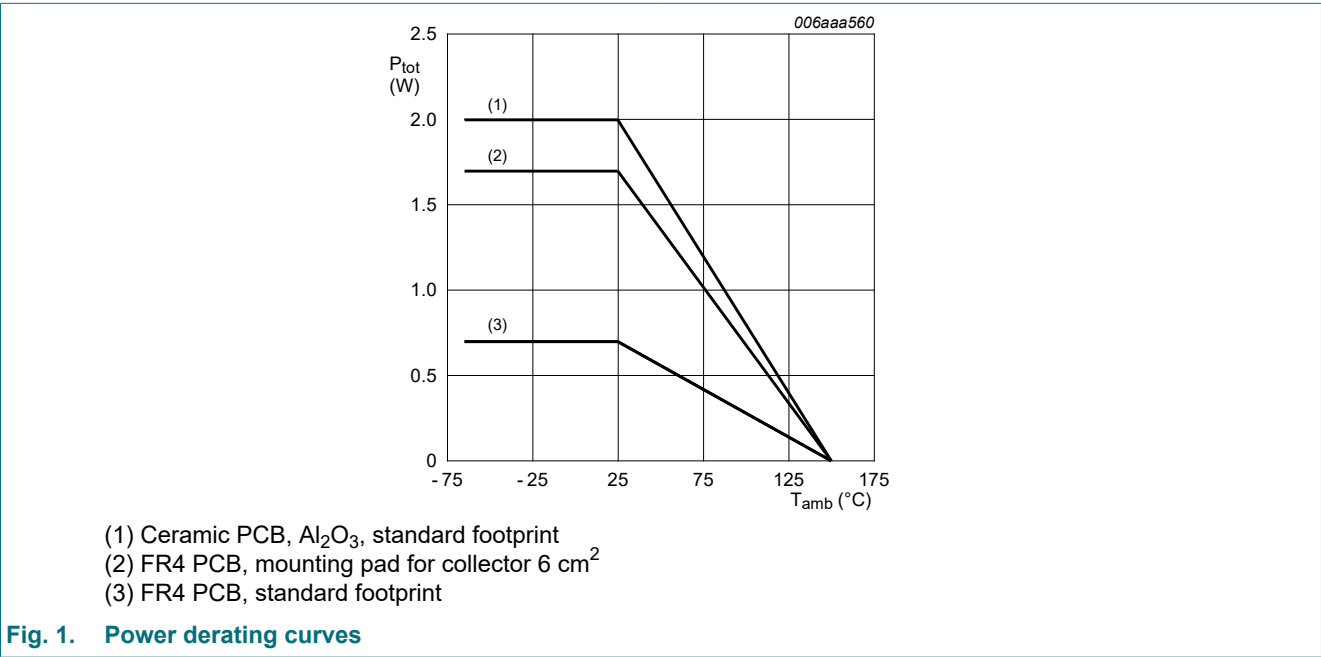
Type number	Marking code
PBSS305PZ-Q	S305PZ

8. Limiting values

Table 5. Limiting values  
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-4.5	A
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-9	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.7	W
			[2]	-	1.7	W
			[3]	-	2	W
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	179	K/W
			[2]	-	-	74	K/W
			[3]	-	-	63	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	15	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
[3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

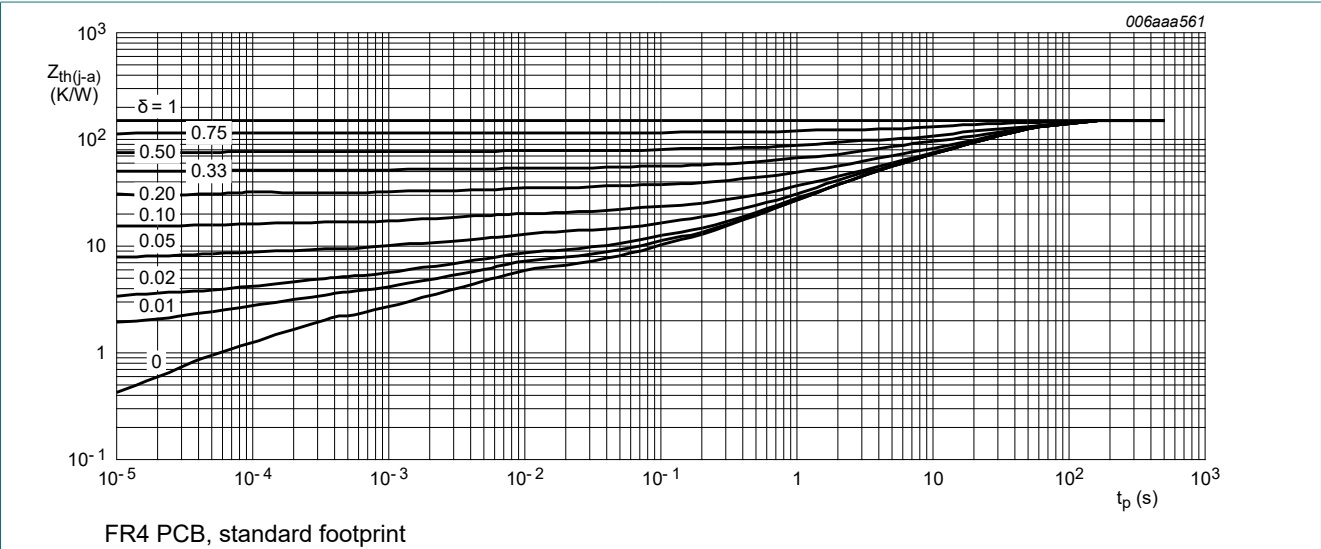


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

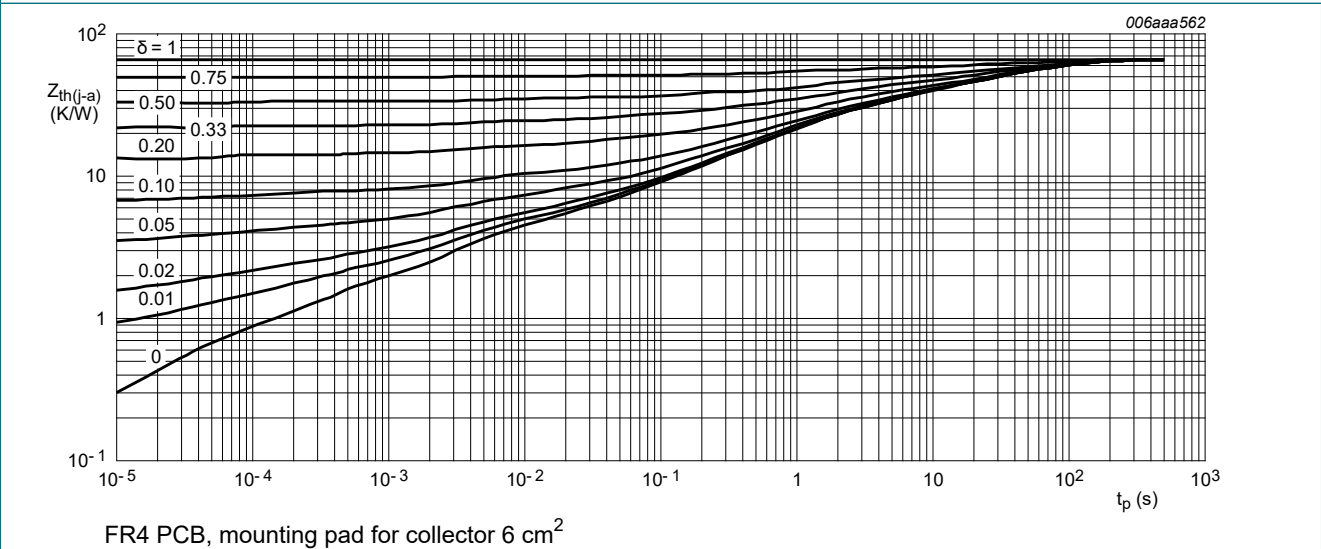
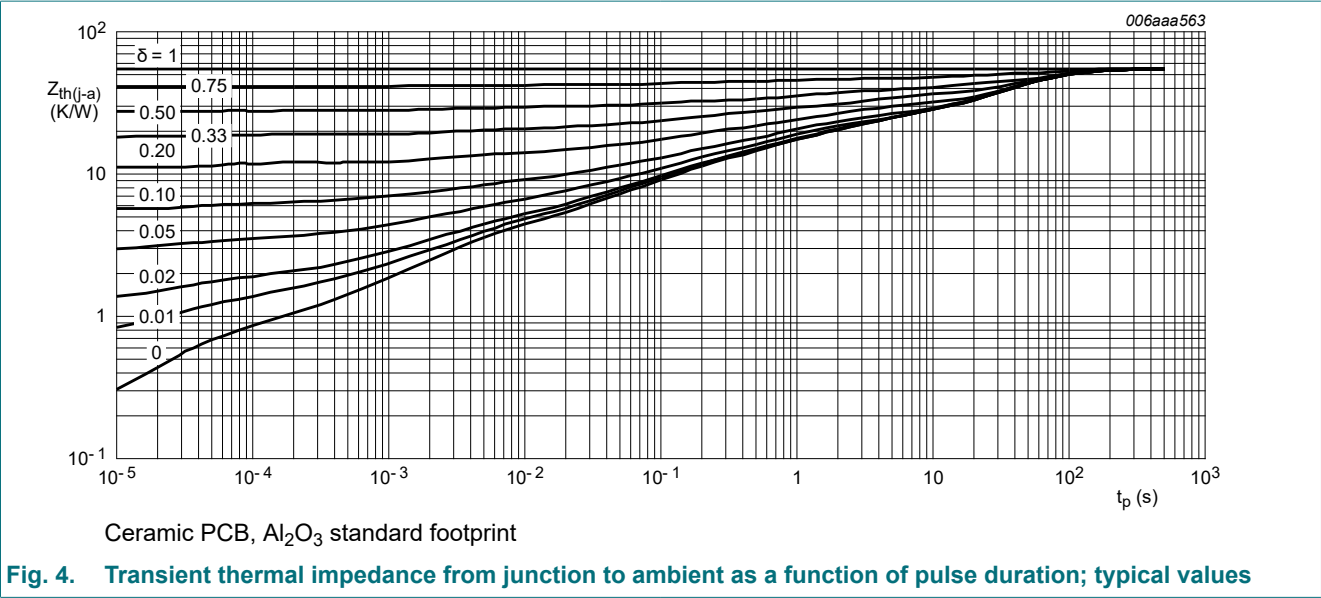


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

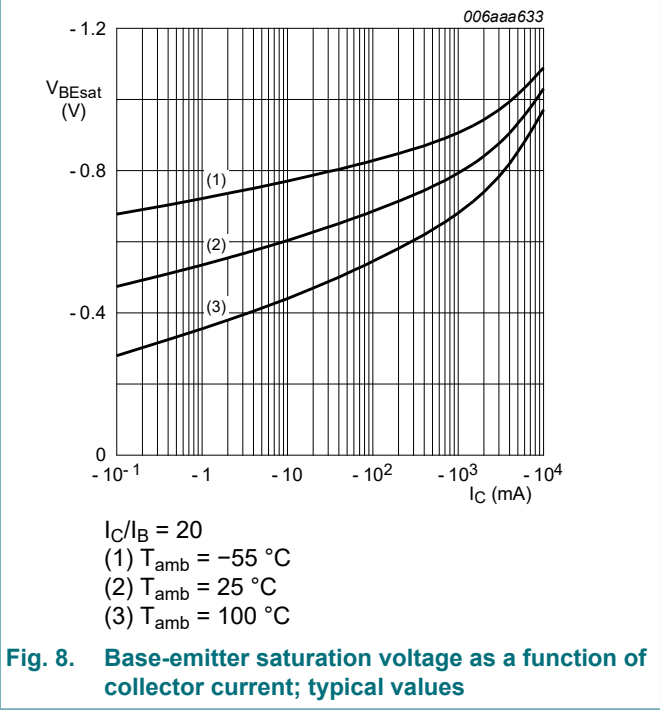
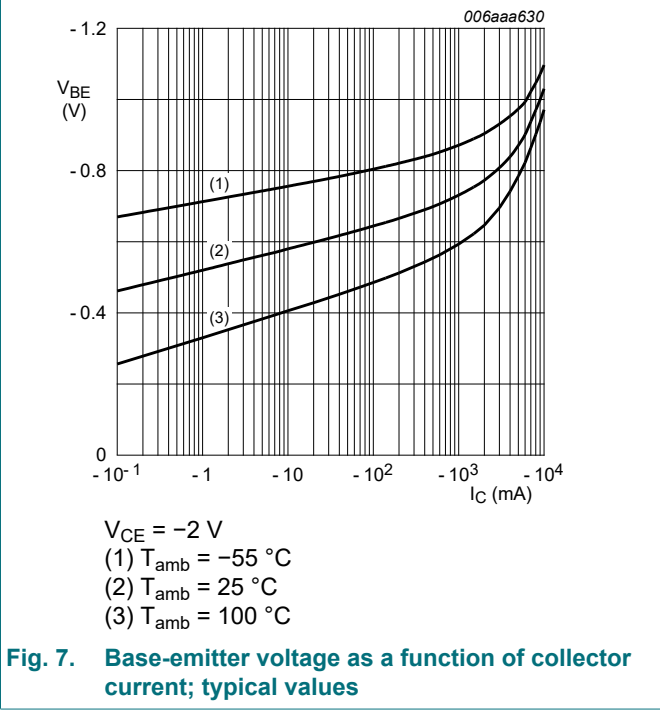
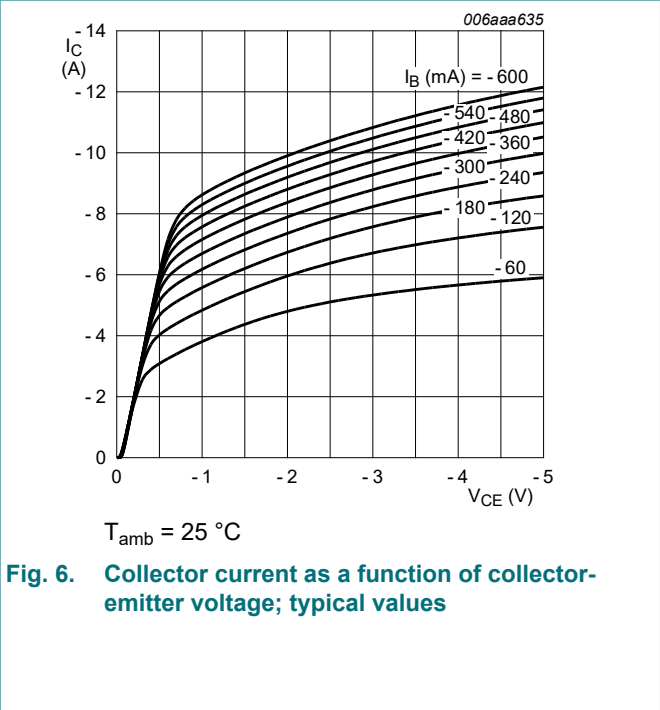
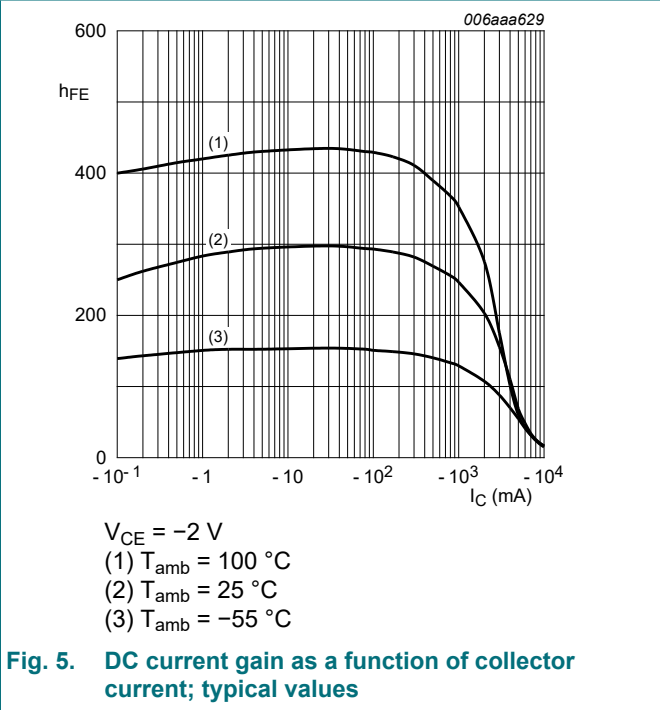


10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -80 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
		V <sub>CB</sub> = -80 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-50	µA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -0.5 A; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		200	280	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -1 A; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		150	240	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -2 A; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		120	190	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -4 A; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		60	100	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -6 A; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		30	45	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -0.5 A; I <sub>B</sub> = -50 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-36	-50	mV
		I <sub>C</sub> = -1 A; I <sub>B</sub> = -50 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-70	-100	mV
		I <sub>C</sub> = -1 A; I <sub>B</sub> = -10 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-180	-260	mV
		I <sub>C</sub> = -2 A; I <sub>B</sub> = -40 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-200	-280	mV
		I <sub>C</sub> = -4 A; I <sub>B</sub> = -200 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-245	-345	mV
		I <sub>C</sub> = -4 A; I <sub>B</sub> = -400 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-180	-245	mV
		I <sub>C</sub> = -4.5 A; I <sub>B</sub> = -225 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-310	-450	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	I <sub>C</sub> = -2 A; I <sub>B</sub> = -40 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	100	140	mΩ
		I <sub>C</sub> = -4 A; I <sub>B</sub> = -200 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	61	87	mΩ
		I <sub>C</sub> = -4 A; I <sub>B</sub> = -400 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	44	63	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = -1 A; I <sub>B</sub> = -100 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-0.81	-0.9	V
		I <sub>C</sub> = -4 A; I <sub>B</sub> = -400 mA; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-0.93	-1.05	V
V <sub>BEon</sub>	base-emitter turn-on voltage	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -2 A; pulsed; t <sub>p</sub> ≤ 300 µs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C		-	-0.78	-0.85	V
t <sub>d</sub>	delay time	V <sub>CC</sub> = -12.5 V; I <sub>C</sub> = -3 A; I <sub>Bon</sub> = -0.15 A; I <sub>Boff</sub> = 0.15 A; T <sub>amb</sub> = 25 °C		-	15	-	ns
t <sub>r</sub>	rise time			-	85	-	ns
t <sub>on</sub>	turn-on time			-	100	-	ns
t <sub>s</sub>	storage time			-	185	-	ns
t <sub>f</sub>	fall time			-	100	-	ns
t <sub>off</sub>	turn-off time			-	285	-	ns

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$f_T$	transition frequency	$V_{CE} = -10\text{ V}$ ; $I_C = -100\text{ mA}$ ; $f = 100\text{ MHz}$ ; $T_{amb} = 25\text{ }^{\circ}\text{C}$	-	100	-	MHz
$C_c$	collector capacitance	$V_{CB} = -10\text{ V}$ ; $I_E = 0\text{ A}$ ; $i_e = 0\text{ A}$ ; $f = 1\text{ MHz}$ ; $T_{amb} = 25\text{ }^{\circ}\text{C}$	-	65	90	pF



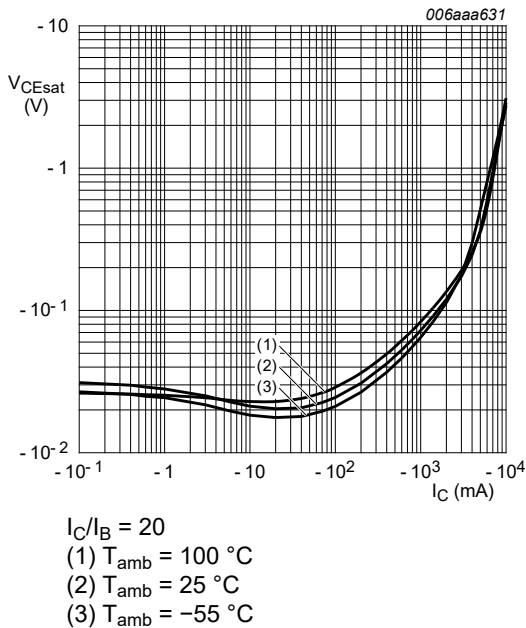


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

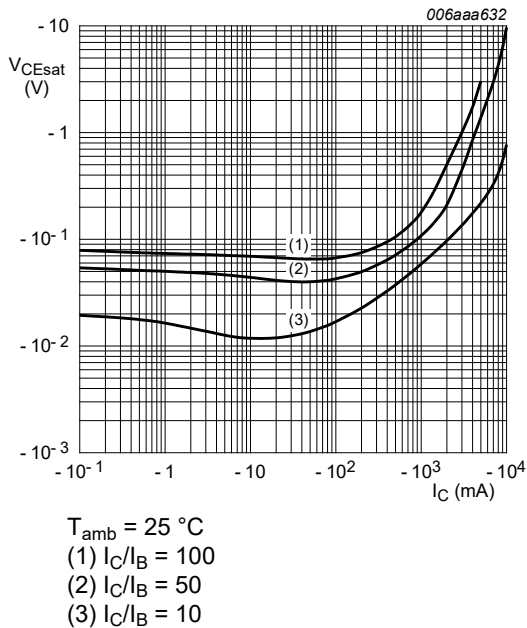


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

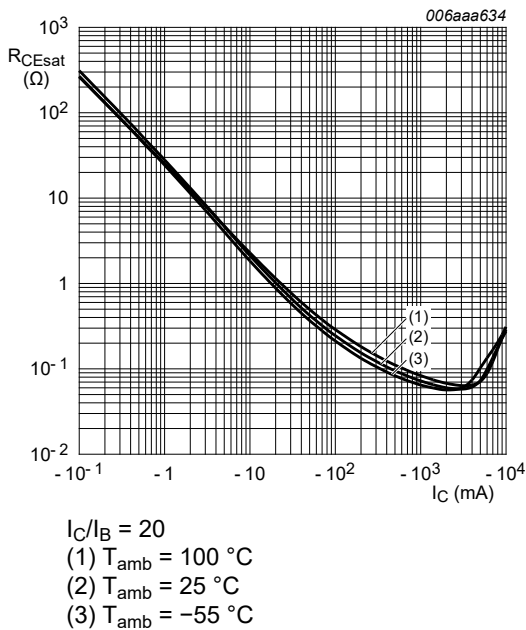


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

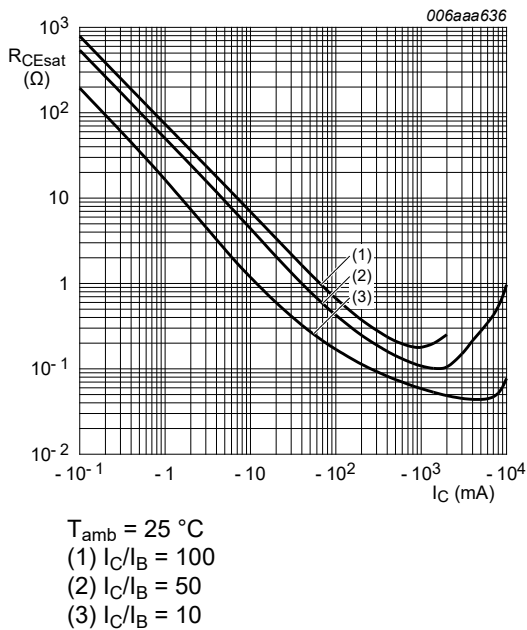


Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values



11. Test information

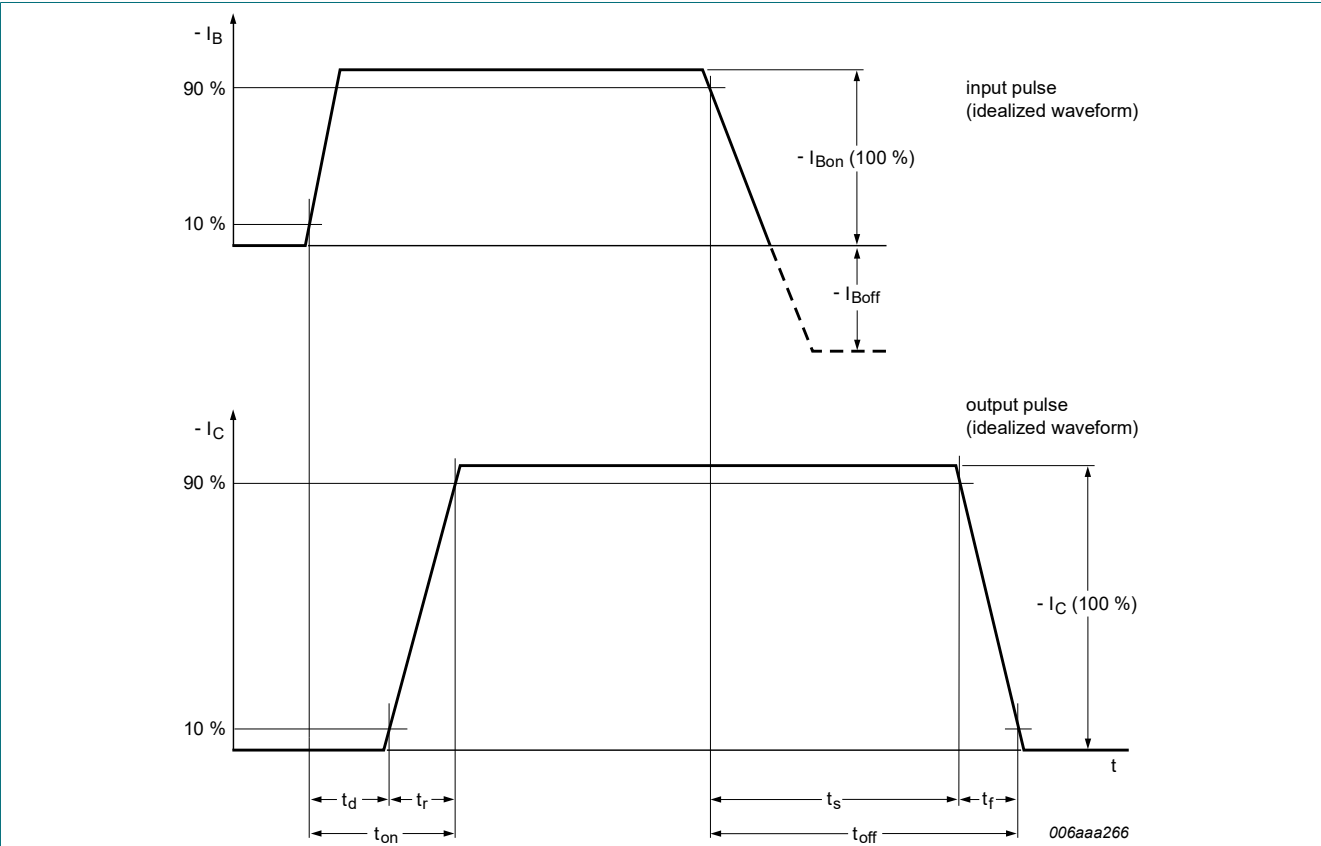


Fig. 13. Transistor switching time definition

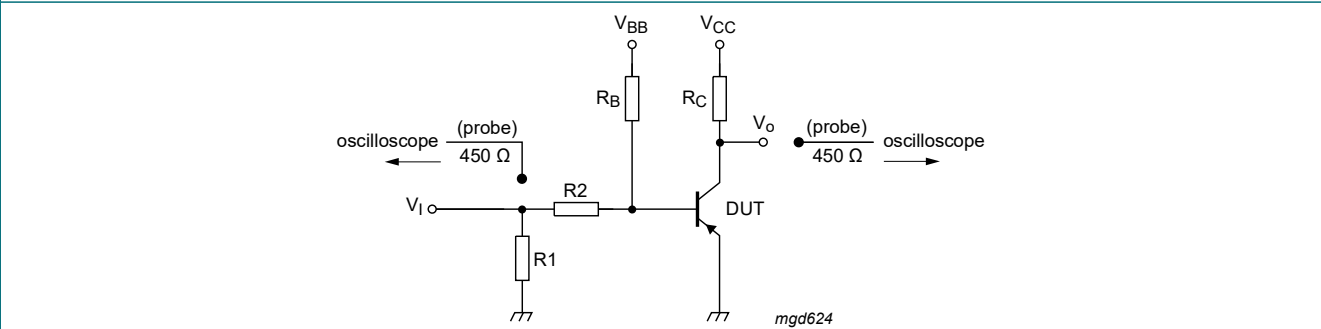
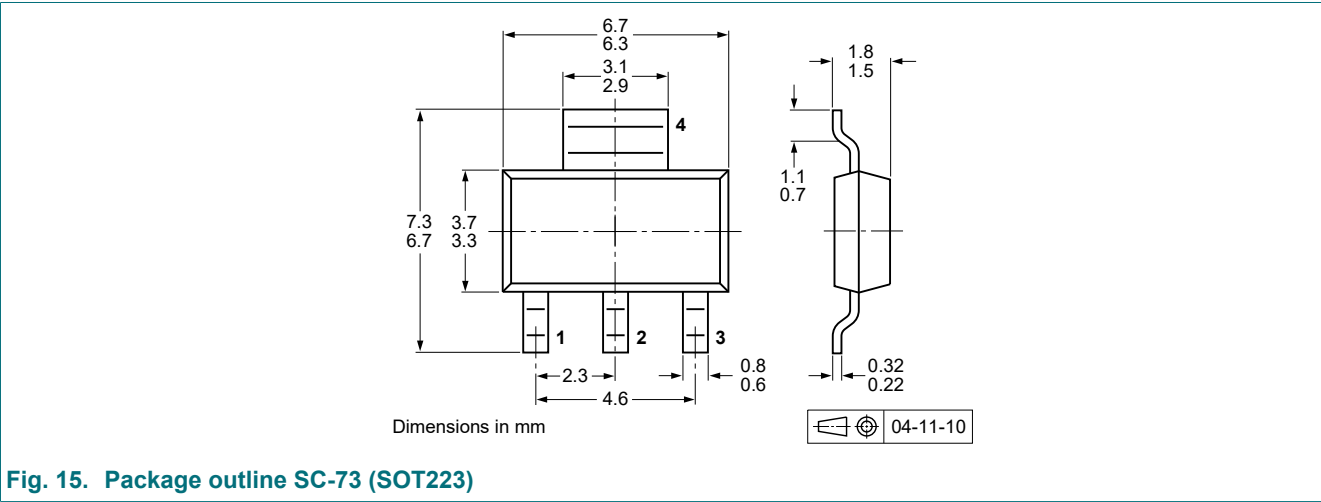


Fig. 14. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering

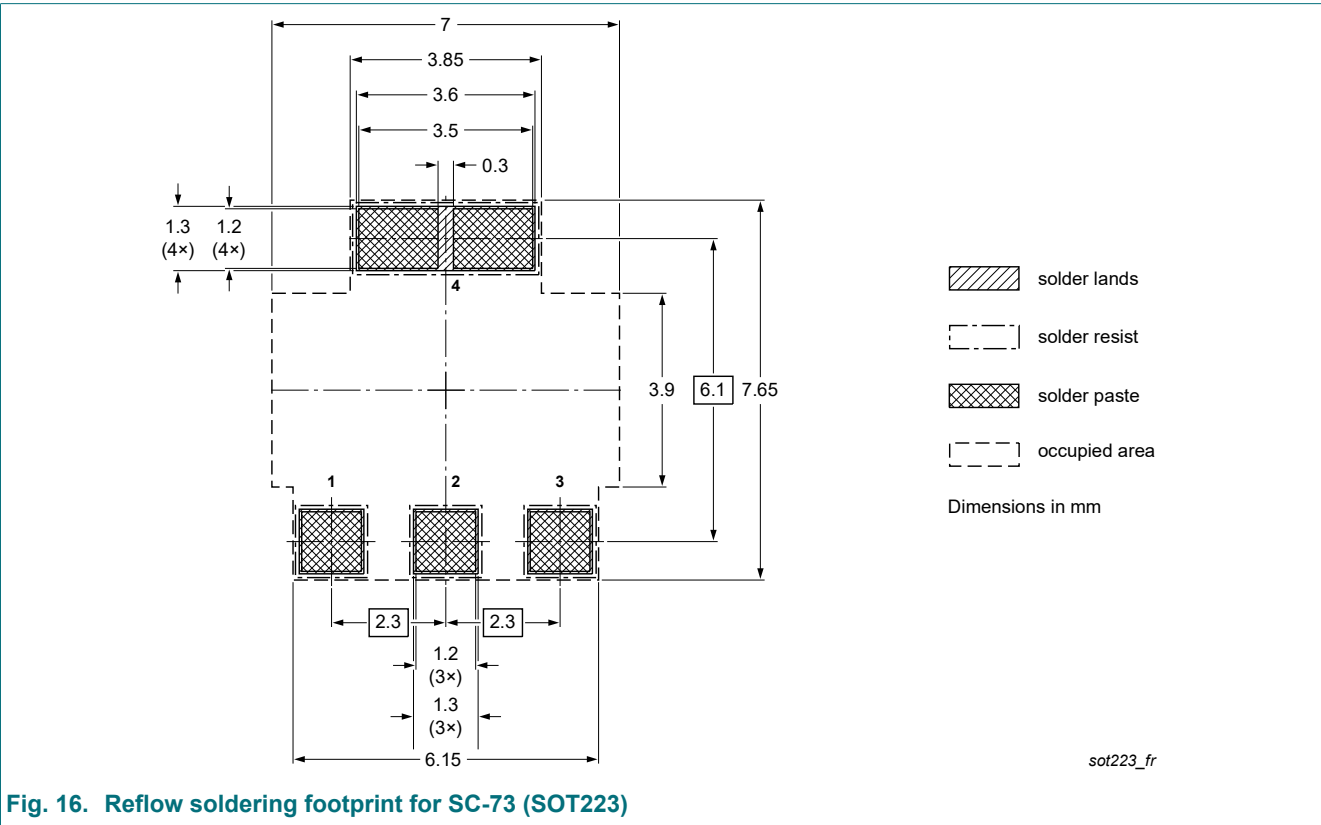
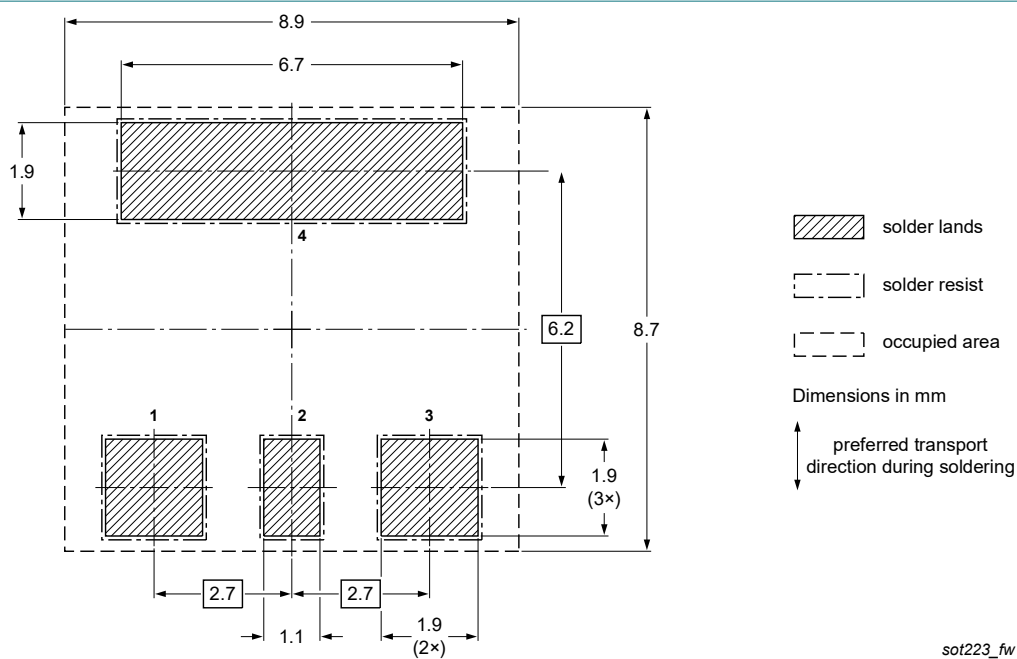


Fig. 16. Reflow soldering footprint for SC-73 (SOT223)



**Fig. 17. Wave soldering footprint for SC-73 (SOT223)**

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS305PZ-Q v.1	20240829	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 3

9. Thermal characteristics..... 4

10. Characteristics..... 6

11. Test information..... 9

12. Package outline..... 10

13. Soldering..... 10

14. Revision history.....12

15. Legal information.....13

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