

PBHV9115X

150 V, 1 A PNP high-voltage low V_{CEsat} transistor

9 October 2024

Product data sheet

1. General description

PNP high-voltage low V_{CEsat} transistor in a SOT89 (SC-62) small and flat Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C

3. Applications

- LED driver for LED chain module
- LCD backlighting
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CESM}	collector-emitter peak voltage	$V_{BE} = 0\text{ V}$	-	-	-200	V
V_{CEO}	collector-emitter voltage	open base	-	-	-150	V
I_C	collector current		-	-	-1	A
h_{FE}	DC current gain	$V_{CE} = -10\text{ V}$; $I_C = -50\text{ mA}$; $T_{amb} = 25\text{ °C}$	100	220	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym079
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBHV9115X	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBHV9115X	% 4 G

[1] % = placeholder for manufacturing site code

8. Limiting values

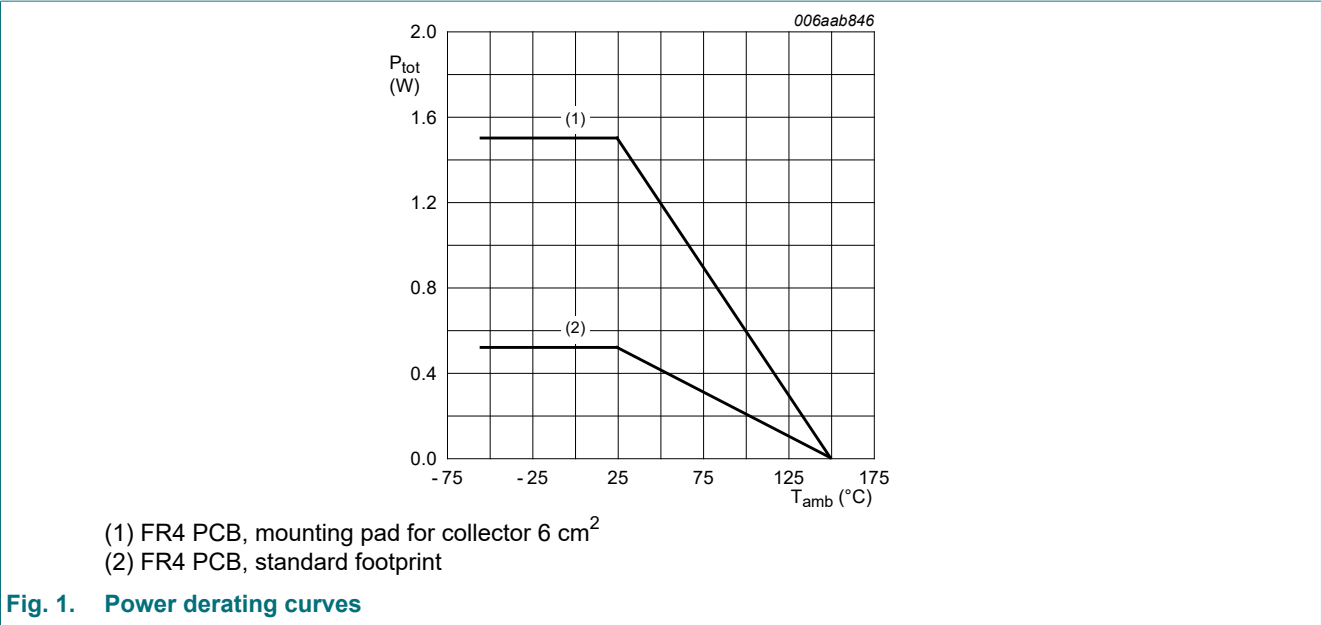
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-200	V
V _{CEO}	collector-emitter voltage	open base		-	-150	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-200	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-1	A
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-2	A
I _{BM}	peak base current			-	-400	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	520	mW
			[2]	-	1.5	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

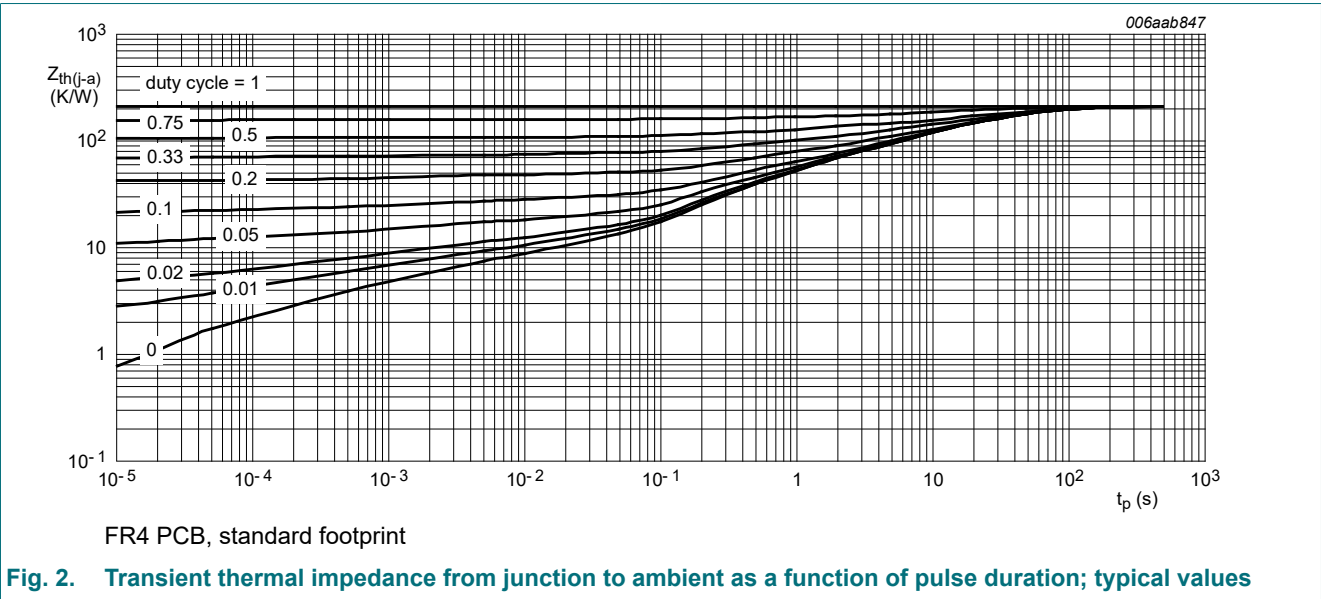


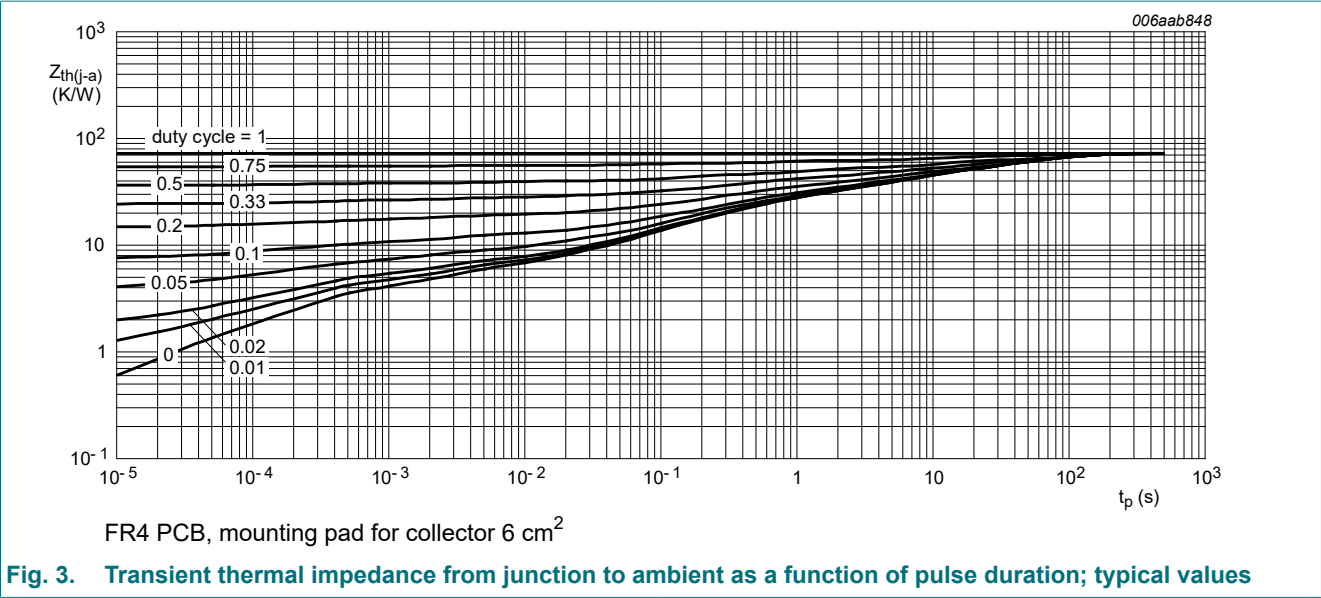
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	240	K/W
			[2]	-	-	80	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².





10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = -120\text{ V}$; $I_E = 0\text{ A}$; $T_{amb} = 25\text{ °C}$	-	-	-100	nA
		$V_{CB} = -120\text{ V}$; $I_E = 0\text{ A}$; $T_j = 150\text{ °C}$	-	-	-10	μA
I_{CES}	collector-emitter cut-off current	$V_{CE} = -120\text{ V}$; $V_{BE} = 0\text{ V}$; $T_{amb} = 25\text{ °C}$	-	-	-100	nA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -4\text{ V}$; $I_C = 0\text{ A}$; $T_{amb} = 25\text{ °C}$	-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -10\text{ V}$; $I_C = -50\text{ mA}$; $T_{amb} = 25\text{ °C}$	100	220	-	
		$V_{CE} = -10\text{ V}$; $I_C = -100\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	100	220	-	
		$V_{CE} = -10\text{ V}$; $I_C = -1\text{ A}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	10	30	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -100\text{ mA}$; $I_B = -10\text{ mA}$; $T_{amb} = 25\text{ °C}$	-	-60	-120	mV
		$I_C = -100\text{ mA}$; $I_B = -20\text{ mA}$; $T_{amb} = 25\text{ °C}$	-	-50	-100	mV
		$I_C = -500\text{ mA}$; $I_B = -50\text{ mA}$; $T_{amb} = 25\text{ °C}$	-	-200	-300	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = -1\text{ A}$; $I_B = -100\text{ mA}$; pulsed; $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$; $T_{amb} = 25\text{ °C}$	-	-1	-1.2	V
t_d	delay time	$V_{CC} = -6\text{ V}$; $I_C = -0.5\text{ A}$; $I_{Bon} = -0.1\text{ A}$; $I_{Boff} = 0.1\text{ A}$; $T_{amb} = 25\text{ °C}$	-	8	-	ns
t_r	rise time		-	282	-	ns
t_{on}	turn-on time		-	290	-	ns
t_s	storage time		-	430	-	ns
t_f	fall time		-	300	-	ns
t_{off}	turn-off time		-	730	-	ns
f_T	transition frequency	$V_{CE} = -10\text{ V}$; $I_C = -10\text{ mA}$; $f = 100\text{ MHz}$; $T_{amb} = 25\text{ °C}$	-	115	-	MHz
C_c	collector capacitance	$V_{CB} = -20\text{ V}$; $I_E = 0\text{ A}$; $i_e = 0\text{ A}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ °C}$	-	10	-	pF
C_e	emitter capacitance	$V_{EB} = -0.5\text{ V}$; $I_C = 0\text{ A}$; $i_c = 0\text{ A}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ °C}$	-	150	-	pF

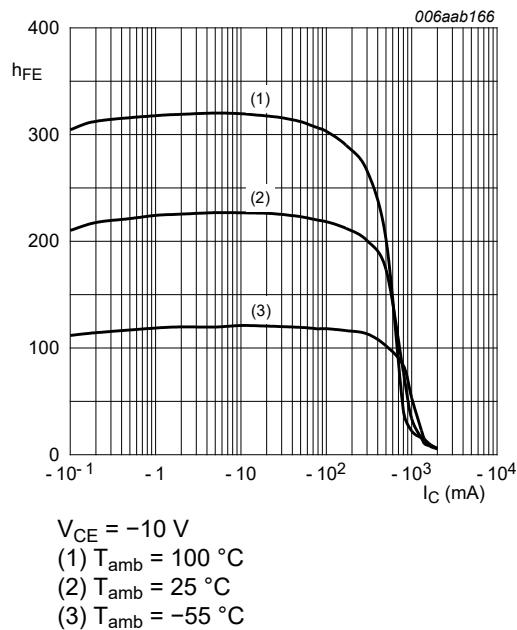


Fig. 4. DC current gain as a function of collector current; typical values

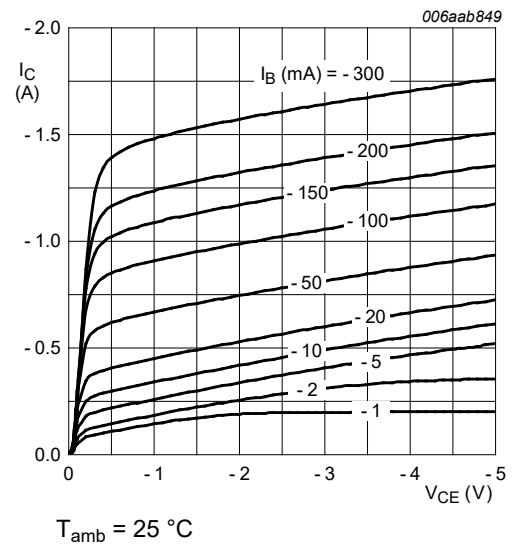


Fig. 5. Collector current as a function of collector-emitter voltage; typical values

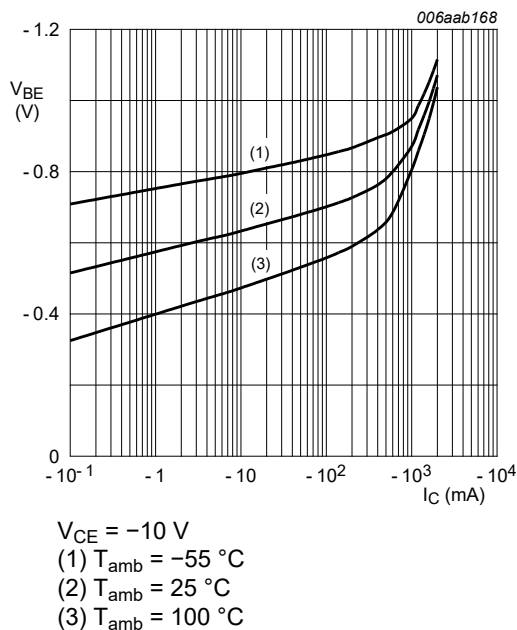


Fig. 6. Base-emitter voltage as a function of collector current; typical values

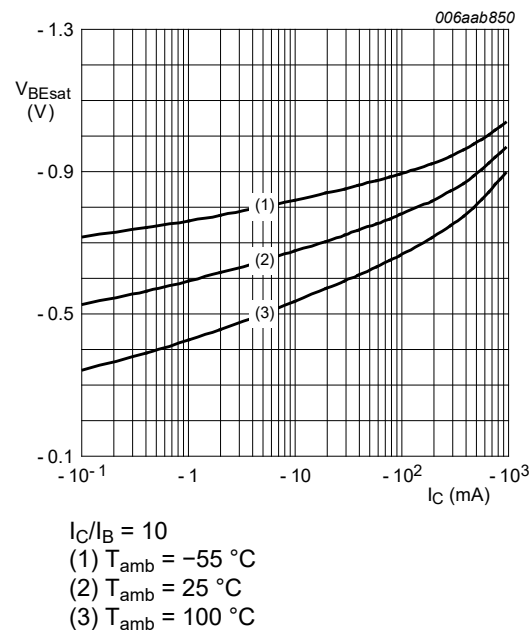


Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

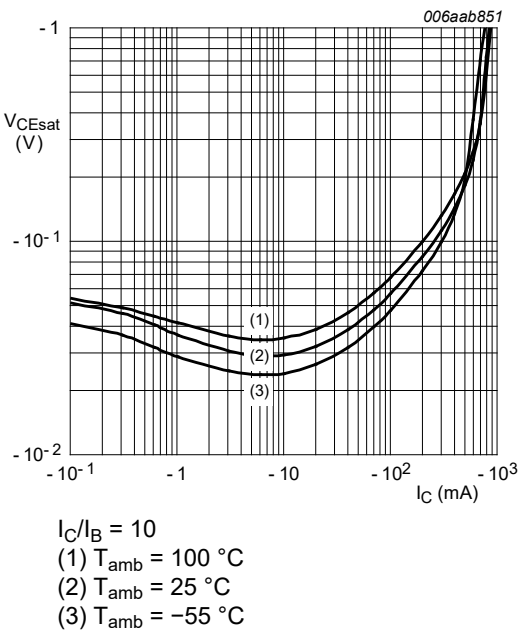


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

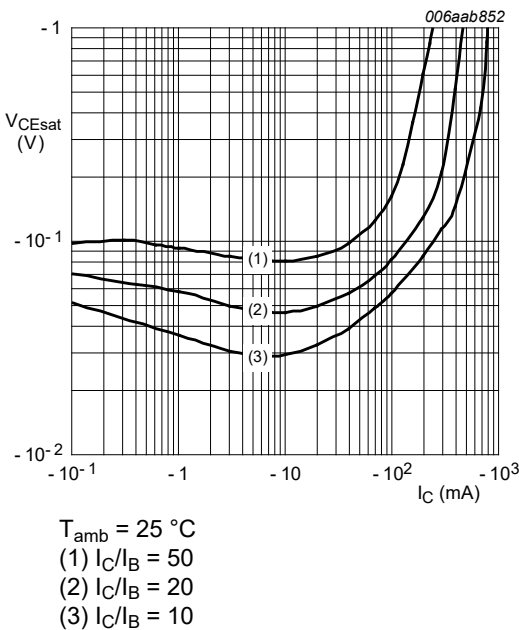


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

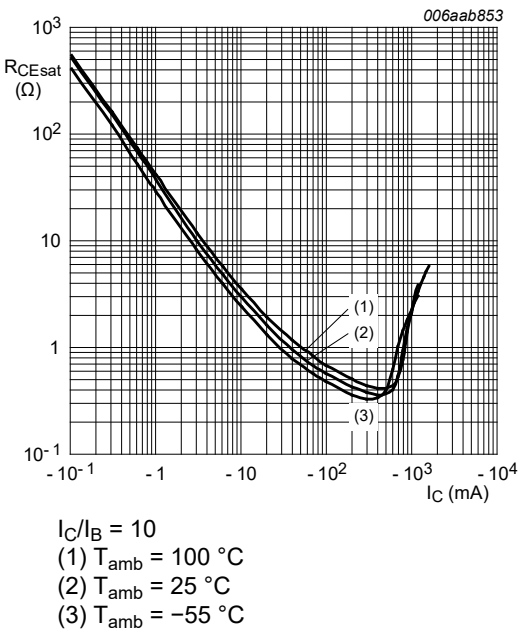


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

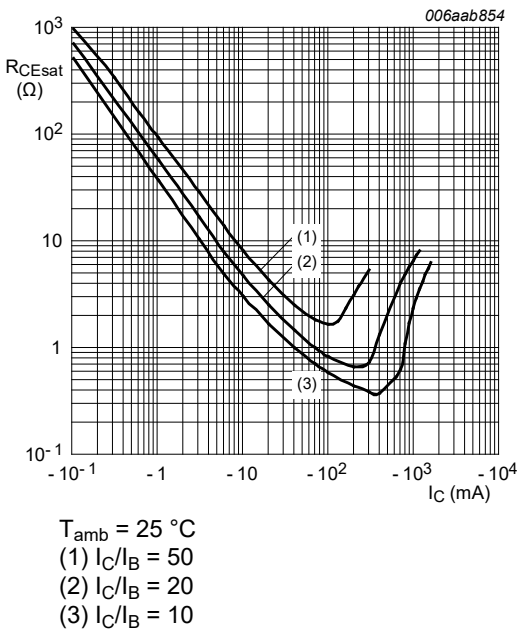


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

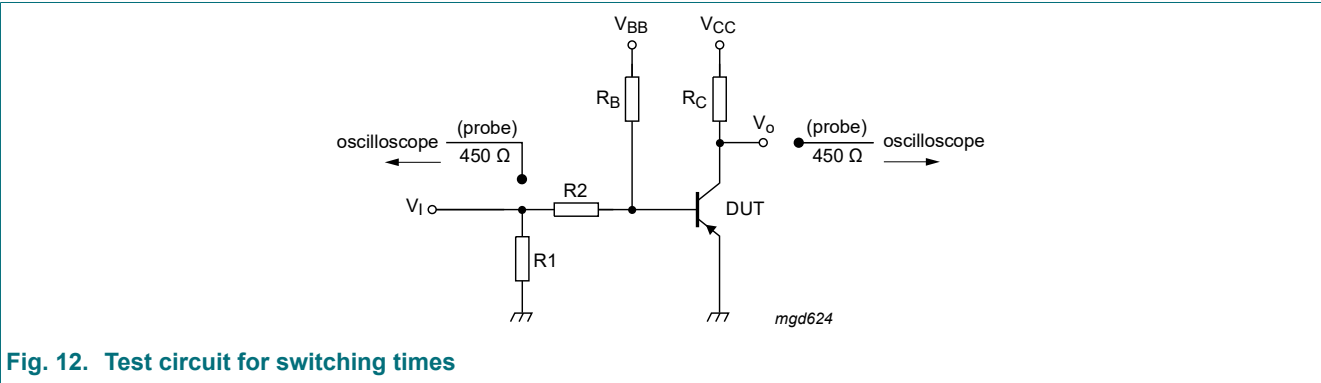


Fig. 12. Test circuit for switching times

12. Package outline

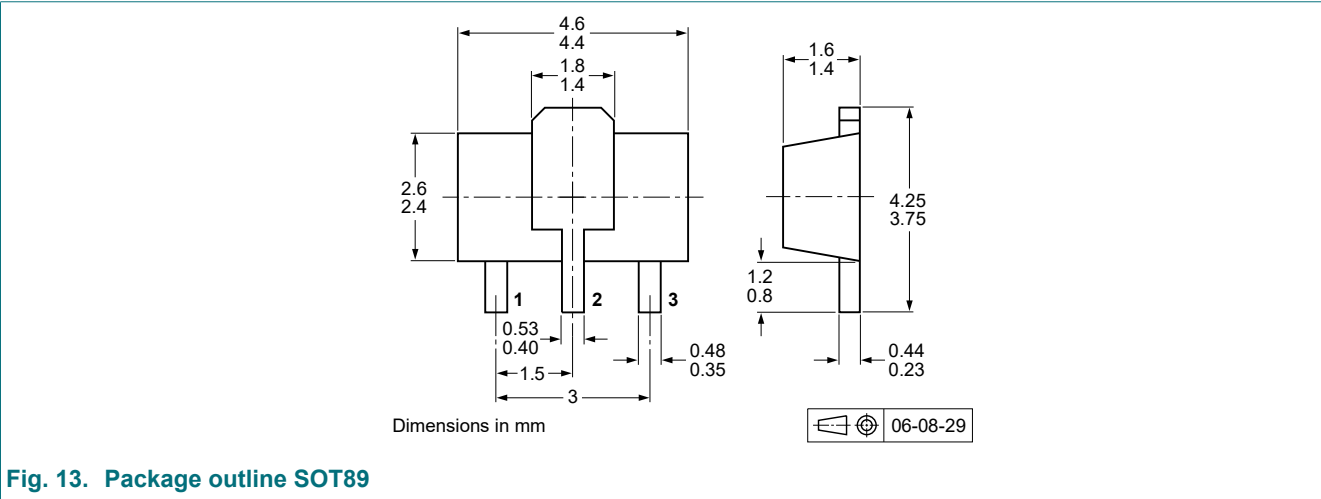


Fig. 13. Package outline SOT89

13. Soldering

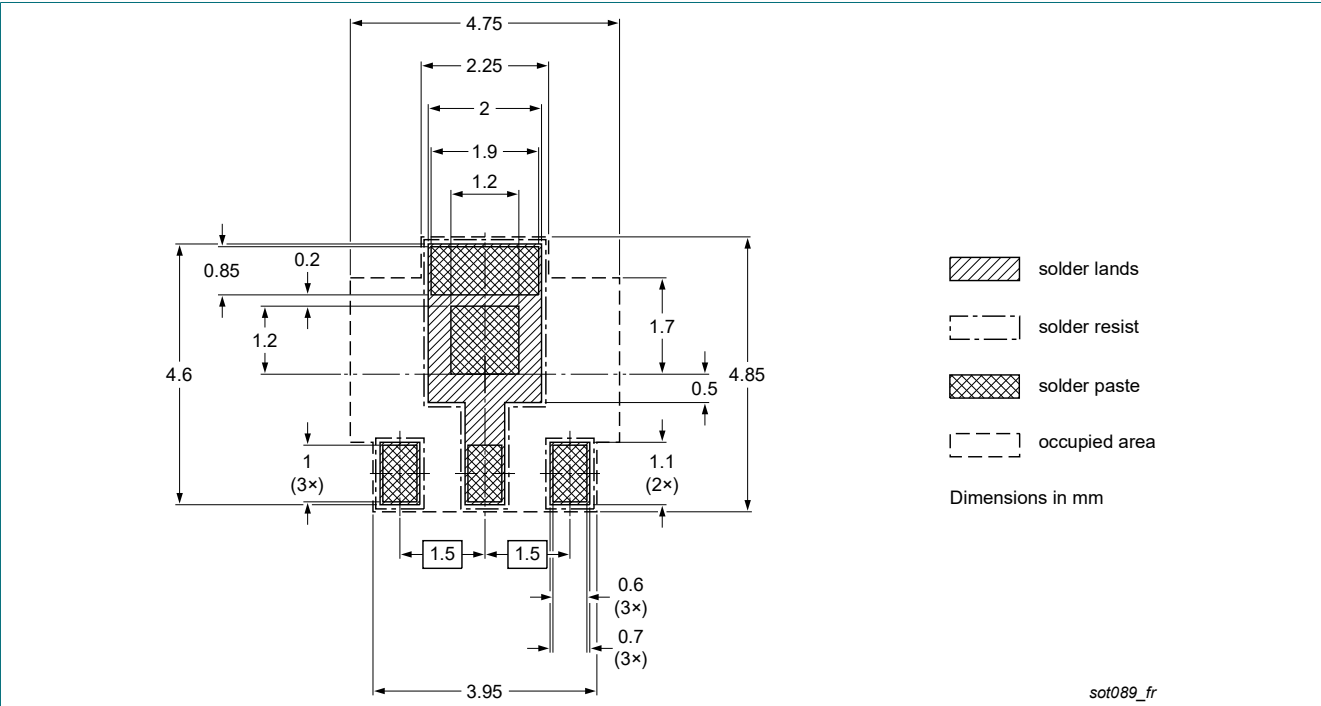


Fig. 14. Reflow soldering footprint for SOT89

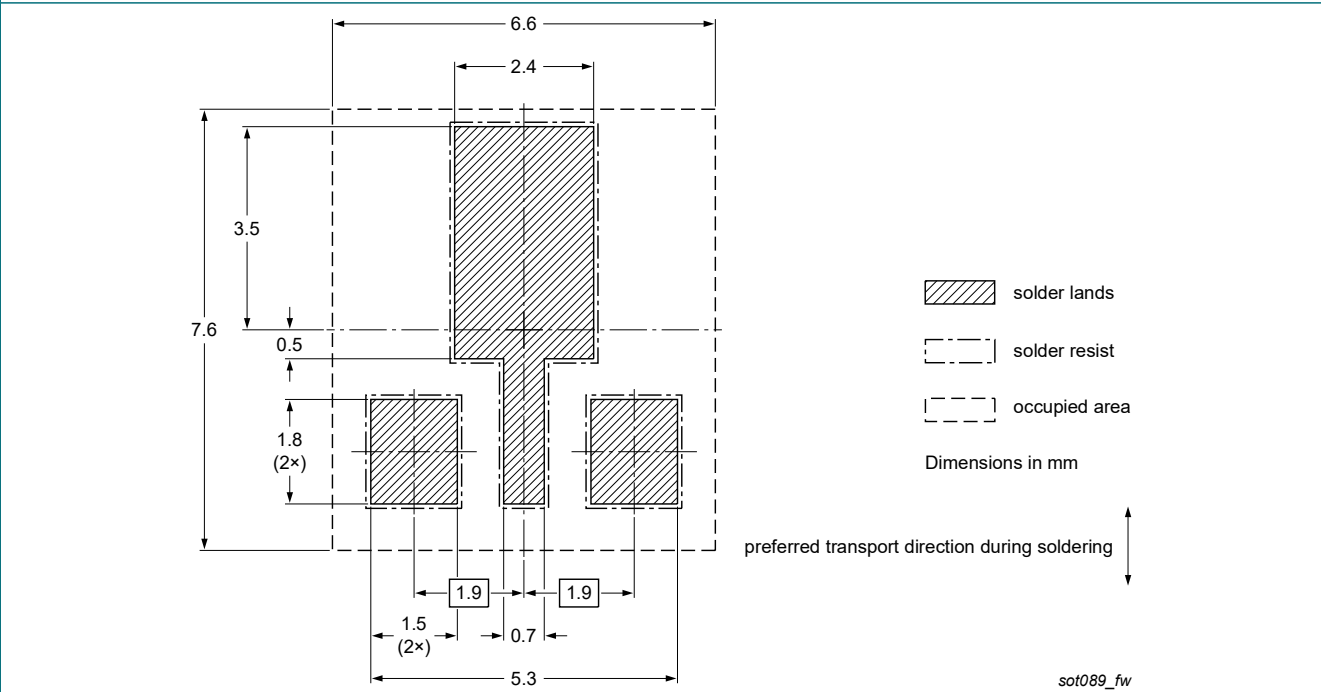


Fig. 15. Wave soldering footprint for SOT89

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBHV9115X v.3	20241009	Product data sheet	-	PBHV9115X v.2
Modifications:	<ul style="list-style-type: none">Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PBHV9115X v.2	20230717	Product data sheet	-	PBHV9115X_1
PBHV9115X_1	20100310	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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