

150 V, 1 A PNP high-voltage low VCEsat transistor

17 July 2023

Product data sheet

1. General description

PNP high-voltage low V_{CEsat} transistor in a SOT89 (SC-62) small and flat Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- LED driver for LED chain module
- LCD backlighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-	-200	V
V _{CEO}	collector-emitter voltage	open base		-	-	-150	V
I _C	collector current			-	-	-1	А
h _{FE}	DC current gain	V_{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C		100	220	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		С
2	С	collector		в-
3	В	base	3 2 1 SOT89	E sym079



6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PBHV9115X-Q	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	<u>SOT89</u>			

7. Marking

Table 4. Marking codes					
Type number	Marking code[1]				
PBHV9115X-Q	%4G				

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

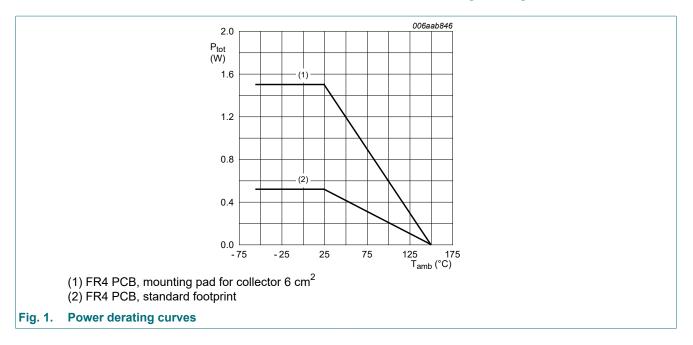
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-200	V
V _{CEO}	collector-emitter voltage	open base		-	-150	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-200	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-2	А
I _{BM}	peak base current			-	-400	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	520	mW
			[2]	-	1.5	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

150 V, 1 A PNP high-voltage low VCEsat transistor

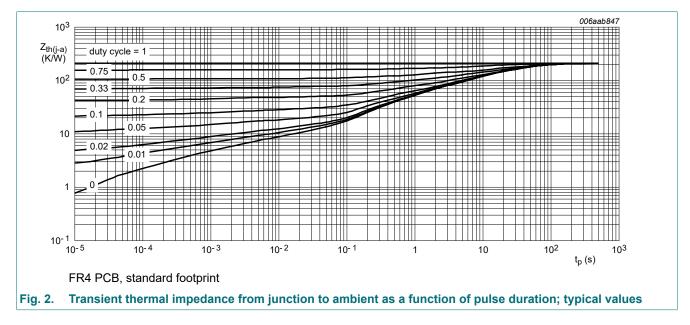


9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
ui(j-a)	thermal resistance from	in free air	[1]	-	-	240	K/W
	junction to ambient		[2]	-	-	80	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

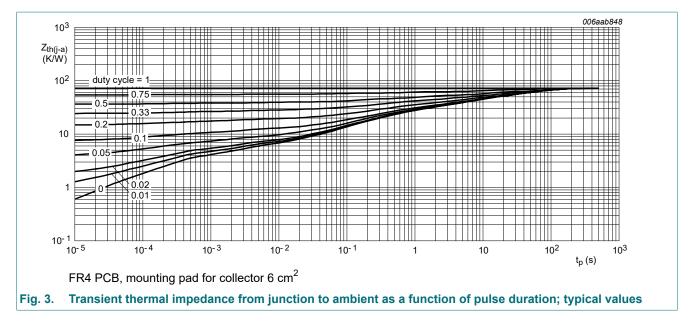
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



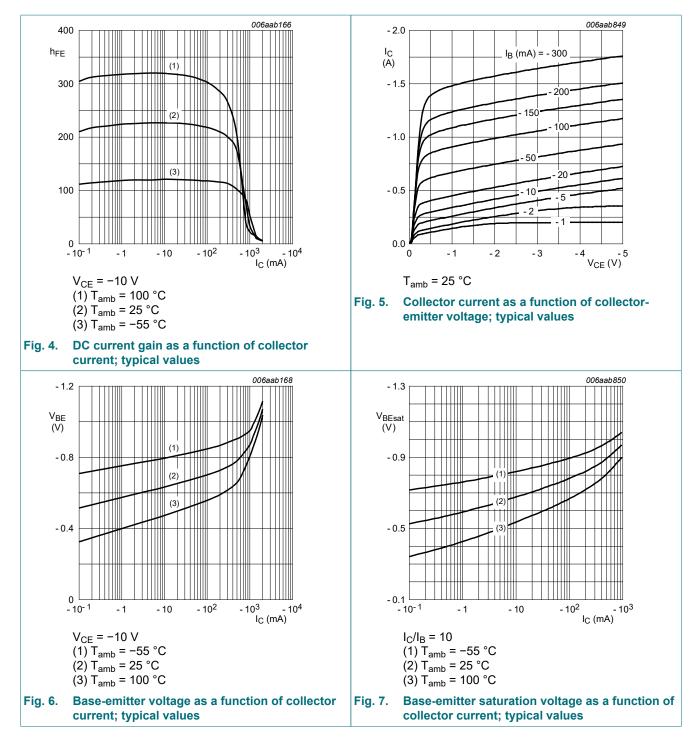
PBHV9115X-Q

150 V, 1 A PNP high-voltage low VCEsat transistor

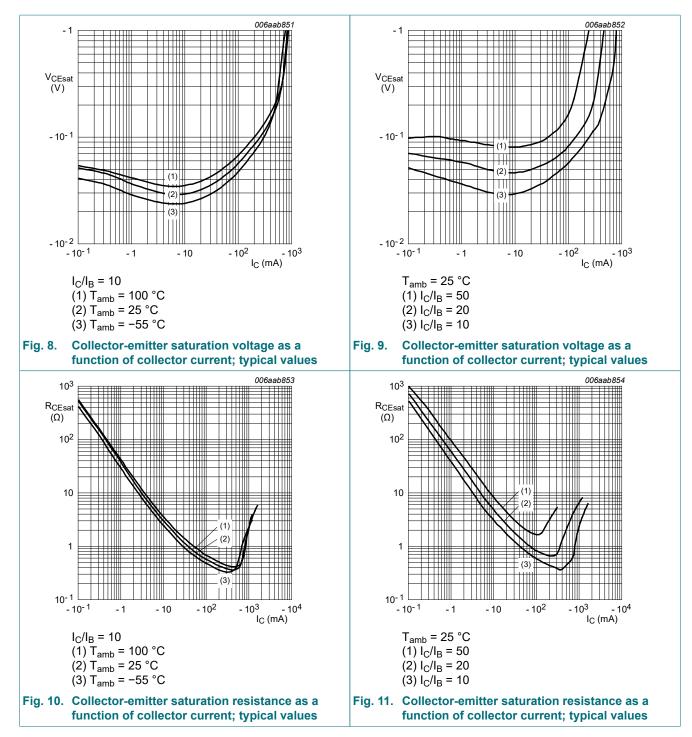


10. Characteristics

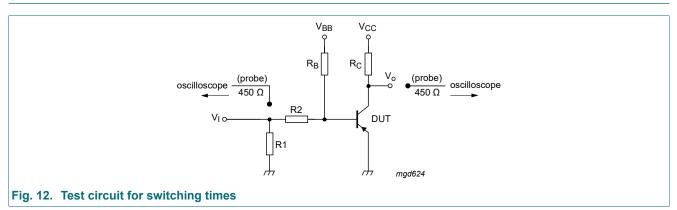
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -120 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
current		V _{CB} = -120 V; I _E = 0 A; T _j = 150 °C	-	-	-10	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -4 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$	-	-	-100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = -120 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C	100	220	-	
		V_{CE} = -10 V; I _C = -100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	100	220	-	
		V_{CE} = -10 V; I _C = -1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	10	30	-	
V _{CEsat}	collector-emitter	I_{C} = -100 mA; I_{B} = -10 mA; T_{amb} = 25 °C	-	-60	-120	mV
	saturation voltage	I _C = -100 mA; I _B = -20 mA; T _{amb} = 25 °C	-	-50	-100	mV
		I _C = -500 mA; I _B = -50 mA; T _{amb} = 25 °C	-	-200	-300	mV
V _{BEsat}	base-emitter saturation voltage	I_{C} = -1 A; I_{B} = -100 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-1	-1.2	V
t _d	delay time	V _{CC} = -6 V; I _C = -0.5 A; I _{Bon} = -0.1 A;	-	8	-	ns
t _r	rise time	I _{Boff} = 0.1 A; T _{amb} = 25 °C	-	282	-	ns
t _{on}	turn-on time		-	290	-	ns
t _s	storage time	-	-	430	-	ns
t _f	fall time		-	300	-	ns
t _{off}	turn-off time	-	-	730	-	ns
f _T	transition frequency	V _{CE} = -10 V; I _C = -10 mA; f = 100 MHz; T _{amb} = 25 °C	-	115	-	MHz
C _c	collector capacitance	V _{CB} = -20 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	10	-	pF
C _e	emitter capacitance	V _{EB} = -0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	150	-	pF



150 V, 1 A PNP high-voltage low VCEsat transistor



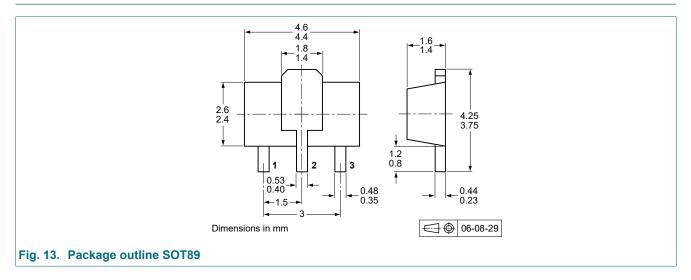
11. Test information



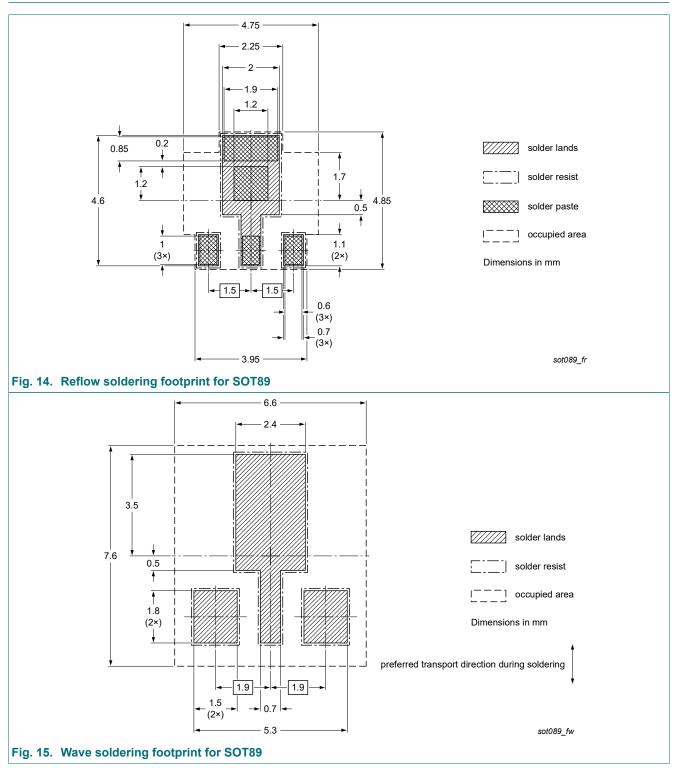
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PBHV9115X-Q v.1	20230717	Product data sheet	-	-		

PBHV9115X-Q

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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PBHV9115X-Q

150 V, 1 A PNP high-voltage low VCEsat transistor

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Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	1
6. Ordering information	2
7. Marking	
8. Limiting values	2
9. Thermal characteristics	
10. Characteristics	4
11. Test information	
12. Package outline	7
13. Soldering	
14. Revision history	
15. Legal information	
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PBHV9115X-Q

11 / 11