

PBHV9115TLH

150 V, 1 A PNP high-voltage low VCEsat transistor

17 July 2023

Product data sheet

1. General description

PNP high-voltage low $V_{\mbox{CEsat}}$ transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

NPN complement: PBHV8115TLH

2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- Small SMD plastic package
- AEC-Q101 qualified

3. Applications

- Power management
- LCD backlighting
- LED driver for LED chain module
- Switch Mode Power Supply (SMPS)

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-150	V
I _C	collector current			-	-	-1	А
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$		-	-	-2	А
h _{FE}	DC current gain	V_{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C		70	-	300	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	E	emitter		С
3	С	collector		в
				 E sym132
			SOT23	Sy11132

nexperia

6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PBHV9115TLH		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>			

7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PBHV9115TLH	FC%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

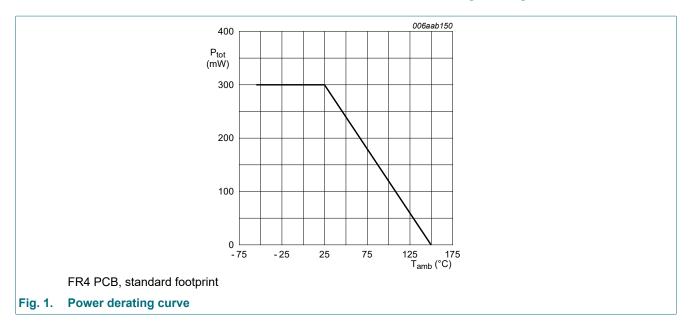
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-200	V
V _{CEO}	collector-emitter voltage	open base		-	-150	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-200	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-2	А
I _{BM}	peak base current			-	-400	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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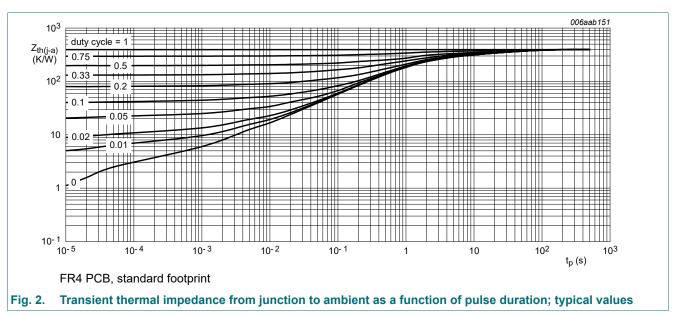


9. Thermal characteristics

Table 6. Thermal characteristics

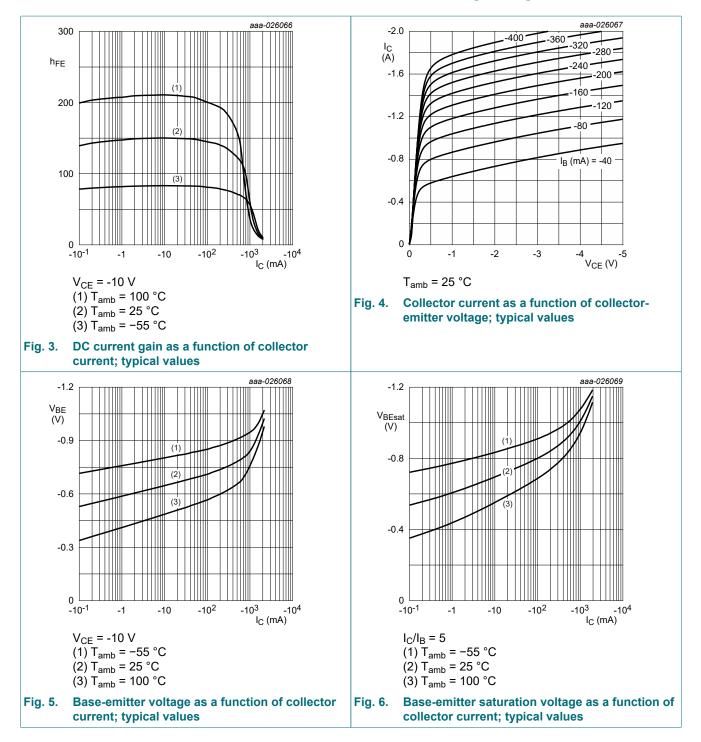
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	70	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



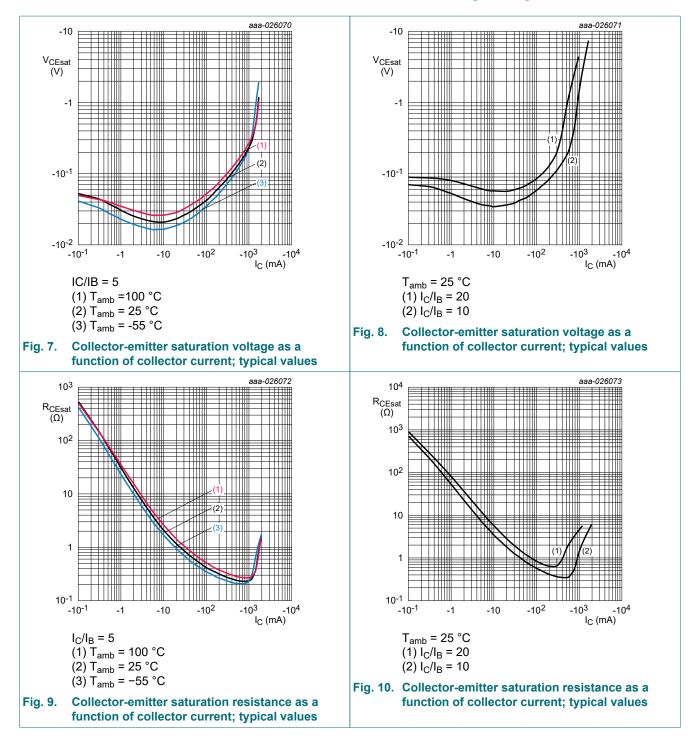
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -120 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CB} = -120 V; I _E = 0 A; T _j = 150 °C	-	-	-10	μA
ЕВО	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
CES	collector-emitter cut-off current	V _{CE} = -120 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C	70	-	300	
		V _{CE} = -10 V; I _C = -100 mA; T _{amb} = 25 °C	60	-	300	
		V_{CE} = -10 V; I _C = -500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	50	-	300	
		V_{CE} = -10 V; I _C = -1 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	10	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -100 mA; I _B = -10 mA; T _{amb} = 25 °C	-	-	-120	mV
		I _C = -100 mA; I _B = -20 mA; T _{amb} = 25 °C	-	-	-100	mV
		I_{C} = -500 mA; I_{B} = -100 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	-300	mV
V _{BEsat}	base-emitter saturation voltage	I_{C} = -1 A; I_{B} = -200 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	-1.2	V
t _d	delay time	V _{CC} = -6 V; I _C = -0.5 A; I _{Bon} = -0.1 mA;	-	10	-	ns
r	rise time	I _{Boff} = 0.1 mA; T _{amb} = 25 °C	-	285	-	ns
on	turn-on time	-	-	295	-	ns
s	storage time	-	-	430	-	ns
f	fall time	-	-	300	-	ns
t _{off}	turn-off time	-	-	730	-	ns
T	transition frequency	V_{CE} = -10 V; I _C = -10 mA; f = 100 MHz; T _{amb} = 25 °C	-	55	-	MHz
C _c	collector capacitance	V _{CB} = -20 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	10	-	pF
C _e	emitter capacitance	V _{EB} = -0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	150	-	pF

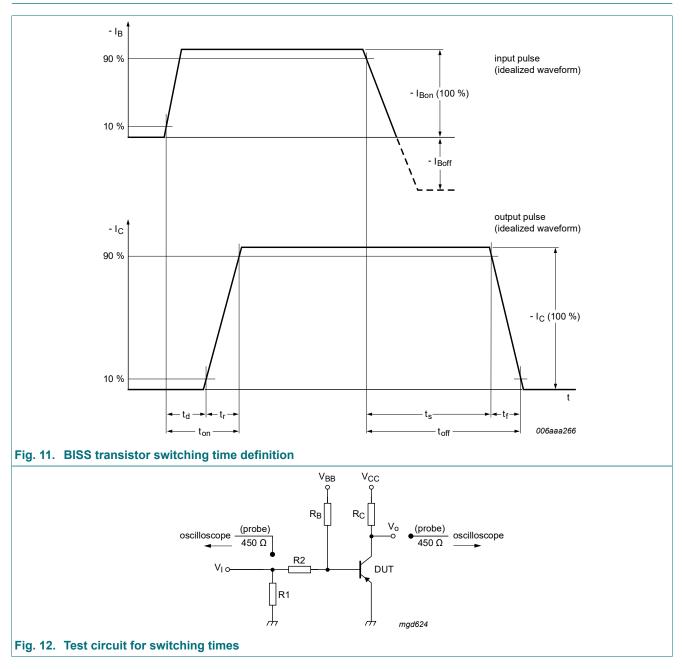


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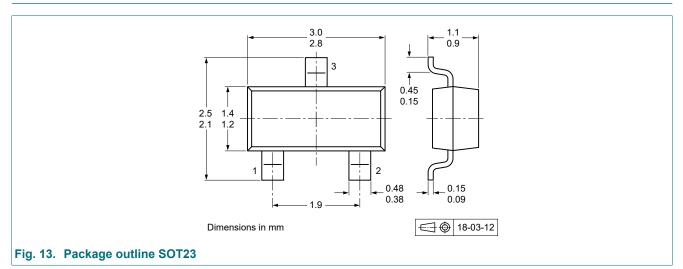
11. Test information



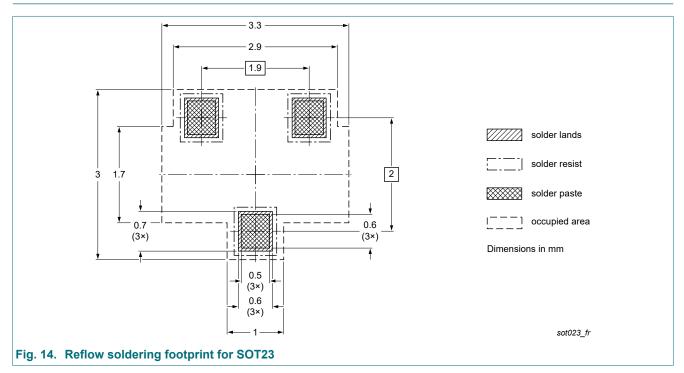
Quality information

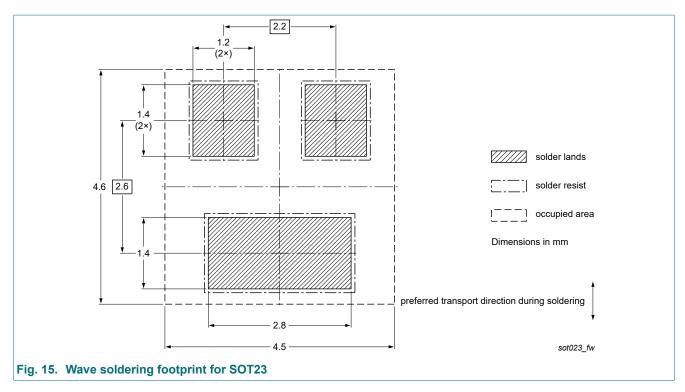
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





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14. Revision history

Table 8. Revision history								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PBHV9115TLH v.2	20230717	Product data sheet	-	PBHV9115TLH v.1				
Modifications:	Nexperia.	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
PBHV9115TLH v.1	20170116	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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