14 May 2024

Product data sheet

1. General description

The NSF060120D7A0 is a Silicon Carbide based 1200 V power MOSFET in a well-established 7-pin TO-263 plastic package for surface mounting PCB technology. The excellent R_{DSon} temperature stability combined with its fast switching speed makes it a product of choice in high power and high voltage industrial applications like E-vehicle charging infrastructure, photovoltaic inverters and motor drives.

2. Features and benefits

- Excellent R_{DSon} temperature stability
- Very low switching losses
- Fast reverse recovery
- · Fast switching speed
- Temperature independent turn-off switching losses
- · Very fast and robust intrinsic body diode
- · Faster commutation and improved switching due to the additional Kelvin source pin

3. Applications

- E-vehicle charging infrastructure
- Photovoltaic inverters
- Switch mode power supply
- Uninterruptable power supply
- Motor drives

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage			-	-	1200	V
V _{GS}	gate-source voltage		[1]	-10	-	22	V
I _D	drain current	T _c = 25 °C	[2]	-	-	38	Α
		T _c = 100 °C	[2]	-	-	27	А
I _{DM}	peak drain current	pulsed; t _p limited by T _j (max)	[3]	-	-	80	Α
Static characte	ristics			•	•		
R _{DSon}	drain-source on-state resistance	$V_{GS} = 18 \text{ V}; I_D = 20 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	60	90	mΩ

- [1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with V_{GSon} < 13 V.
- [2] Limited by the maximum values of T_j , $R_{th(j-c)}$ and $R_{DSon}(T_j)$.
- [3] Designed value (not tested).



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	
2	KS	kelvin source		
3 to 7	S	source		D L
mb	D	mounting base; connected to drain	TO-263-7 (SOT8070-1)	G KS S aaa-036675

6. Ordering information

Table 3. Ordering information

Type number	Package	Package						
	Name	Description	Version					
NSF060120D7A0	TO-263-7	plastic single-ended surface-mounted package; 7 leads	SOT8070-1					

7. Marking

Table 4. Marking codes

Type number	Marking code
NSF060120D7A0	60120D7A0

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage			-	1200	V
V _{GS}	gate-source voltage		[1]	-10	22	V
I _D	drain current	T _c = 25 °C	[2]	-	38	Α
		T _c = 100 °C	[2]	-	27	Α
I _{DM}	peak drain current	pulsed; t _p limited by T _j (max)	[3]	-	80	Α
P _{tot}	total power dissipation	T _c = 25 °C	[2]	-	167	W
T _j	junction temperature			-55	175	°C
T _{stg}	storage temperature			-55	150	°C
T _{sld(M)}	peak soldering temperature			-	260	°C
Source-drai	n diode			1		
I _S	source current	T _c = 25 °C	[2]	-	29	Α
I _{SM}	peak source current	pulsed; limited by T _j (max)	[3]	-	60	Α

^[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with V_{GSon} < 13 V.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-c)}	thermal resistance from junction to case		-	0.68	0.9	K/W

^[2] Limited by the maximum values of T_j , $R_{th(j-c)}$ and $R_{DSon}(T_j)$.

^[3] Designed value (not tested).

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	acteristics						
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = 1 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		1200	-	-	V
V _{GS(th)}	gate-source threshold	$I_D = 2 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	[1]	1.7	2.3	2.9	V
	voltage	I_D = 10 mA; V_{DS} = V_{GS} ; T_j = 25 °C	[1]	-	2.77	-	V
I _{DSS}	drain leakage current	V _{DS} = 1200 V; V _{GS} = 0 V; T _j = 25 °C		-	-	100	μΑ
I _{GSS}	gate leakage current	V _{GS} = 22 V; V _{DS} = 0 V; T _j = 25 °C		-	-	100	nA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C		-	-	100	nA
R_{DSon}	drain-source on-state	V _{GS} = 15 V; I _D = 20 A; T _j = 25 °C		-	80	-	mΩ
	resistance	V _{GS} = 15 V; I _D = 20 A; T _j = 175 °C		-	110	-	mΩ
		V _{GS} = 18 V; I _D = 20 A; T _j = 25 °C		-	60	90	mΩ
		V _{GS} = 18 V; I _D = 20 A; T _j = 175 °C		-	98	-	mΩ
9 _{fs}	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 20 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	9	-	S
R _{G(int)}	internal gate resistance	f = 0.5 MHz; T _j = 25 °C		-	2	-	Ω
Dynamic ch	naracteristics			'			
Q _{G(tot)}	total gate charge	V _{DD} = 800 V; I _D = 20 A; V _{GS} = -5/+18 V; T _j = 25 °C		-	57	-	nC
Q _{GS}	gate-source charge			-	25	-	nC
Q_{GD}	gate-drain charge			-	15	-	nC
C _{iss}	input capacitance	V_{DD} = 800 V; f = 0.5 MHz; V_{GS} = 0 V;		-	1335	-	pF
C _{oss}	output capacitance	T _j = 25 °C		-	74	-	pF
C _{rss}	reverse transfer capacitance			-	4	-	pF
t _{d(on)}	turn-on delay time	V_{DD} = 800 V; I_{D} = 20 A; $R_{G(ext)}$ = 2.2 Ω ;		-	12	-	ns
t _r	rise time	$L_L = 82 \mu H; V_{GS} = -5/+18 \text{ V}; T_j = 25 \text{ °C}$		-	9	-	ns
t _{d(off)}	turn-off delay time			-	14	-	ns
t _f	fall time			-	7	-	ns
E _{on}	turn-on switching loss			-	161	-	μJ
E _{off}	turn-off switching loss			-	56	-	μJ
Source-dra	in diode			'	<u>'</u>	<u>'</u>	
V _{SD}	source-drain voltage	I _S = 20 A; V _{GS} = -5 V; T _j = 25 °C		-	4.4	-	V
t _{rr}	reverse recovery time	V _{DD} = 800 V; I _S = 20 A; dI _S /dt = 7861 A/		-	6	-	ns
Q _r	recovered charge	μs; V _{GS} = -5 V; T _j = 25 °C		-	89	-	nC

^[1] Measured according to JEP183.

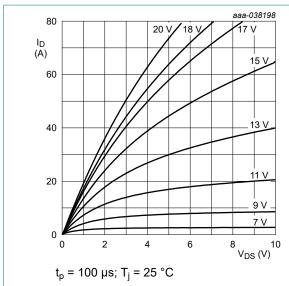


Fig. 1. Output characteristics: drain current as a function of drain-source voltage; typical values

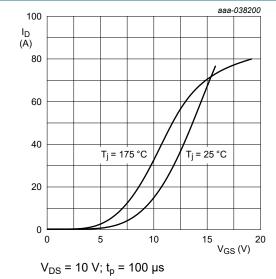


Fig. 3. Transfer characteristics: drain current as a function of gate-source voltage; typical values

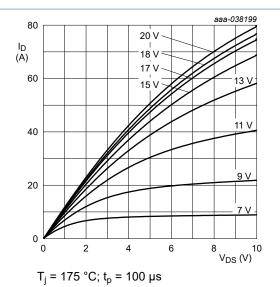


Fig. 2. Output characteristics: drain current as a function of drain-source voltage; typical values

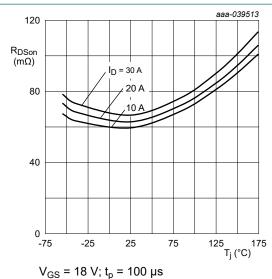


Fig. 4. Drain-source on-state resistance as a function of junction temperature; typical values

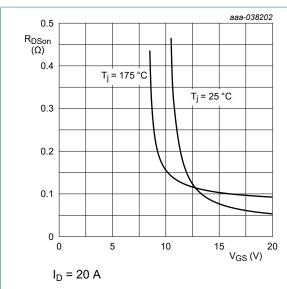


Fig. 5. Drain-source on-state resistance as a function of threshold voltage

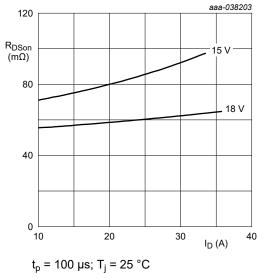


Fig. 6. Drain-source on-state resistance as a function of drain current; typical values

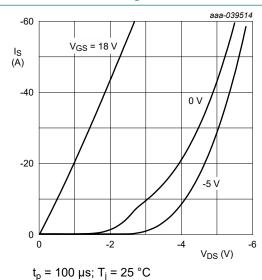


Fig. 7. Source current as a function of sourcedrain voltage; typical values (third quadrant characteristics)

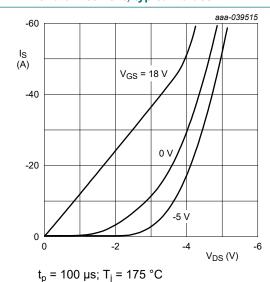


Fig. 8. Source current as a function of sourcedrain voltage; typical values (third quadrant characteristics)

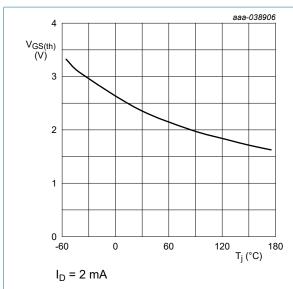


Fig. 9. Gate-source threshold voltage as a function of junction temperature; typical values

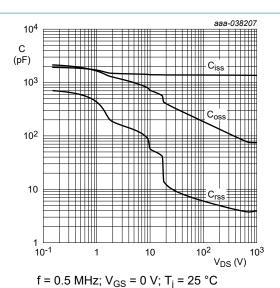


Fig. 10. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical

values

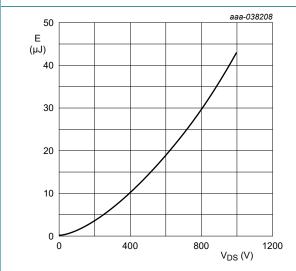
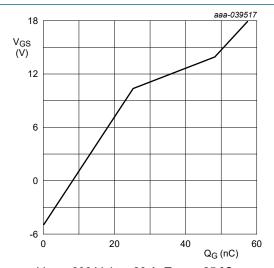


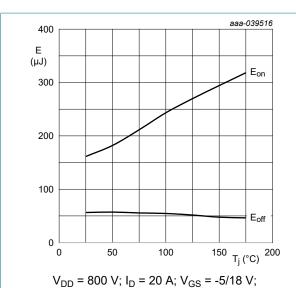
Fig. 11. C_{oss} stored energy as a function of drain-souce voltage; typical values



 V_{DD} = 800 V; I_D = 20 A; T_{amb} = 25 °C

Fig. 12. Gate-source voltage as a function of gate charge; typical values

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 $R_{G, ext} = 2.2 \ \Omega; \ L_L = 82 \ \mu H$ Fig. 13. Switching loss as a function of junction

temperature; typical values

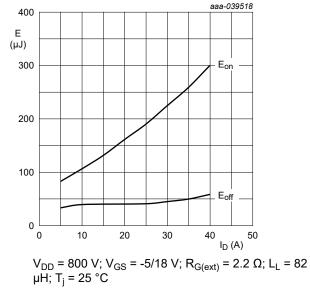


Fig. 14. Switching loss as a function of drain current; typical values

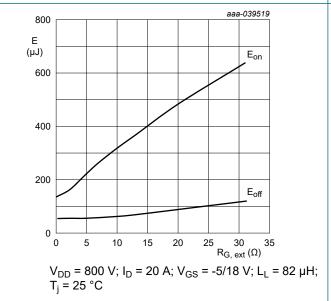
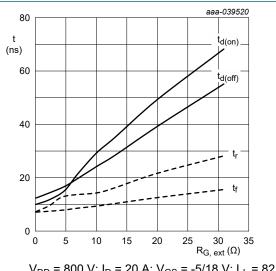
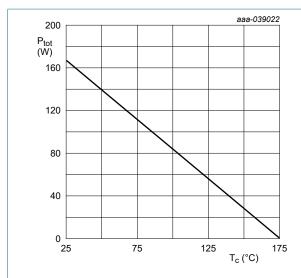


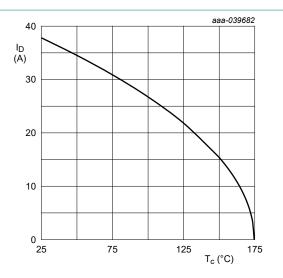
Fig. 15. Switching loss as a function of external gate resistance; typical values



 V_{DD} = 800 V; I_{D} = 20 A; V_{GS} = -5/18 V; L_{L} = 82 $\mu H;$ T_{i} = 25 °C

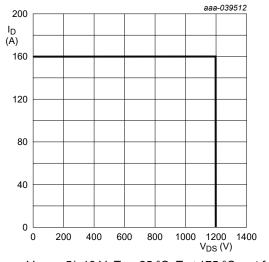
Fig. 16. Switching times as a function of external gate resistance; typical values

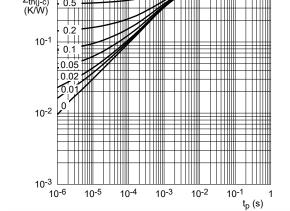




temperature; maximum values

Fig. 17. Power dissipation derating as a function of case Fig. 18. Continuous drain current as a function of case temperature; maximum values





 $V_{GS} = -5/+18 \text{ V}; T_c = 25 \text{ °C}; T_j \le 175 \text{ °C}; \text{ not for }$ linear use

Fig. 20. Transient thermal impedance from junction to case as a function of pulse duration; typical values

Fig. 19. Reverse bias safe operating area (RBSOA)

11. Test information

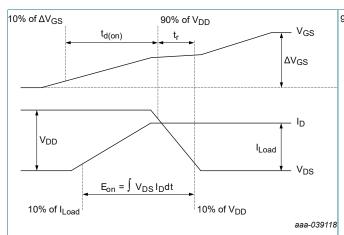


Fig. 21. Definition of switching times and losses during channel turn-on

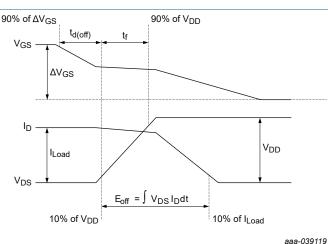


Fig. 22. Definition of switching times and losses during channel turn-off

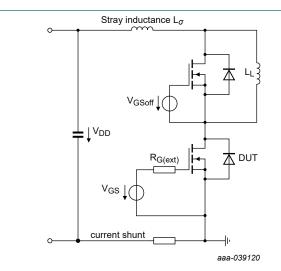


Fig. 23. Test circuit for dynamic characterization of channel and gate charge characteristics

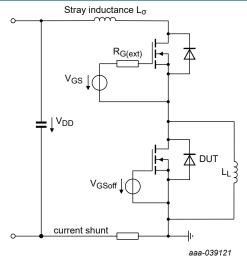


Fig. 24. Test circuit for dynamic characterization of body diode

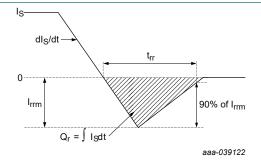


Fig. 25. Definition of dynamic characteristics of body diode

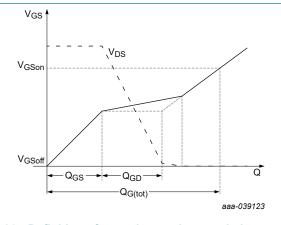
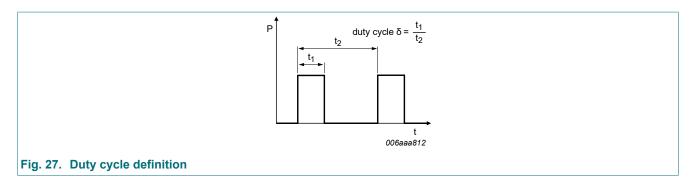


Fig. 26. Definition of gate charge characteristics

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12. Package outline

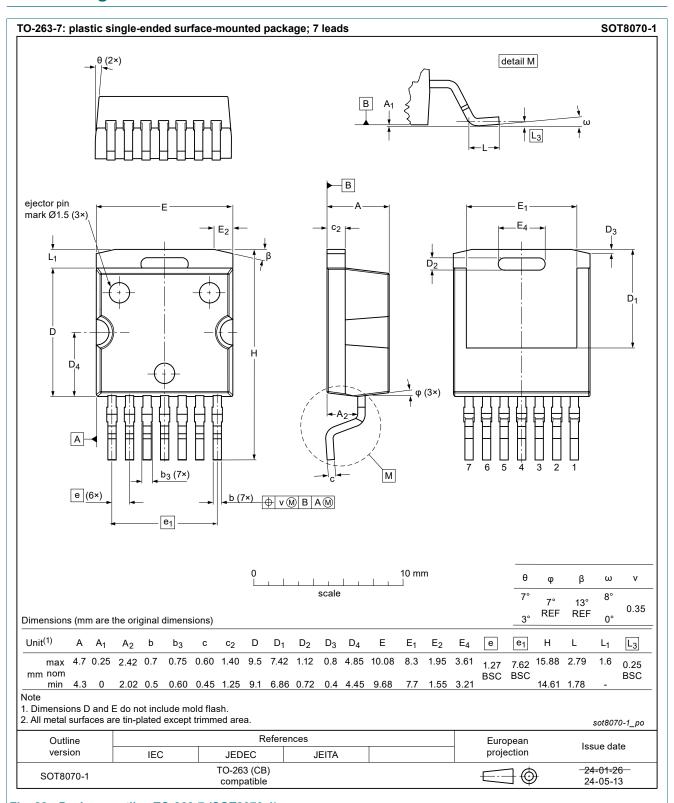
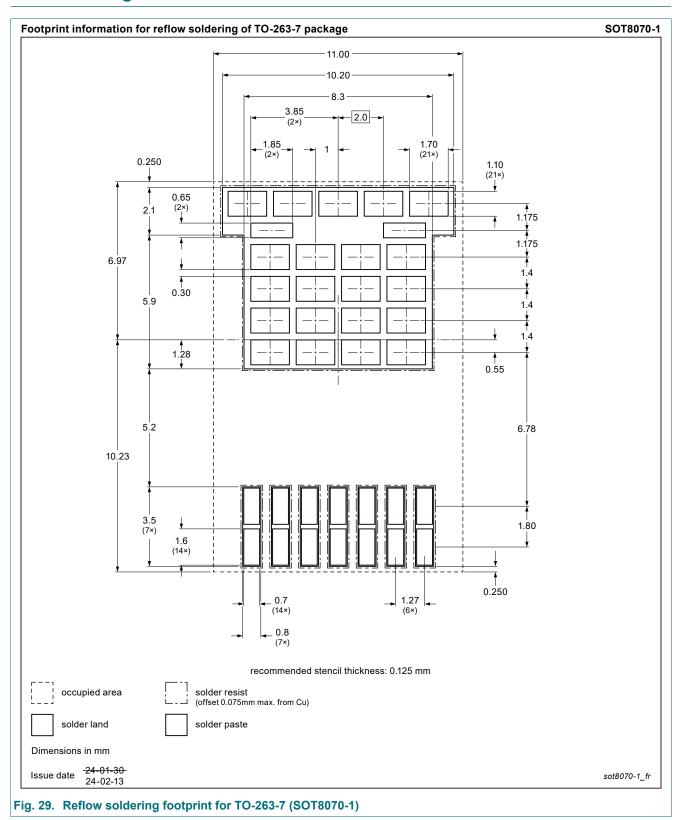


Fig. 28. Package outline TO-263-7 (SOT8070-1)

13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NSF060120D7A0 v.1	20240514	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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	Features and benefits

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