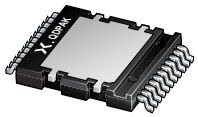


NSF040120T1A1

1200 V, 40 mΩ, N-channel SiC MOSFET

26 March 2026 **Product data sheet**

1. General description

The NSF040120T1A1 is a Silicon Carbide based 1200 V power MOSFET in the QDPAK plastic package for surface-mounted, topside-cooled technology. Its excellent R_{DSon} temperature stability, combined with fast switching speed, makes it a product of choice in high power and high voltage industrial applications, such as E-vehicle charging infrastructure, photovoltaic inverters and motor drives.

2. Features and benefits

- Excellent R_{DSon} temperature stability
- Very low switching losses
- Fast reverse recovery
- Fast switching speed
- Temperature independent turn-off switching losses
- Very fast and robust intrinsic body diode
- Faster commutation and improved switching due to the additional Kelvin source pin

3. Applications

- E-vehicle charging infrastructure
- Photovoltaic inverters
- Switch mode power supply
- Uninterruptable power supply
- Motor drives

4. Quick reference data

Table 1. Quick reference data

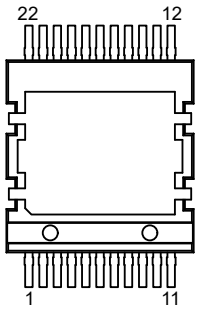
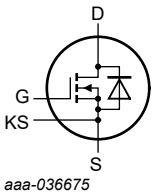
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage			-	-	1200	V
V_{GS}	gate-source voltage		[1]	-10	-	22	V
I_D	drain current	T_c = 25 °C	[2]	-	-	50	A
		T_c = 100 °C	[2]	-	-	35	A
I_{DM}	peak drain current	pulsed; t_p limited by T_j (max)	[3]	-	-	120	A
Static characteristics							
R_{DSon}	drain-source on-state resistance	V_{GS} = 18 V; I_D = 30 A; T_j = 25 °C		-	40	60	mΩ

[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with V_{GSon} < 13 V.
[2] Limited by the maximum values of T_j, $R_{th(j-c)}$ and $R_{DSon}(T_j)$.
[3] Designed value (not tested).

nexperia

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 Transparent top view **QDPAK (SOT8114-1)**	
2	KS	Kelvin source		
3 to 11	S	source		
12 to 22	D	drain		
HS	D	heatsink, connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NSF040120T1A1	QDPAK	Plastic, surface-mounted top side cooling (TSC) package; 22 leads; 1.14 mm pitch; 15.0 × 15.4 mm × 2.3 mm body	SOT8114-1

7. Marking

Table 4. Marking codes

Type number	Marking code
NSF040120T1A1	NSF040120T1A1

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage			-	1200	V
V_{GS}	gate-source voltage		[1]	-10	22	V
I_D	drain current	T_c = 25 °C	[2]	-	50	A
		T_c = 100 °C	[2]	-	35	A
I_{DM}	peak drain current	pulsed; t_p limited by T_j (max)	[3]	-	120	A
P_{tot}	total power dissipation	T_c = 25 °C		-	217	W
T_j	junction temperature			-55	175	°C
T_{stg}	storage temperature			-55	150	°C
$T_{sld(M)}$	peak soldering temperature			-	260	°C
Source-drain diode						
I_S	source current	T_c = 25 °C	[2]	-	40	A
I_{SM}	peak source current	pulsed; limited by T_j (max)	[3]	-	90	A

[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with V_{GSon} < 13 V.
[2] Limited by the maximum values of T_j, $R_{th(j-c)}$ and $R_{DSon}(T_j)$.
[3] Designed value (not tested).

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case			-	0.53	0.69	K/W

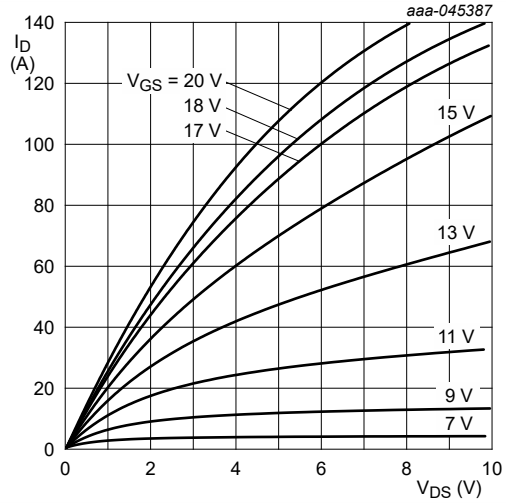
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 1 mA; V_{GS} = 0 V; T_j = 25 °C		1200	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I_D = 3 mA; V_{DS} = V_{GS}; T_j = 25 °C	[1]	1.7	2.3	2.9	V
		I_D = 15 mA; V_{DS} = V_{GS}; T_j = 25 °C	[1]	-	2.77	-	V
I_{DSS}	drain leakage current	V_{DS} = 1200 V; V_{GS} = 0 V; T_j = 25 °C		-	-	100	µA
I_{GSS}	gate leakage current	V_{GS} = 22 V; V_{DS} = 0 V; T_j = 25 °C		-	-	100	nA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C		-	-	100	nA
R_{DSon}	drain-source on-state resistance	V_{GS} = 18 V; I_D = 30 A; T_j = 25 °C		-	40	60	mΩ
		V_{GS} = 18 V; I_D = 30 A; T_j = 175 °C		-	67	-	mΩ
		V_{GS} = 15 V; I_D = 30 A; T_j = 25 °C		-	53	-	mΩ
		V_{GS} = 15 V; I_D = 30 A; T_j = 175 °C		-	73	-	mΩ
g_{fs}	forward transconductance	V_{DS} = 10 V; I_D = 30 A; T_j = 25 °C		-	13.1	-	S
$R_{G(int)}$	internal gate resistance	f = 0.5 MHz; T_j = 25 °C		-	2	-	Ω
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	V_{DD} = 800 V; I_D = 30 A; V_{GS} = -5/+18 V; T_j = 25 °C		-	78	-	nC
Q_{GS}	gate-source charge			-	38	-	nC
Q_{GD}	gate-drain charge			-	18	-	nC
C_{iss}	input capacitance	V_{DD} = 800 V; f = 500 kHz; V_{GS} = 0 V; T_j = 25 °C		-	1984	-	pF
C_{oss}	output capacitance			-	109	-	pF
C_{rss}	reverse transfer capacitance			-	3	-	pF
$C_{o(er)}$	effective output capacitance, energy related	V_{DD} = 800 V; V_{GS} = 0 V; f = 500 kHz; Calculated based on C_{oss}; T_j = 25 °C		-	137	-	pF
$C_{o(tr)}$	effective output capacitance, time related			-	194	-	pF
Q_{oss}	output capacitance charge			-	155	-	nC
E_{oss}	C_{oss} stored energy			-	44	-	µJ
$t_{d(on)}$	turn-on delay time	V_{DD} = 800 V; I_D = 30 A; $R_{G(ext)}$ = 2.2 Ω; L_L = 82 µH; V_{GS} = -5/+18 V; T_j = 25 °C		-	17	-	ns
t_r	rise time			-	9	-	ns
$t_{d(off)}$	turn-off delay time			-	19	-	ns
t_f	fall time			-	8	-	ns
E_{on}	turn-on switching loss			-	336	-	µJ
E_{off}	turn-off switching loss			-	47	-	µJ
Source-drain diode							
V_{SD}	source-drain voltage	V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; T_j = 25 °C		-	4.4	-	V

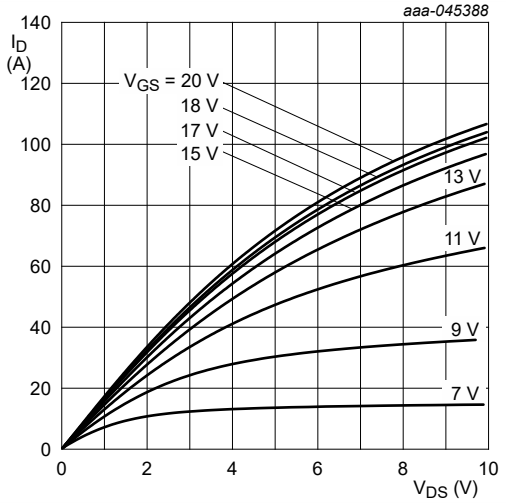
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
t_{rr}	reverse recovery time	V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; dI_S/dt = -6078 A/µs; T_j = 175 °C		-	21	-	ns
		V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; dI_S/dt = -2257 A/µs; T_j = 175 °C		-	36	-	ns
Q_r	recovered charge	V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; dI_S/dt = -6078 A/µs; T_j = 175 °C		-	721	-	nC
		V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; dI_S/dt = -2257 A/µs; T_j = 175 °C		-	668	-	nC
I_{RRM}	peak reverse recovery current	V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; dI_S/dt = -6078 A/µs; T_j = 175 °C		-	46	-	A
		V_{DD} = 800 V; I_S = 30 A; V_{GS} = -5 V; dI_S/dt = -2257 A/µs; T_j = 175 °C		-	25	-	A

[1] Measured according to JEP183.



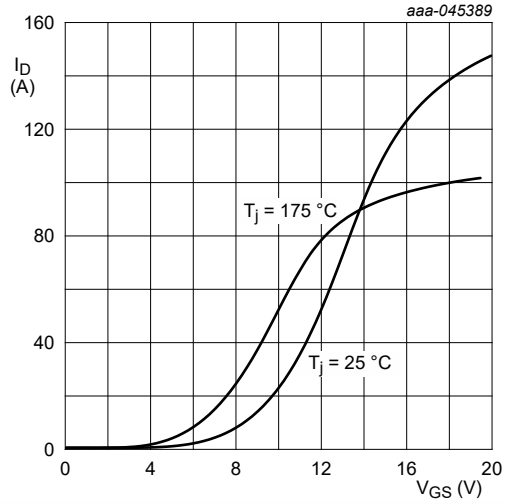
t_p = 300 µs; T_j = 25 °C

Fig. 1. Output characteristics: drain current as a function of drain-source voltage; typical values



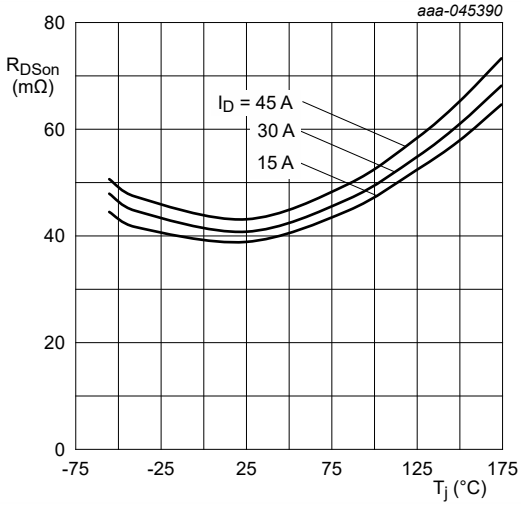
t_p = 300 µs; T_j = 175 °C

Fig. 2. Output characteristics: drain current as a function of drain-source voltage; typical values



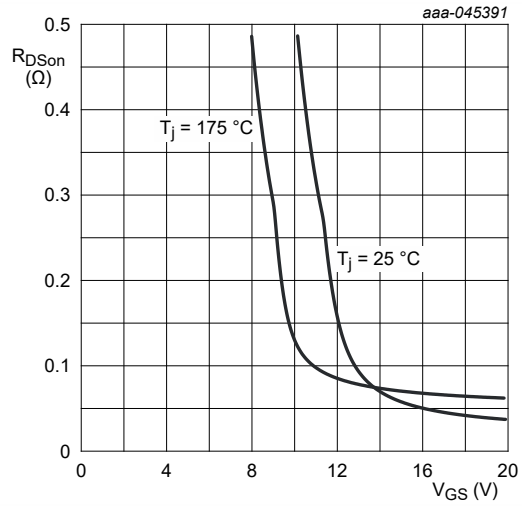
V_{DS} = 10 V; t_p = 300 µs

Fig. 3. Transfer characteristics: drain current as a function of gate-source voltage; typical values



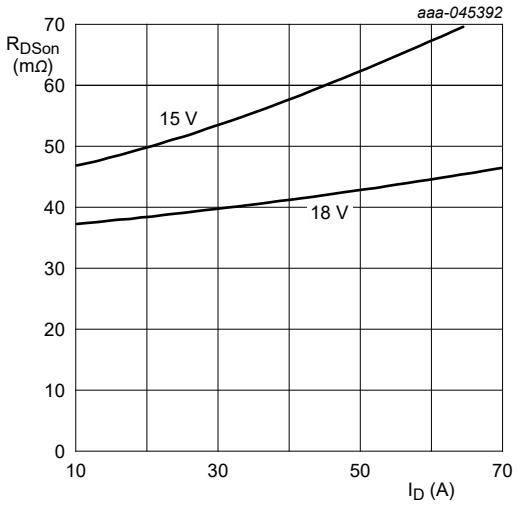
V_{GS} = 18 V; t_p = 300 µs

Fig. 4. Drain-source on-state resistance as a function of junction temperature; typical values



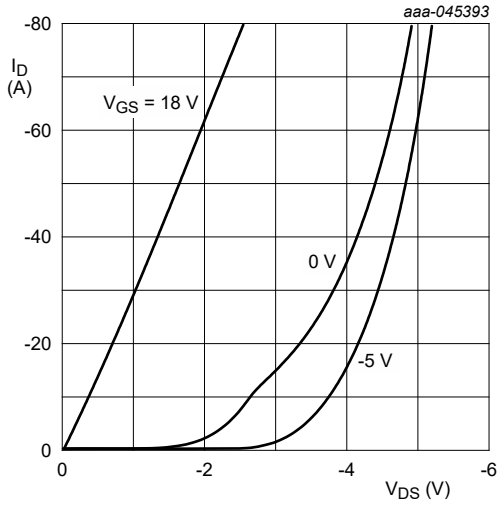
I_D = 30 A

Fig. 5. Drain-source on-state resistance as a function of gate-source voltage



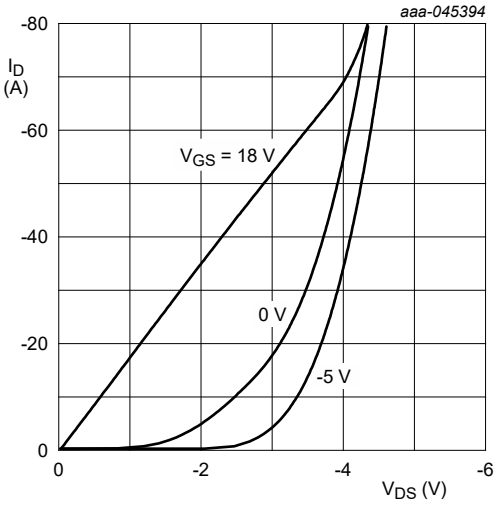
t_p = 300 µs; T_j = 25 °C

Fig. 6. Drain-source on-state resistance as a function of drain current; typical values



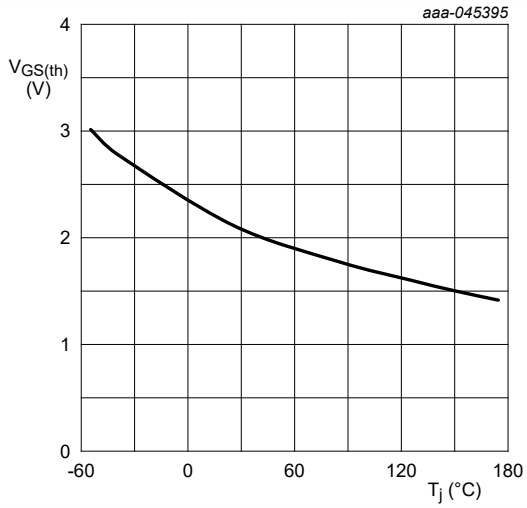
t_p = 300 µs; T_j = 25 °C

Fig. 7. Drain current as a function of drain-source voltage; typical values (third quadrant characteristics)



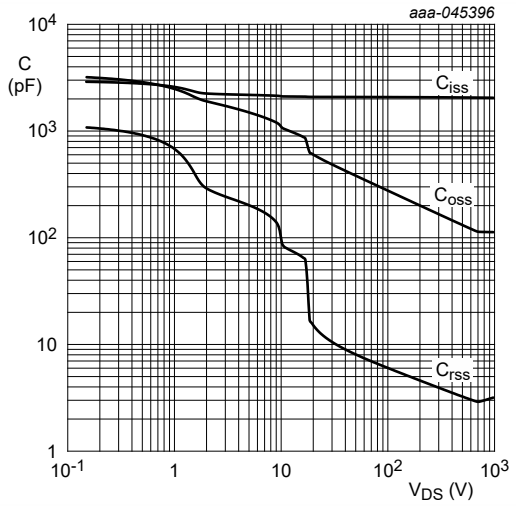
t_p = 300 µs; T_j = 175 °C

Fig. 8. Drain current as a function of drain-source voltage; typical values (third quadrant characteristics)



I_D = 3 mA

Fig. 9. Gate-source threshold voltage as a function of junction temperature; typical values



f = 0.5 MHz; V_{GS} = 0 V; T_j = 25 °C

Fig. 10. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

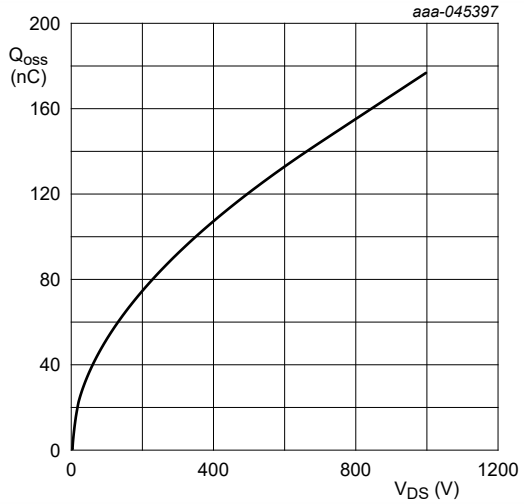


Fig. 11. Output capacitance charge as a function of drain-source voltage; typical values

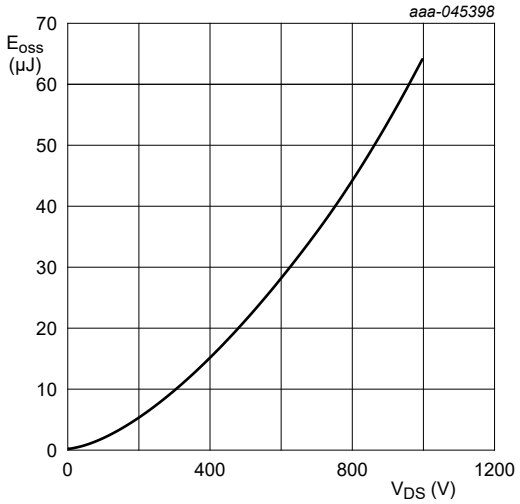
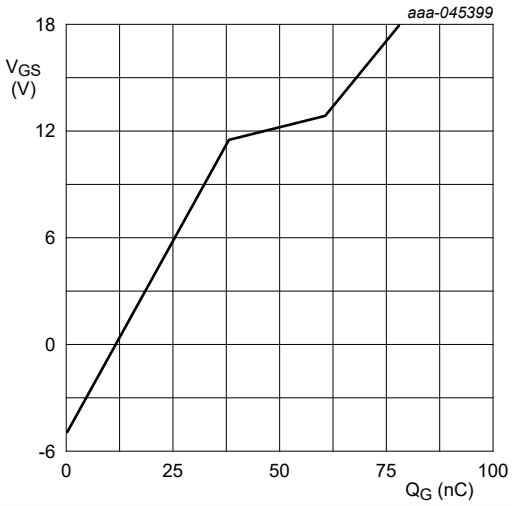
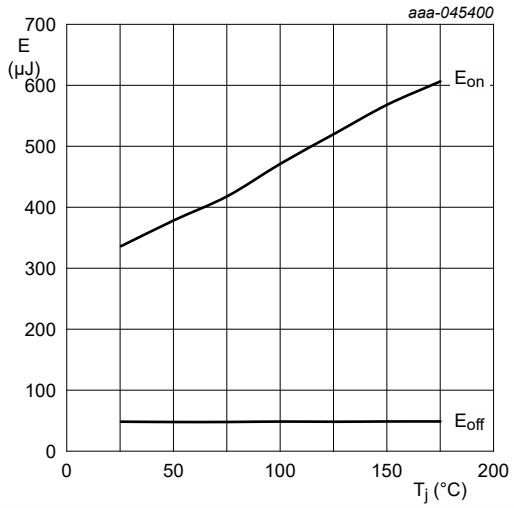


Fig. 12. C_{oss} stored energy as a function of drain-source voltage; typical values



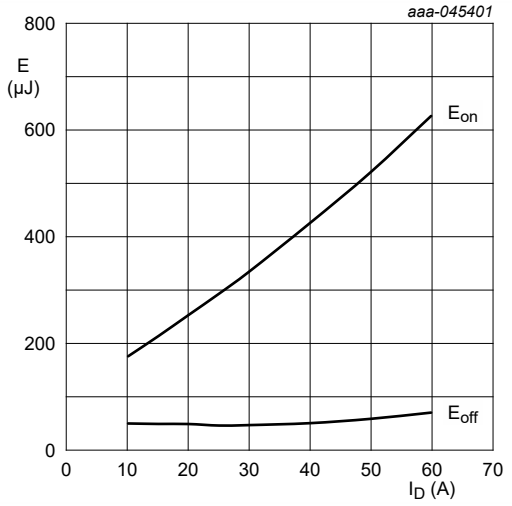
V_{DD} = 800 V; I_D = 30 A; T_j = 25 °C

Fig. 13. Gate-source voltage as a function of gate charge; typical values



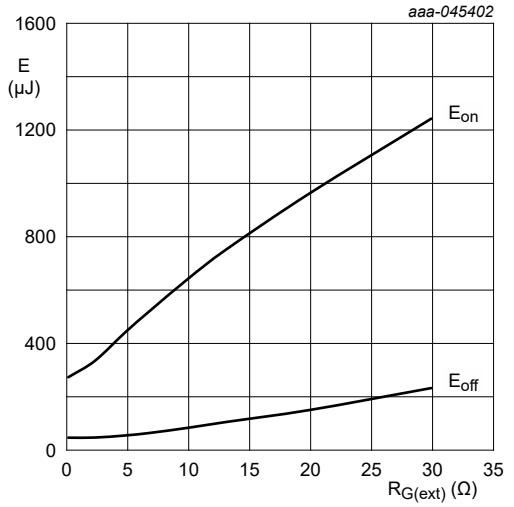
V_{DD} = 800 V; I_D = 30 A; V_{GS} = -5/18 V; $R_{G(ext)}$ = 2.2 Ω; L_L = 82 µH

Fig. 14. Switching loss as a function of junction temperature; typical values



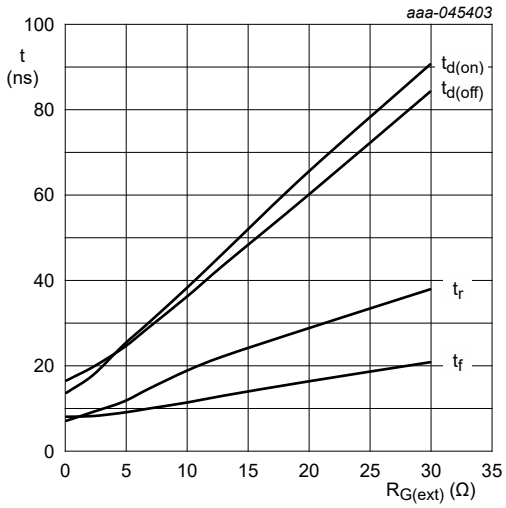
V_{DD} = 800 V; V_{GS} = -5/18 V; $R_{G(ext)}$ = 2.2 Ω; L_L = 82 µH; T_j = 25 °C

Fig. 15. Switching loss as a function of drain current; typical values



V_{DD} = 800 V; I_D = 30 A; V_{GS} = -5/18 V; L_L = 82 µH; T_j = 25 °C

Fig. 16. Switching loss as a function of external gate resistance; typical values



V_{DD} = 800 V; I_D = 30 A; V_{GS} = -5/18 V; L_L = 82 µH; T_j = 25 °C

Fig. 17. Switching times as a function of external gate resistance; typical values

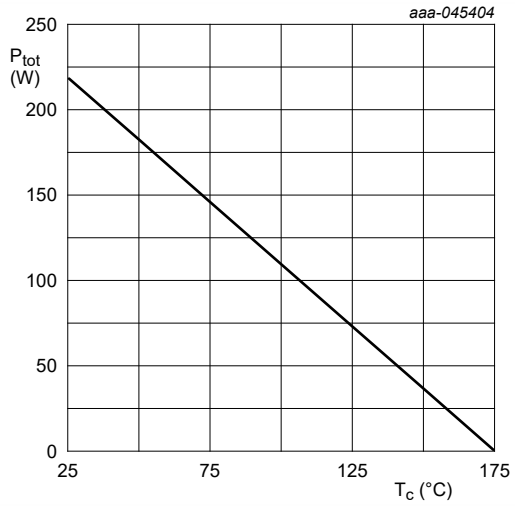


Fig. 18. Power dissipation derating as a function of case temperature; maximum values

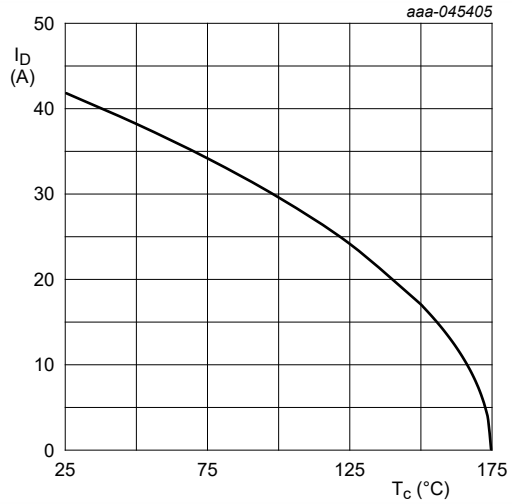
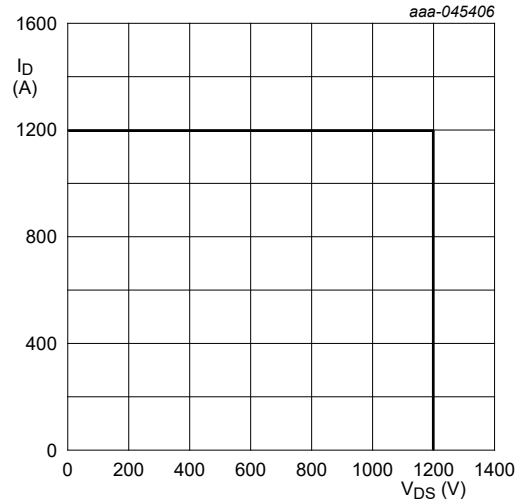


Fig. 19. Continuous drain current as a function of case temperature; maximum values



V_{GS} = -5/+18 V; T_c = 25 °C; T_j ≤ 175 °C; not for linear use

Fig. 20. Reverse bias safe operating area (RBSOA)

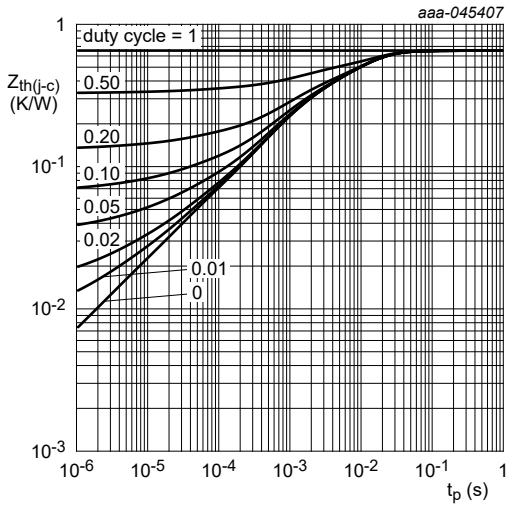


Fig. 21. Transient thermal impedance from junction to case as a function of pulse duration; typical values

11. Test information

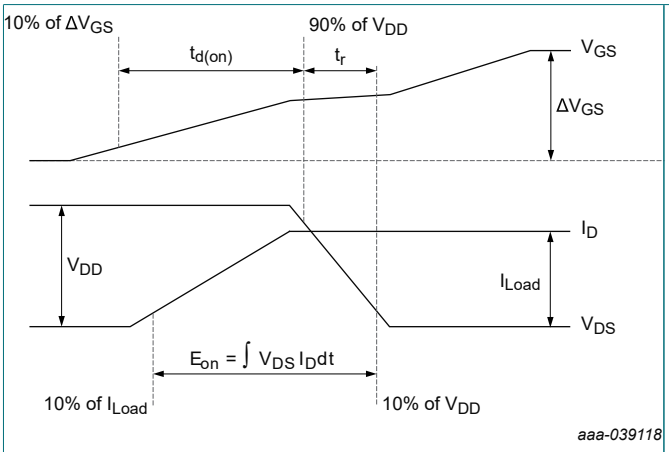


Fig. 22. Definition of switching times and losses during channel turn-on

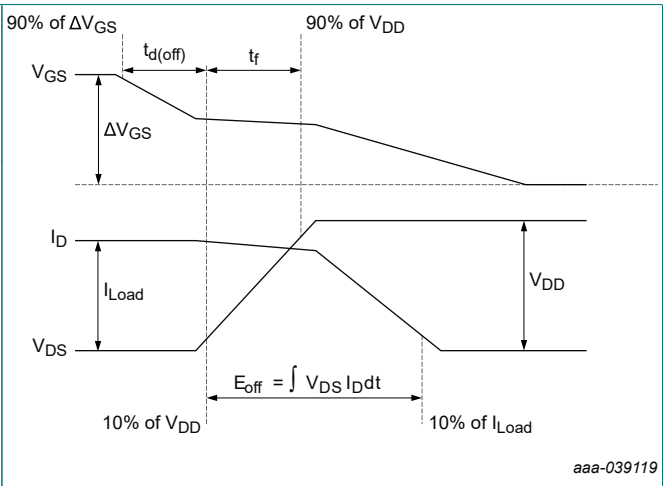


Fig. 23. Definition of switching times and losses during channel turn-off

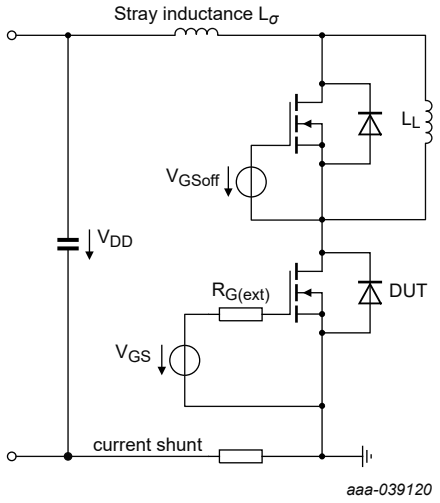


Fig. 24. Test circuit for dynamic characterization of channel and gate charge characteristics

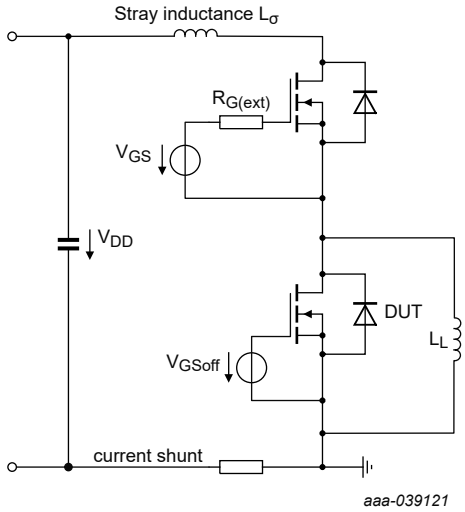


Fig. 25. Test circuit for dynamic characterization of body diode

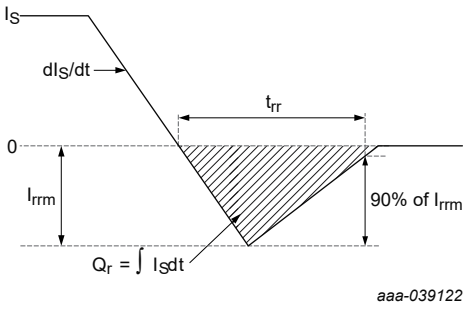


Fig. 26. Definition of dynamic characteristics of body diode

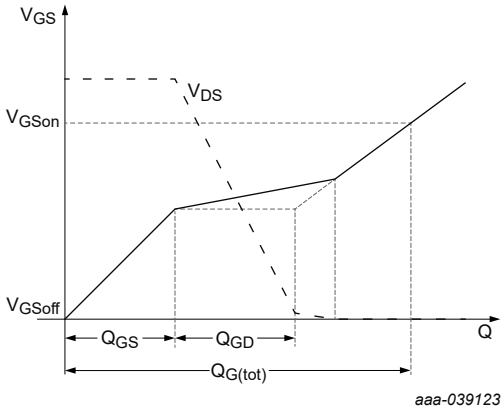


Fig. 27. Definition of gate charge characteristics

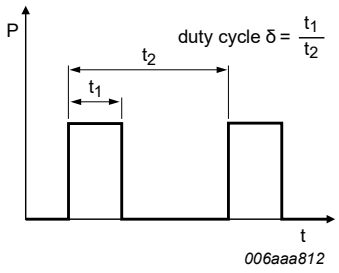
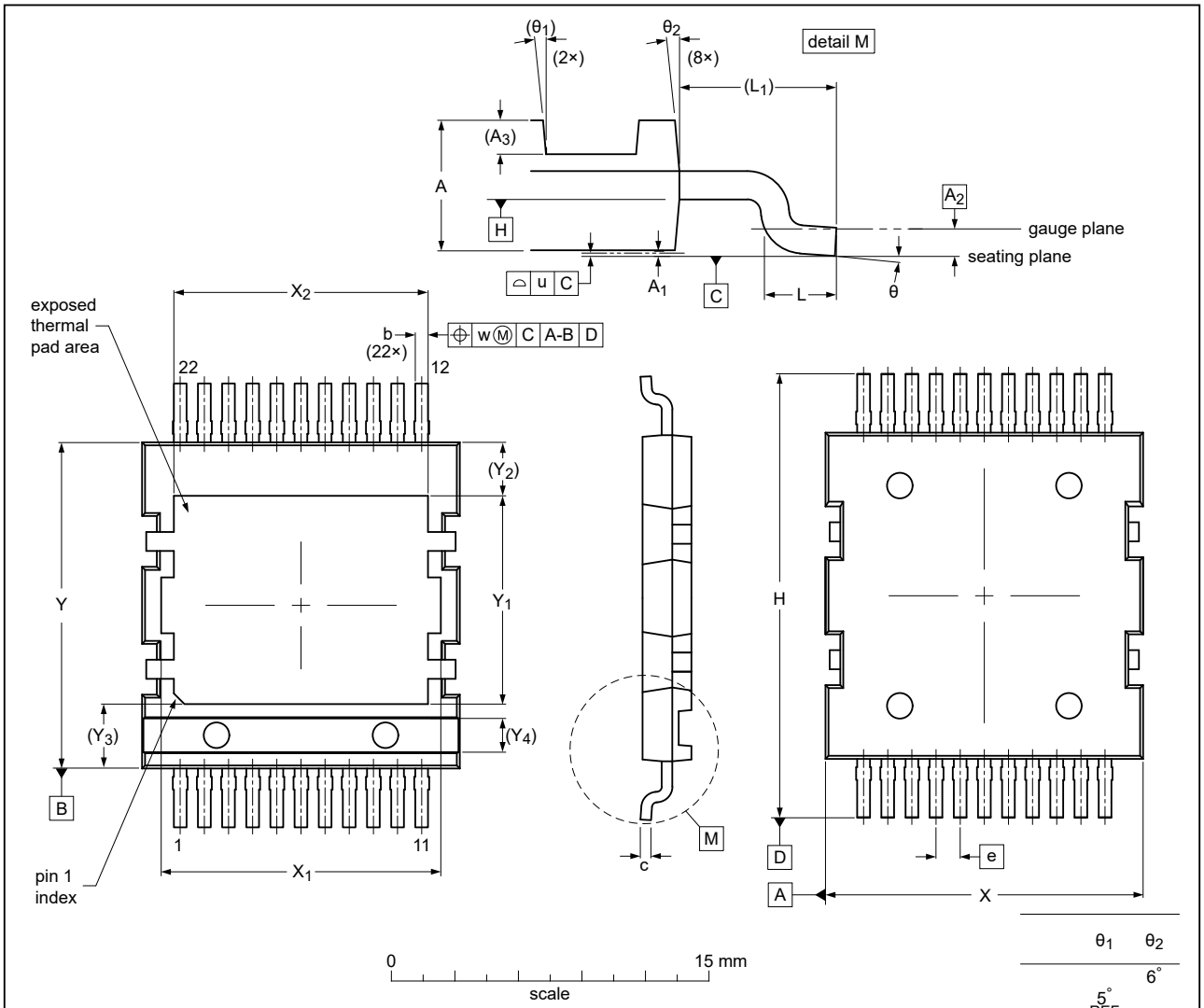


Fig. 28. Duty cycle definition

12. Package outline

Plastic, surface-mounted top side cooling (TSC) package; 22 leads; 1.14 mm pitch; 15.0 × 15.4 mm × 2.3 mm body **SOT8114-1**



	θ1	θ2
		6°
	5° REF	4°

Dimensions (mm are the original dimensions)

Unit[1]		A	A_1	A_2	A_3	b	c	e	H	L	L_1	u	w	X	X_1	X_2	Y	Y_1	Y_2	Y_3	Y_4	θ
mm	max	2.35	0.15	0.50	0.60 REF	0.70	0.58	1.14	21.11	1.40	2.78 REF	0.10	0.25	15.10	13.30	12.11	15.50	9.93	2.53 REF	3.04 REF	1.70 REF	8°
	nom	2.30				0.60	0.50		20.96	1.30				15.00	13.20	12.01	15.40	9.83				
	min	2.20	0			0.50	0.46		20.81	1.20				14.90	13.10	11.91	15.30	9.73				0°

Note

1. X dimension does not include mold flash, protrusion or gate burrs. Mold flash, protrusion or gate burr shall not exceed 0.15 mm per end. Y dimension does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0. 25 mm per side. X and Y dimensions are determined at datum H.
2. Datums A and B to be determined at datum H.

sot8114-1_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT8114-1		MO-354				26-01-07

Fig. 29. Package outline QDPAK (SOT8114-1)

13. Soldering

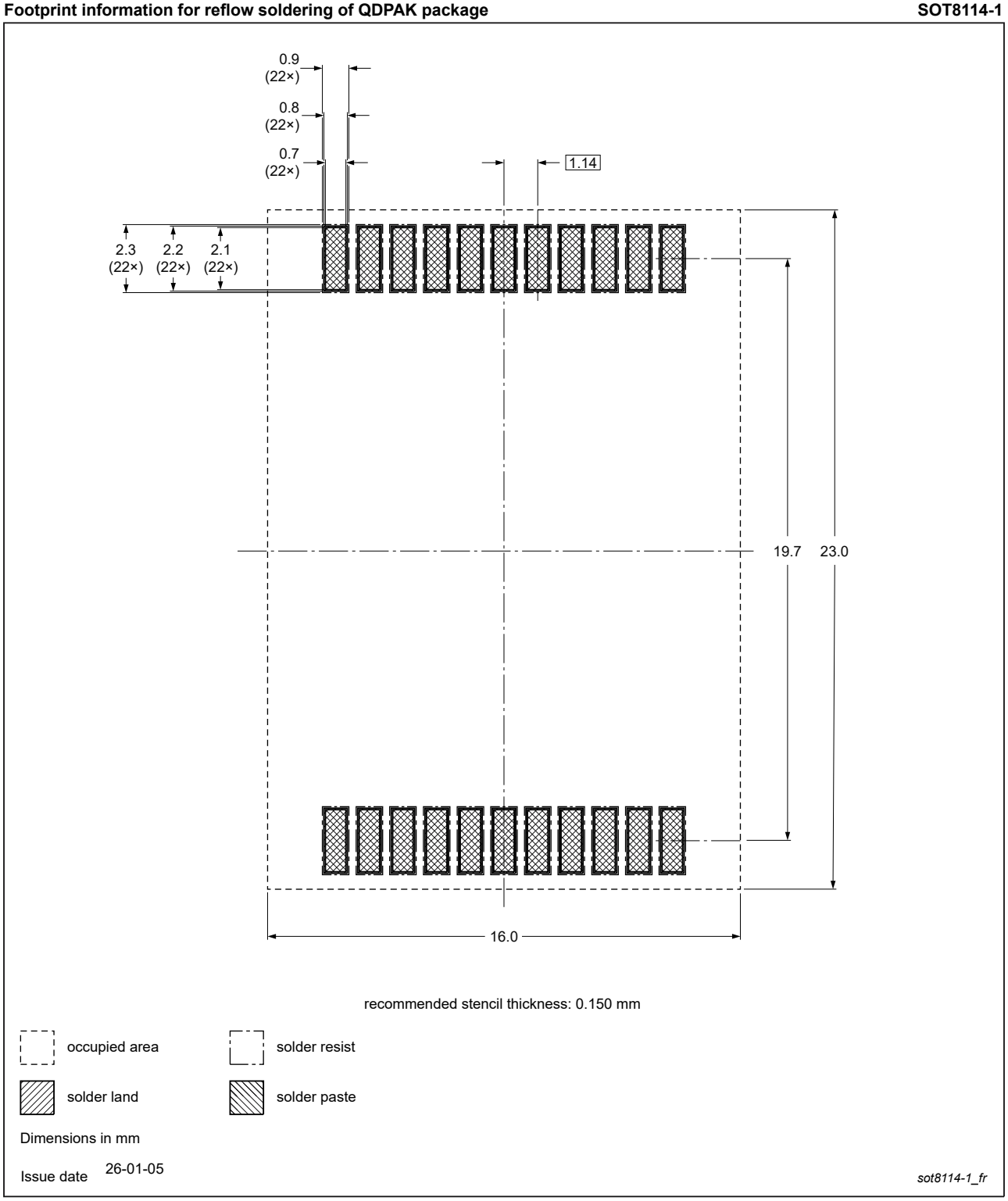


Fig. 30. Reflow soldering footprint for QDPAK (SOT8114-1)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NSF040120T1A1 v.1	20260326	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

For more information, please visit: http://www.nexperia.com
For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 26 March 2026