

1200 V, 40 mΩ, N-channel SiC MOSFET

14 May 2024

**Product data sheet** 

### 1. General description

The NSF040120D7A0 is a Silicon Carbide based 1200 V power MOSFET in a well-established 7-pin TO-263 plastic package for surface mounting PCB technology. The excellent  $R_{DSon}$  temperature stability combined with its fast switching speed makes it a product of choice in high power and high voltage industrial applications like E-vehicle charging infrastructure, photovoltaic inverters and motor drives.

### 2. Features and benefits

- Excellent R<sub>DSon</sub> temperature stability
- Very low switching losses
- · Fast reverse recovery
- Fast switching speed
- Temperature independent turn-off switching losses
- · Very fast and robust intrinsic body diode
- · Faster commutation and improved switching due to the additional Kelvin source pin

### 3. Applications

- E-vehicle charging infrastructure
- Photovoltaic inverters
- Switch mode power supply
- Uninterruptable power supply
- Motor drives

### 4. Quick reference data

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage			-	-	1200	V
V <sub>GS</sub>	gate-source voltage		[1]	-10	-	22	V
I <sub>D</sub>	drain current	T <sub>c</sub> = 25 °C	[2]	-	-	65	А
		T <sub>c</sub> = 100 °C	[2]	-	-	46	А
I <sub>DM</sub>	peak drain current	pulsed; $t_p$ limited by $T_j$ (max)	[3]	-	-	160	А
Static chara	octeristics			·	·	·	
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 40 A; T <sub>j</sub> = 25 °C		-	40	60	mΩ

[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V. Do not use with V<sub>GSon</sub> < 13 V.

[2] Limited by the maximum values of  $T_j$ ,  $R_{th(j-c)}$  and  $R_{DSon}(T_j)$ .

[3] Designed value (not tested).

# ne<mark>x</mark>peria

# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	
2	KS	kelvin source		
3 to 7	S	source		D
mb	D	mounting base; connected to drain	TO-263-7 (SOT8070-1)	G KS aaa-036675

# 6. Ordering information

Table 3. Ordering information								
Type number	Package	Package						
	Name	Description	Version					
NSF040120D7A0	TO-263-7	plastic single-ended surface-mounted package; 7 leads	<u>SOT8070-1</u>					

# 7. Marking

Table 4. Marking codes	
Type number	Marking code
NSF040120D7A0	40120D7A0

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage			-	1200	V
V <sub>GS</sub>	gate-source voltage		[1]	-10	22	V
I <sub>D</sub>	drain current	T <sub>c</sub> = 25 °C	[2]	-	65	А
		T <sub>c</sub> = 100 °C	[2]	-	46	Α
I <sub>DM</sub>	peak drain current	pulsed; t <sub>p</sub> limited by T <sub>j</sub> (max)	[3]	-	160	Α
P <sub>tot</sub>	total power dissipation	T <sub>c</sub> = 25 °C	[2]	-	306	W
Tj	junction temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-55	150	°C
T <sub>sld(M)</sub>	peak soldering temperature			-	260	°C
Source-drai	n diode		I			
I <sub>S</sub>	source current	T <sub>c</sub> = 25 °C	[2]	-	54	А
I <sub>SM</sub>	peak source current	pulsed; limited by T <sub>j</sub> (max)	[3]	-	120	А

[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V. Do not use with V<sub>GSon</sub> < 13 V.

[2] Limited by the maximum values of  $T_j$ ,  $R_{th(j-c)}$  and  $R_{DSon}(T_j)$ .

[3] Designed value (not tested).

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

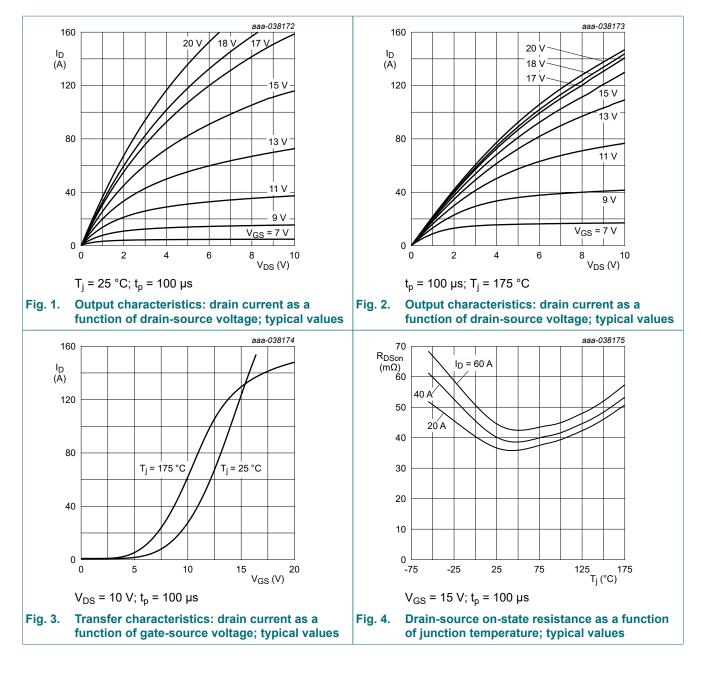
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-c)</sub>	thermal resistance from junction to case		-	0.4	0.49	K/W

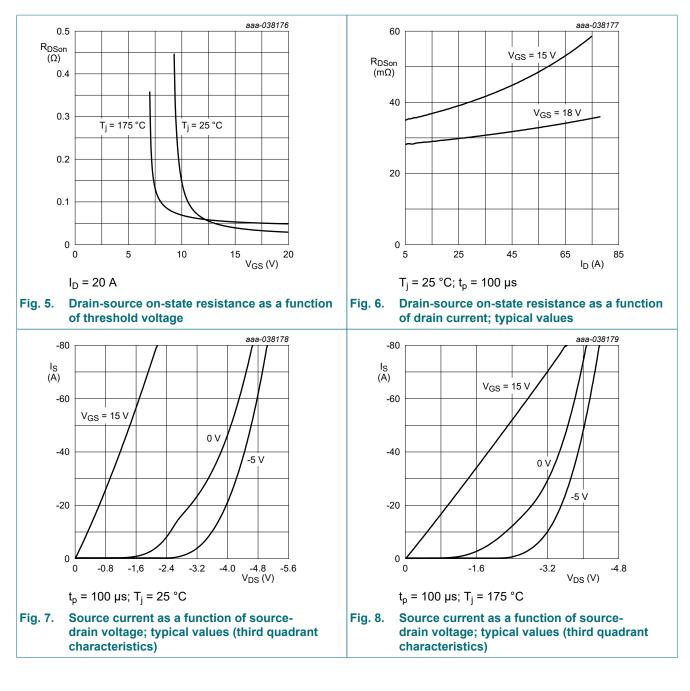
# **10. Characteristics**

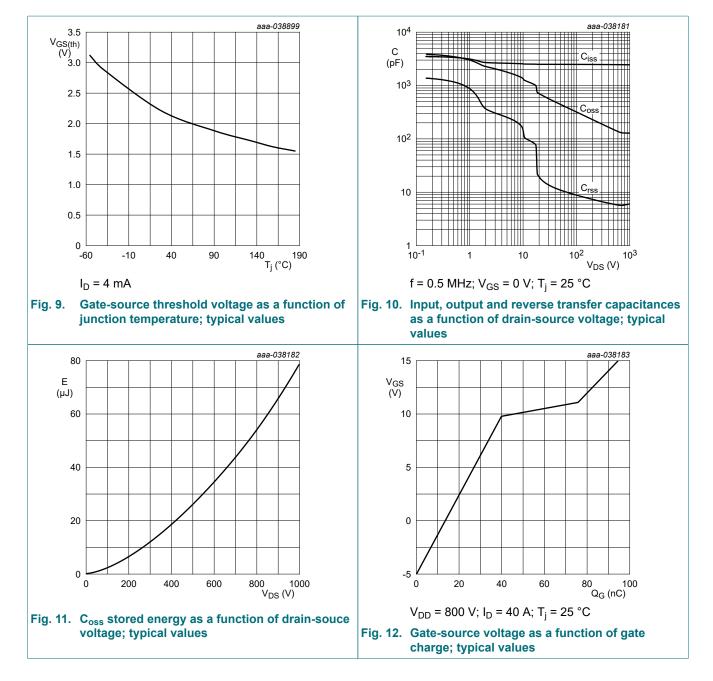
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	cteristics	1					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 1 mA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C		1200	-	-	V
V <sub>GS(th)</sub>	gate-source threshold	I <sub>D</sub> = 4 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C	[1]	1.7	2.3	2.9	V
	voltage	I <sub>D</sub> = 20 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C	[1]	-	2.77	-	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 1200 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	100	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 22 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	100	nA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	100	nA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 40 A; T <sub>j</sub> = 25 °C		-	40	60	mΩ
	resistance	V <sub>GS</sub> = 15 V; I <sub>D</sub> = 40 A; T <sub>j</sub> = 175 °C		-	53	-	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 40 A; T <sub>j</sub> = 25 °C		-	31	-	mΩ
		V <sub>GS</sub> = 18 V; I <sub>D</sub> = 40 A; T <sub>j</sub> = 175 °C		-	49	-	mΩ
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 40 A; T <sub>j</sub> = 25 °C		-	19	-	S
R <sub>G(int)</sub>	internal gate resistance	f = 0.5 MHz; T <sub>j</sub> = 25 °C		-	2.3	-	Ω
Dynamic ch	aracteristics			I			
Q <sub>G(tot)</sub>	total gate charge	$V_{DD} = 800 \text{ V}; I_D = 40 \text{ A}; V_{GS} = -5/+15 \text{ V};$ $T_j = 25 \text{ °C}$		-	95	-	nC
Q <sub>GS</sub>	gate-source charge			-	40	-	nC
Q <sub>GD</sub>	gate-drain charge			-	30	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DD</sub> = 800 V; f = 0.5 MHz; V <sub>GS</sub> = 0 V;		-	2600	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	136	-	pF
C <sub>rss</sub>	reverse transfer capacitance	-		-	6	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DD}$ = 800 V; I <sub>D</sub> = 40 A; R <sub>G(ext)</sub> = 2.2 Ω;		-	21	-	ns
t <sub>r</sub>	rise time	L <sub>L</sub> = 82 μH; V <sub>GS</sub> = -5/+15 V; Τ <sub>j</sub> = 25 °C		-	23	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	20	-	ns
t <sub>f</sub>	fall time			-	8	-	ns
Eon	turn-on switching loss			-	551	-	μJ
E <sub>off</sub>	turn-off switching loss			-	97	-	μJ
Source-drai	n diode	·					
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 40 A; V <sub>GS</sub> = -5 V; T <sub>j</sub> = 25 °C		-	4.4	-	V
t <sub>rr</sub>	reverse recovery time	V <sub>DD</sub> = 800 V; I <sub>S</sub> = 40 A; dI <sub>S</sub> /dt = 6670 A/		-	8	-	ns
Q <sub>r</sub>	recovered charge	μs; V <sub>GS</sub> = -5 V; Τ <sub>j</sub> = 25 °C		-	117	-	nC

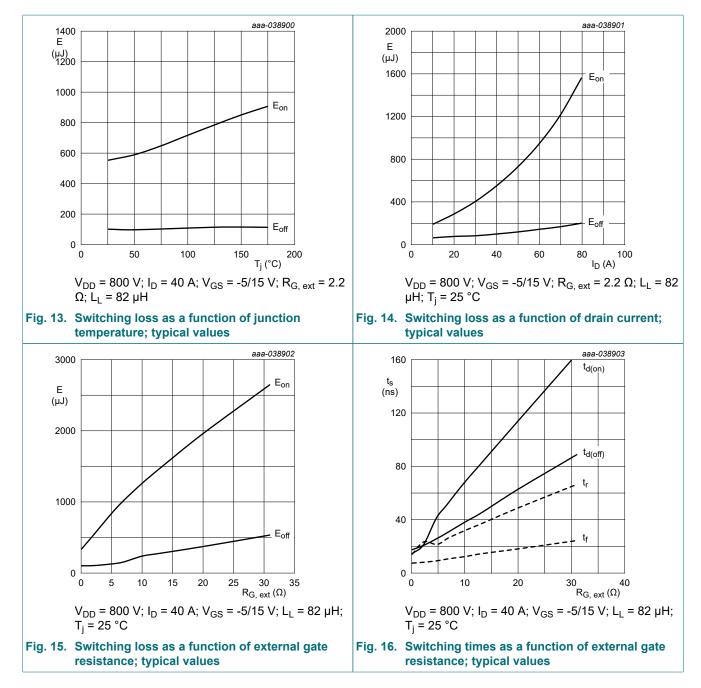
[1] Measured according to JEP183.

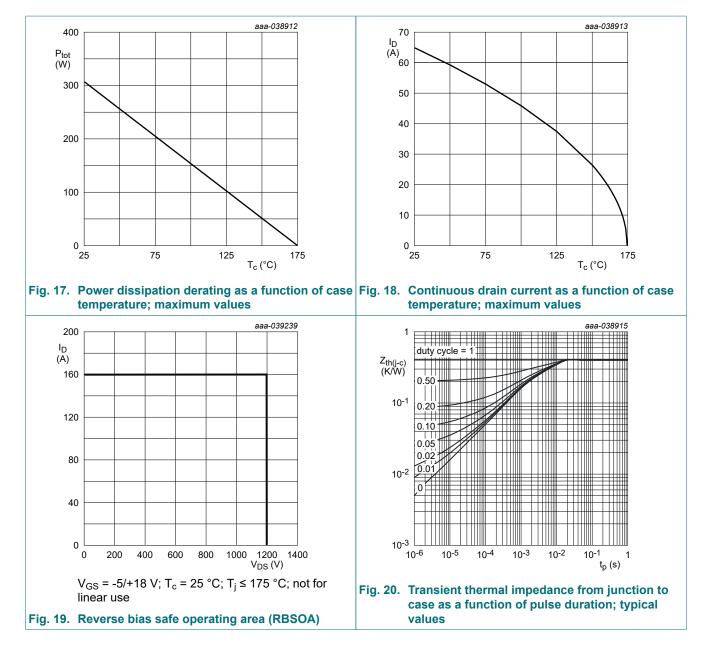
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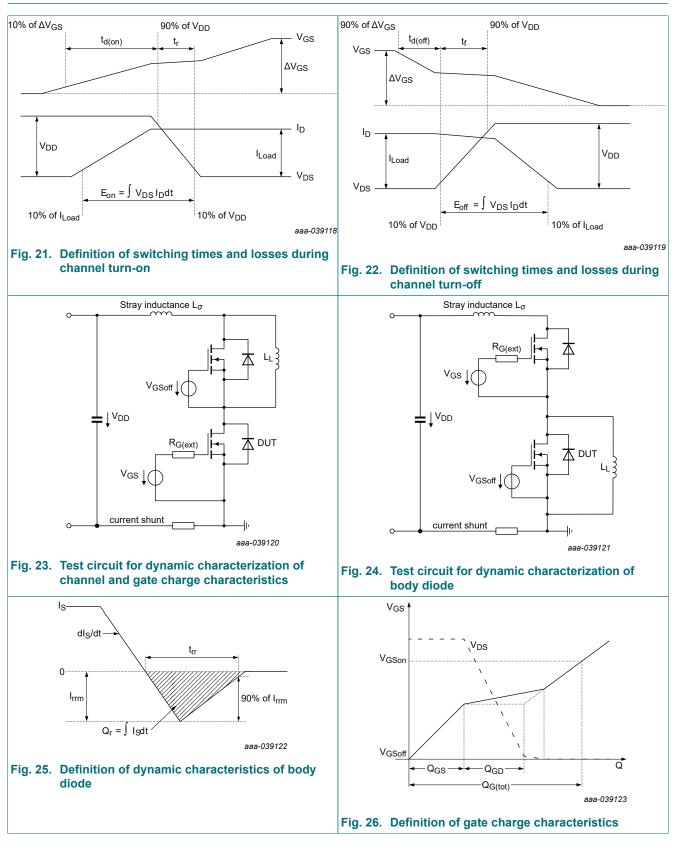




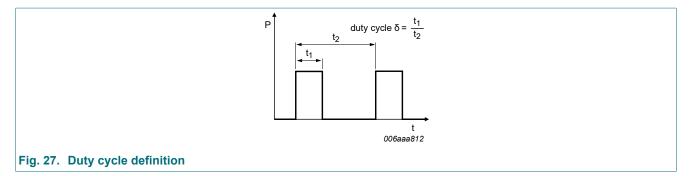




### **11. Test information**

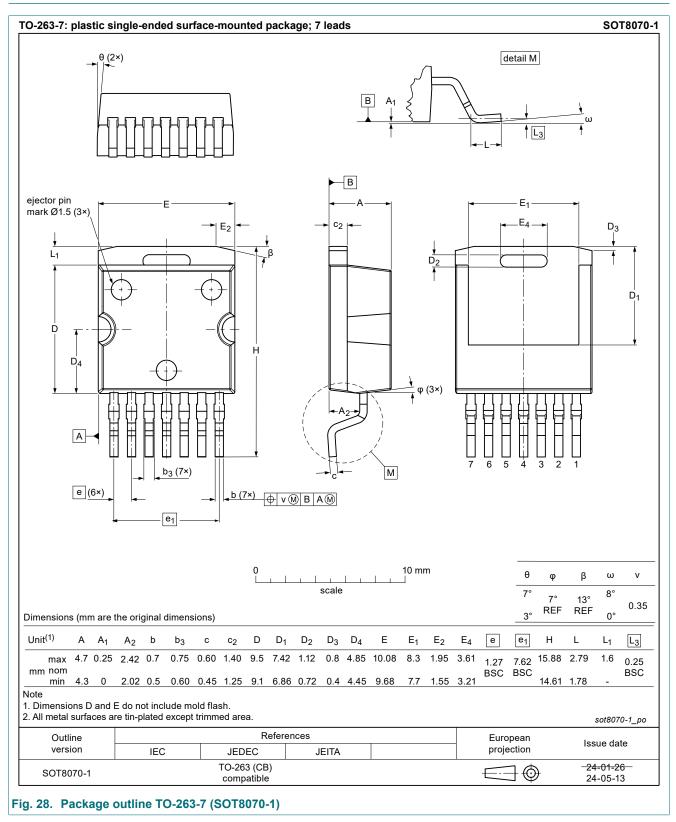


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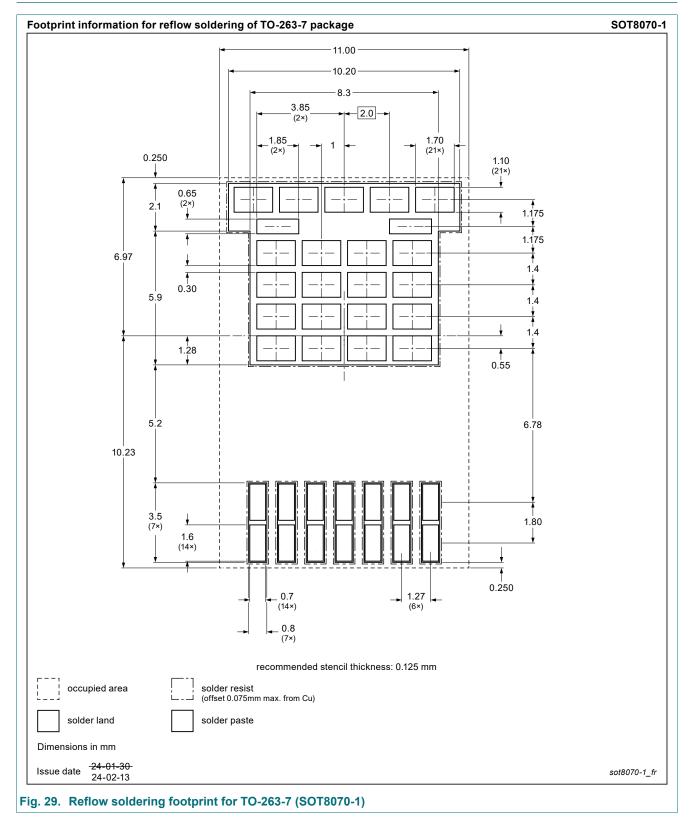
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### 12. Package outline



#### 1200 V, 40 mΩ, N-channel SiC MOSFET

### 13. Soldering



# 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NSF040120D7A0 v.3	20240514	Product data sheet	-	NSF040120D7A0 v.2
Modifications:	Product status ch	nanged		,
NSF040120D7A0 v.2	20240319	Preliminary data sheet	-	NSF040120D7A0 v.1
NSF040120D7A0 v.1	20240131	Objective data sheet	-	-

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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