

Rev. 1.0 — 3 July 2023

Preliminary data sheet

## 1. General description

The NGW50T60M3DF is a robust Insulated-Gate Bipolar Transistor (IGBT) featuring third-generation technology. It combines carrier stored trench-gate and field-stop (FS) structures. The NGW50T60M3DF is rated to 175 °C with optimized IGBT turn-off losses, and has a short circuit withstand time of 5  $\mu$ s. This hard-switching 600 V, 50 A IGBT is optimized for high-voltage, low-frequency industrial power inverter and servo motor drive applications.

### 2. Features and benefits

- Collector current (I<sub>C</sub>) rated at 50 A
- Low conduction and switching losses
- Stable and tight parameters for easy parallel operation
- Maximum junction temperature of 175 °C
- Fully rated as a soft fast reverse recovery diode
- 5 µs short circuit withstand time
- RoHS compliant, lead-free plating

### 3. Applications

- Motor drives for industrial and consumer appliances
  - Servo motors operating between 5-20 kW (up to 20 kHz) for robotics, elevators, operating grippers, in-line manufacturing, etc.
- Power inverters
  - Uninterruptible Power Supply (UPS) inverter
  - Photovoltaic (PV) strings
  - · EV charging
- Induction heating
- Welding

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CE</sub>	collector-emitter voltage	T <sub>j</sub> = 25 °C	-	600	V
Tj	operating junction temperature		-40	+175	°C
t <sub>sc</sub>	short circuit withstand time	$V_{GE} = 15 \text{ V}; V_{CC} = 400 \text{ V};$ $T_j \le 175 \text{ °C}$	-	5	μs



## 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	
2	С	collector		C 
3	Е	emitter		
mb	С	mounting base; connected to collector		G E aaa-036518

# 6. Ordering information

**Table 3. Ordering information** 

Type number	umber Package			
	Name	Description	Version	
NGW50T60M3DF	TO-247-3L	Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247-3L	SOT429-2	

# 7. Limiting values

#### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
IGBT					
$V_{CE}$	collector-emitter voltage	T <sub>j</sub> = 25 °C	-	600	V
I <sub>C</sub>	collector current	$T_{case} = 25 ^{\circ}C$ [1]	-	80	Α
		T <sub>case</sub> = 100 °C [1]	-	77	Α
I <sub>Cpuls</sub>	peak pulse collector current [2]		-	150	Α
t <sub>sc</sub>	short circuit withstand time	$V_{GE} = 15 \text{ V}; V_{CC} = 400 \text{ V};$ $T_j \le 150 \text{ °C}$	-	5.0	μS
$V_{GE}$	gate-emitter voltage		-20	+20	V
P <sub>tot</sub>	total power dissipation	T <sub>case</sub> = 25 °C	-	357	W
		T <sub>case</sub> = 100 °C	-	179	W
Tj	operating junction temperature		-40	+175	°C
T <sub>stg</sub>	storage temperature		-55	+150	°C
T <sub>solder</sub>	soldering temperature		-	260	°C
М	mounting torque, M3 screw		-	0.6	Nm

Symbol	Parameter	Conditions	Min	Max	Unit
Diode					
I <sub>F</sub>	diode forward current	$T_{case} = 25  ^{\circ}C$ [1]	-	80	Α
		$T_{case} = 100  ^{\circ}C$ [1]	-	65	Α
I <sub>Fpuls</sub>	peak pulse diode current [2]	T <sub>case</sub> = 25 °C		150	Α

### 8. Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-c)</sub>	thermal resistance from	IGBT	-	0.29	0.35	K/W
	junction to case	diode	-	0.48	0.57	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	-	40	K/W

## 9. Characteristics

#### **Table 6. Characteristics**

All values at  $T_i$  = 25 °C, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cl	naracteristics		_			
$V_{(BR)CE}$	collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}; I_{C} = 0.2 \text{ mA}$	600	-	-	V
V <sub>CEsat</sub>	collector-emitter saturation	V <sub>GE</sub> = 15 V; I <sub>C</sub> = 50 A; T <sub>j</sub> = 25 °C	-	1.4	1.8	V
	voltage	V <sub>GE</sub> = 15 V; I <sub>C</sub> = 50 A; T <sub>j</sub> = 175 °C	-	1.8	-	V
V <sub>F</sub>	diode forward voltage	V <sub>GE</sub> = 0 V; I <sub>F</sub> = 50 A; T <sub>j</sub> = 25 °C	-	1.5	2	V
		V <sub>GE</sub> = 0 V; I <sub>F</sub> = 50 A; T <sub>j</sub> = 175 °C	-	1.3	-	V
V <sub>GE(th)</sub>	gate-emitter threshold voltage	$I_C = 0.5 \text{ mA}; V_{CE} = V_{GE}; T_j = 25 \text{ °C}$	4	5.4	7	V
I <sub>CES</sub>	zero gate voltage collector current	V <sub>CE</sub> = 600 V; V <sub>GE</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	400	μΑ
		V <sub>CE</sub> = 600 V; V <sub>GE</sub> = 0 V; T <sub>j</sub> = 175 °C	-	-	10	mA
I <sub>GES</sub>	gate-emitter leakage current	V <sub>CE</sub> = 0 V; V <sub>GE</sub> = 20 V	-	-	200	nA
g <sub>fS</sub>	transconductance	V <sub>CE</sub> = 20 V; I <sub>C</sub> = 50 A; T <sub>j</sub> = 25 °C	-	22.6	-	S
r <sub>G</sub>	integrated gate resistor		-	0.8	-	Ω
Dynami	c characteristics		'			
C <sub>ies</sub>	input capacitance	V <sub>CE</sub> = 25 V; V <sub>GE</sub> = 0 V; f = 1 MHz	-	5527	-	pF
C <sub>oes</sub>	output capacitance		-	212	-	pF
C <sub>res</sub>	reverse transfer capacitance		-	44	-	pF
$Q_G$	gate charge	V <sub>CC</sub> = 480 V; V <sub>GE</sub> = 15 V; I <sub>C</sub> = 50 A	-	213	-	nC
L <sub>sCE</sub>	internal stray conductance	Measured 5 mm from case	-	7.9	-	nΗ
I <sub>C(sc)</sub>	short circuit collector current	$V_{GE}$ = 15 V; $V_{CC}$ = 400 V; $t_{sc} \le 5 \mu s$ ; $T_j \le 150  ^{\circ}C$	-	190	-	Α

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
IGBT sw	vitching characteristics, inductiv	e load					
t <sub>d(on)</sub>	turn-on delay time	V <sub>GE</sub> = 15/0 V;	T <sub>j</sub> = 25 °C	-	58	-	ns
		$V_{CC}$ = 400 V; $I_{C}$ = 50 A; $r_{G(on)}$ = 10 Ω; $r_{G(off)}$ = 10 Ω;	T <sub>j</sub> = 175 °C	-	55	-	ns
t <sub>r</sub>	rise time	see <u>Fig. 26</u> and <u>Fig. 27</u>	T <sub>j</sub> = 25 °C	-	41	-	ns
			T <sub>j</sub> = 175 °C	-	42	-	ns
t <sub>d(off)</sub>	turn-off delay time		T <sub>j</sub> = 25 °C	-	243	-	ns
			T <sub>j</sub> = 175 °C	-	299	-	ns
t <sub>f</sub>	fall time		T <sub>j</sub> = 25 °C	-	19	-	ns
			T <sub>j</sub> = 175 °C	-	58	-	ns
E <sub>on</sub> turn-on switching loss	turn-on switching loss		T <sub>j</sub> = 25 °C	-	1.7	-	mJ
			T <sub>j</sub> = 175 °C	-	1.85	-	mJ
E <sub>off</sub>	turn-off switching loss		T <sub>j</sub> = 25 °C	-	0.84	-	mJ
			T <sub>j</sub> = 175 °C	-	1.33	-	mJ
E <sub>ts</sub>	total switching loss		T <sub>j</sub> = 25 °C	-	2.54	-	mJ
			T <sub>j</sub> = 175 °C	-	3.18	-	mJ
Diode sv	witching characteristics, inductive	ve load					
t <sub>rr</sub>	diode reverse recovery time	$V_R = 400 \text{ V}; I_F = 50 \text{ A};$	T <sub>j</sub> = 25 °C	-	180	-	ns
		$\Delta I_F/\Delta t = 500 \text{ A/}\mu\text{s}$ ; see Fig. 25	T <sub>j</sub> = 175 °C	-	630	-	ns
Q <sub>rr</sub>	diode reverse recovery charge		T <sub>j</sub> = 25 °C	-	520	-	nC
			T <sub>j</sub> = 175 °C	-	4180	-	nC
I <sub>rrm</sub>	diode peak reverse recovery		T <sub>j</sub> = 25 °C	-	9	-	Α
	current		T <sub>j</sub> = 175 °C	-	18	-	Α
E <sub>rr</sub>	reverse recovery energy		T <sub>j</sub> = 25 °C	=	0.01	-	mJ
			T <sub>j</sub> = 175 °C	-	0.14	-	mJ
di <sub>rr</sub> /dt	diode peak rate or fall of reverse		T <sub>j</sub> = 25 °C	=	270	-	A/µs
	recovery current		T <sub>j</sub> = 175 °C	-	200	-	A/µs

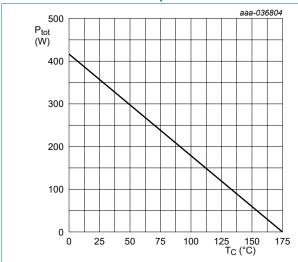
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#### 600 V, 50 A trench field-stop IGBT with full rated silicon diode

### 9.1. Waveforms and output characteristics

100

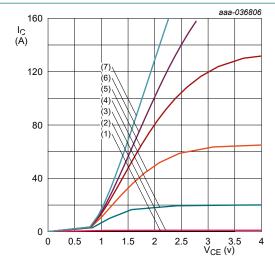
Table 7. Waveforms and output characteristics

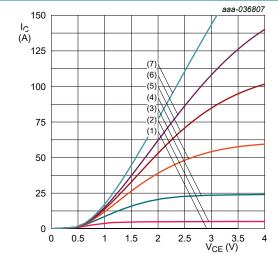


80 40 20 0 25 50 75 100 125 150 175 200 Tc (°C)

Fig. 1. Power dissipation (P<sub>tot</sub>) as a function of case temperature

Fig. 2. Collector current  $(I_C)$  as a function of case temperature





$$\begin{split} T_{j} &= 25 \text{ °C} \\ &(1) \text{ V}_{GE} = 5 \text{ V} \\ &(2) \text{ V}_{GE} = 7 \text{ V} \\ &(3) \text{ V}_{GE} = 9 \text{ V} \\ &(4) \text{ V}_{GE} = 11 \text{ V} \\ &(5) \text{ V}_{GE} = 13 \text{ V} \\ &(6) \text{ V}_{GE} = 15 \text{ V} \\ &(7) \text{ V}_{GE} = 20 \text{ V} \end{split}$$

$$\begin{split} T_{j} &= 175 \text{ °C} \\ &(1) \text{ V}_{GE} = 5\text{V} \\ &(2) \text{ V}_{GE} = 7\text{ V} \\ &(3) \text{ V}_{GE} = 9\text{ V} \\ &(4) \text{ V}_{GE} = 11\text{ V} \\ &(5) \text{ V}_{GE} = 13\text{ V} \\ &(6) \text{ V}_{GE} = 15\text{ V} \\ &(7) \text{ V}_{GE} = 20\text{ V} \end{split}$$

Fig. 3. Collector current as a function of collectoremitter voltage

Fig. 4. Collector current as a function of collector-emitter voltage

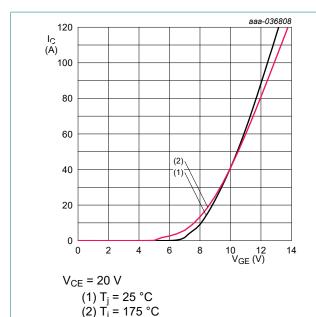


Fig. 5. Collector current as a function of gate-emitter voltage; typical values

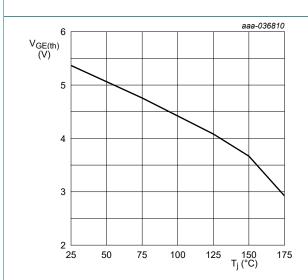
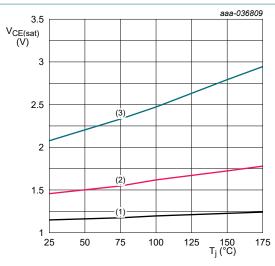
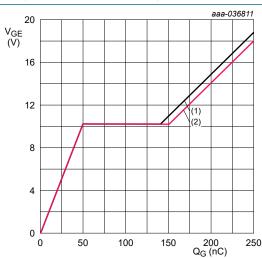


Fig. 7. Gate-emitter threshold voltage as a function of junction temperature



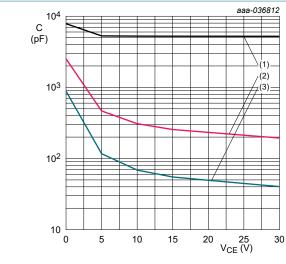
$$V_{GE} = 15 \text{ V}$$
(1)  $I_{C} = 25 \text{ A}$ 
(2)  $I_{C} = 50 \text{ A}$ 
(3)  $I_{C} = 100 \text{ A}$ 

Fig. 6. Collector-emitter saturation voltage as a function of junction temperature; typical values



 $I_C$  = 50 A (1)  $V_{CE}$  = 120 V (1)  $V_{CE}$  = 480 V

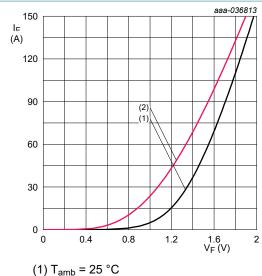
Fig. 8. Gate-emitter voltage as a function of gate charge; typical values



$$V_{GE} = 0 V, f = 1 MHz$$

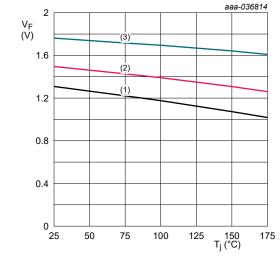
- (1) C<sub>ies</sub>
- (2) Coes
- (3) C<sub>res</sub>

Typical capacitance as a function of Fig. 9. collector-emitter voltage



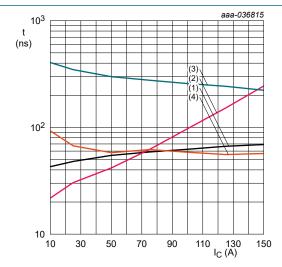
- (2)  $T_{amb}$  = 175 °C

Fig. 10. Typical diode forward current as function of forward voltage



- (1)  $I_F = 25 A$
- (2)  $I_F = 50 A$
- $(3) I_F = 100 A$

Fig. 11. Typical diode forward voltage as a function of junction temperature



 $V_{GE}$  = 15 V to 0 V;  $V_{CC}$  = 400 V;  $r_{G(on)}$  = 10  $\Omega;$ 

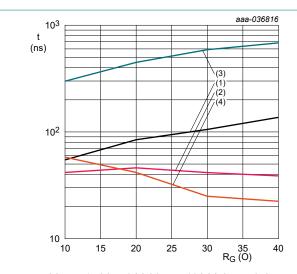
- $r_{G(off)} = 10 \Omega$ 
  - (1) t<sub>d(on)</sub>
  - $(2) t_r$
  - $(3) t_{d(off)}$  $(4) t_f$

Fig. 12. Typical switching times as a function of collector current

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175

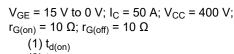
#### 600 V, 50 A trench field-stop IGBT with full rated silicon diode



$$V_{GE} = 15 \text{ V to 0 V; } V_{CC} = 400 \text{ V; } I_{C} = 50 \text{ A;}$$

T<sub>i</sub> = 175 °C

- (1)  $t_{d(on)}$
- $(2) t_{r}$
- $(3) t_{d(off)}$
- $(4) t_f$



75

 $\frac{4}{4}(4)$ 

100

50

10<sup>3</sup>

10<sup>2</sup>

10

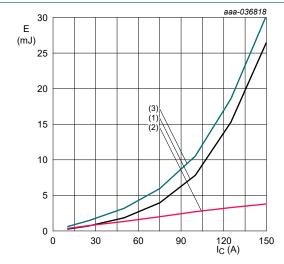
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(ns)

- $(2) t_{r}$
- $(3) t_{d(off)}$
- $(4) t_f$

Fig. 13. Typical switching times as a function of gate resistor

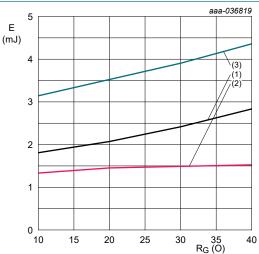




 $V_{GE}$  = 15 V to 0 V;  $V_{CC}$  = 400 V;  $r_{G(on)}$  = 10  $\Omega;$  $r_{G(off)} = 10 \Omega; T_i = 175 °C$ 

- (1) E<sub>on</sub>
- (2) E<sub>off</sub>
- (3) E<sub>ts</sub>

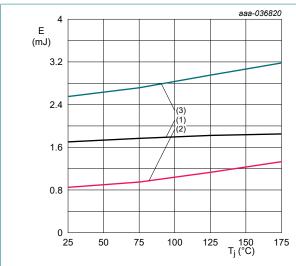
Fig. 15. Typical switching energy losses as a function of collecor current



 $V_{GE}$  = 15 V to 0 V;  $V_{CC}$  = 400 V;  $I_{C}$  = 50 A;  $T_{i}$  = 175 °C

- (1) E<sub>on</sub>
- (2) E<sub>off</sub>
- $(3) E_{ts}$

Fig. 16. Typical switching energy losses as a function of gate resistance

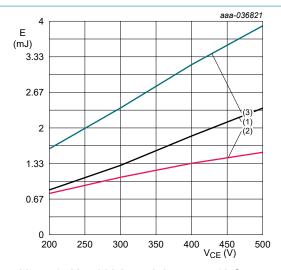


 $V_{GE} = 15 \text{ V to } 0 \text{ V}; I_{C} = 50 \text{ A}; V_{CC} = 400 \text{ V};$  $r_{G(on)}$  = 10  $\Omega$ ;  $r_{G(off)}$  = 10  $\Omega$ 

(1) E<sub>on</sub>

(2) E<sub>off</sub>

(3) E<sub>ts</sub>



 $V_{GE}$  = 15 V to 0 V;  $I_{C}$  = 50 A;  $r_{G(on)}$  = 10  $\Omega$ ;

 $r_{G(off)} = 10 \Omega; T_j = 175 °C$ 

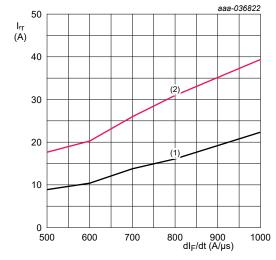
(1) E<sub>on</sub>

(2) E<sub>off</sub>

(3) E<sub>ts</sub>

of junction temperature

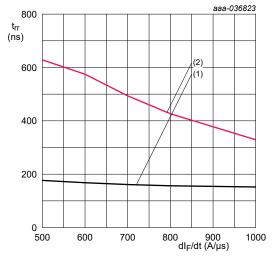
Fig. 17. Typical switching energy losses as a function | Fig. 18. Typical switching energy losses as a function of collector-emitter voltage



 $V_R = 400 \text{ V}; I_F = 50 \text{ A}$ 

(1)  $T_{amb} = 25 \, ^{\circ}C$ 

(2)  $T_{amb} = 175 \, ^{\circ}C$ 



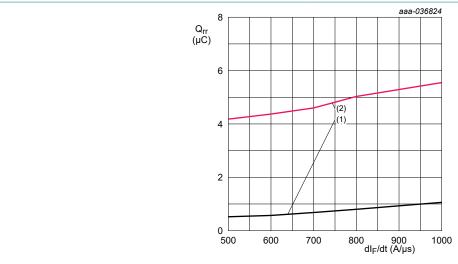
 $V_R = 400 \text{ V}; I_F = 50 \text{ A}$ 

(1)  $T_{amb} = 25 \, ^{\circ}C$ 

(2)  $T_{amb} = 175 \, ^{\circ}C$ 

Fig. 19. Typical reverse recovery current as a function of change of forward current

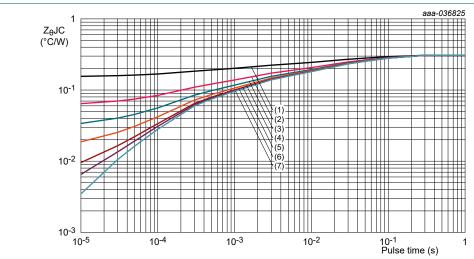
Fig. 20. Typical reverse recovery time as a function of change of forward current



 $V_R = 400 \text{ V}; I_F = 50 \text{ A}$ 

(1) T<sub>amb</sub> = 25 °C (2) T<sub>amb</sub> = 175 °C

Fig. 21. Typical reverse recovery charge as a function of rate of change of forward current



(1)  $Z_{th}$  (D = 0.5)

(2)  $Z_{th}$  (D = 0.2)

(3)  $Z_{th}$  (D = 0.1)

(4)  $Z_{th}$  (D = 0.05)

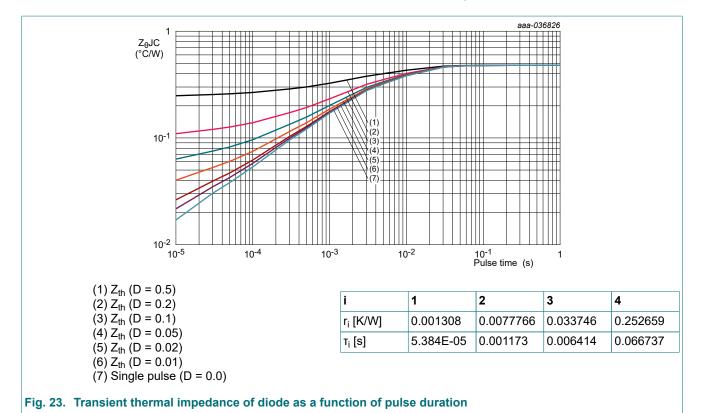
(5)  $Z_{th}$  (D = 0.02)

(6)  $Z_{th}$  (D = 0.01)

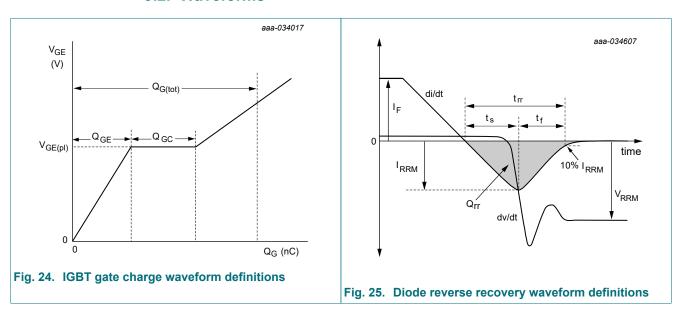
(7) Single pulse (D = 0.0)

1 2 3 4 0.111922 r<sub>i</sub> [K/W] 0.046192 0.071159 0.078864 T<sub>i</sub> [s] 0.000162 0.001166 0.010189 0.071390

Fig. 22. Transient thermal impedance of IGBT as a function of pulse duration



#### 9.2. Waveforms



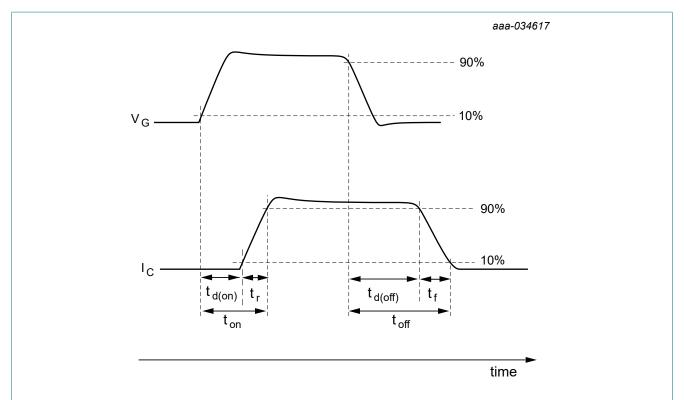
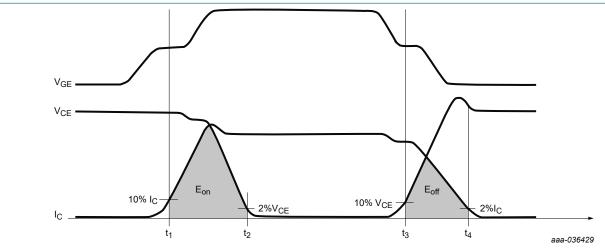


Fig. 26. IGBT switching times definitions



$$E_{\text{on}} = {}^{t_2}_{t_1} V_{\text{CE}} I_C dt$$

$$E_{\text{on}} = {}^{t_2}_{t_1} V_{\text{CE}} I_C dt$$

$$E_{\text{off}} = {}^{t_4}_{t_3} V_{\text{CE}} I_C dt$$

Fig. 27. IGBT switching loss definitions

## 10. Package outline

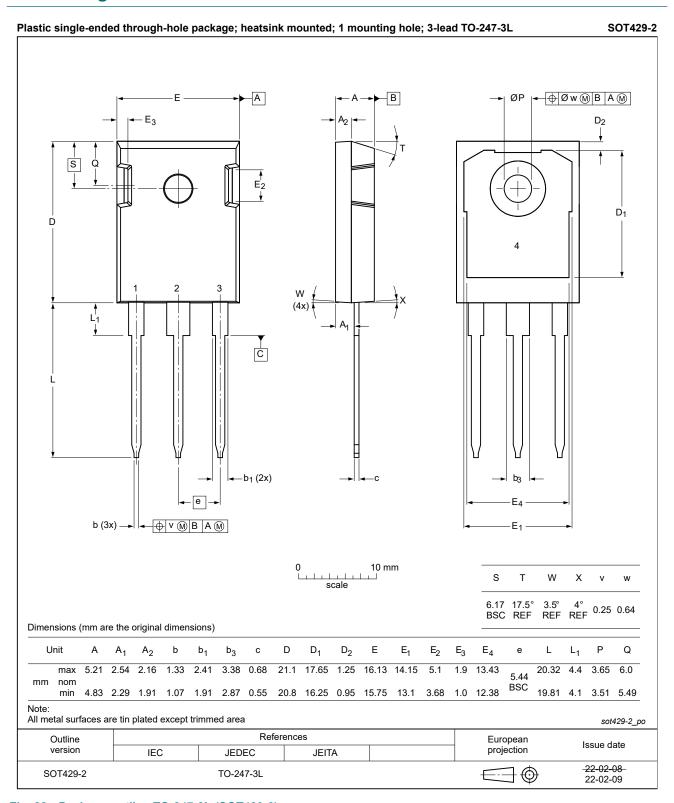


Fig. 28. Package outline TO-247-3L (SOT429-2)

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# 11. Revision history

#### **Table 8. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
NGW50T60M3DF v. 1.0	20230703	Preliminary data sheet	-	-

## 12. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <a href="https://www.nexperia.com">https://www.nexperia.com</a>.

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