



MMBZ33VCT-Q

Low capacitance unidirectional double ESD protection diode

6 March 2024

Product data sheet

1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a common cathode configuration, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package. The device is designed for ESD and transient overvoltage protection of up to two signal lines.

2. Features and benefits

- Unidirectional ESD protection of two lines
- Bidirectional ESD protection of one line
- Very low diode capacitance: $C_d \leq 25$ pF
- Reverse stand-off voltage: $V_{RWM} = 26$ V
- Low clamping voltage: $V_{CL} = 49$ V typ. at $I_{PP} = 2.8$ A
- ESD protection up to 20 kV (IEC 61000-4-2)
- Ultra low leakage current: $I_{RM} = 1$ nA typ.
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Automotive electronic control units
- Portable electronics

4. Quick reference data

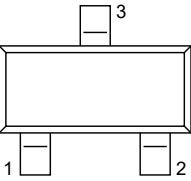
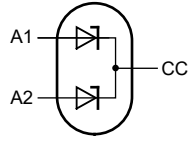
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25$ °C		-	-	26	V
I_{PPM}	rated peak pulse current	$t_p = 8/20$ μ s	[1]	-	-	2.8	A
C_d	diode capacitance	$f = 1$ MHz; $V_R = 0$ V; $T_{amb} = 25$ °C		-	20	25	pF

[1] Device stressed with 8/20 μ s exponential decay waveform according to IEC 61000-4-5

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	 <p style="text-align: center;">SOT23</p>	 <p style="text-align: center;"><i>sym002</i></p>
2	A2	anode (diode 2)		
3	CC	common cathode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
MMBZ33VCT-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
MMBZ33VCT-Q	%1Z

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I_{PPM}	rated peak pulse current	$t_p = 8/20 \mu s$	[1]	-	2.8	A
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V_{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2]	-	20	kV
		IEC 61000-4-2; air discharge	[2]	-	20	kV

- [1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5
- [2] Device stressed with ten non-repetitive ESD pulses

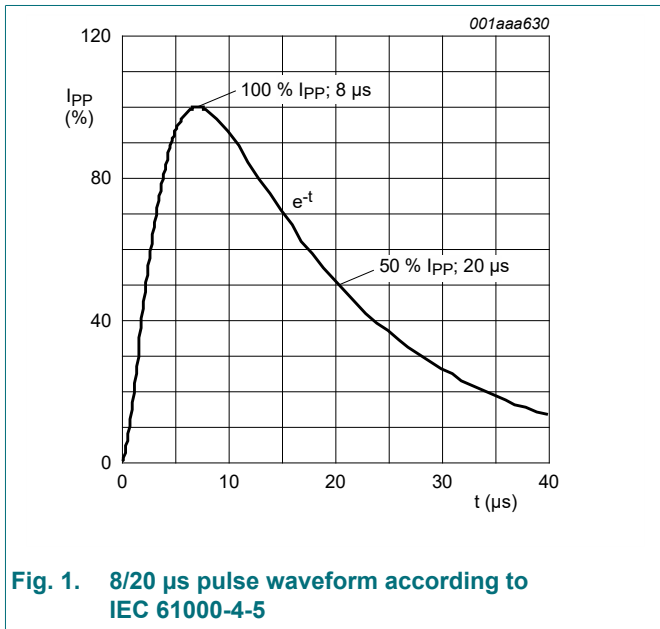


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5

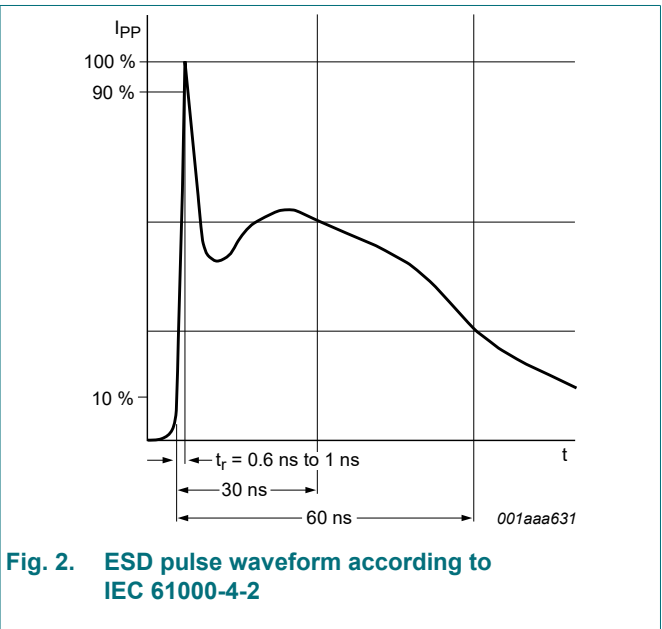


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25\text{ }^{\circ}\text{C}$	-	-	26	V
V_{BR}	breakdown voltage	$I_R = 1\text{ mA}; T_{amb} = 25\text{ }^{\circ}\text{C}$	31.3	33	34.7	V
I_{RM}	reverse leakage current	$V_R = 26\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	1	50	nA
C_d	diode capacitance	$f = 1\text{ MHz}; V_R = 0\text{ V}; T_{amb} = 25\text{ }^{\circ}\text{C}$	-	20	25	pF
V_{CL}	clamping voltage	$I_{PPM} = 2.8\text{ A}; t_p = 8/20\text{ }\mu\text{s}; T_{amb} = 25\text{ }^{\circ}\text{C}$ [1]	-	49	-	V

[1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5

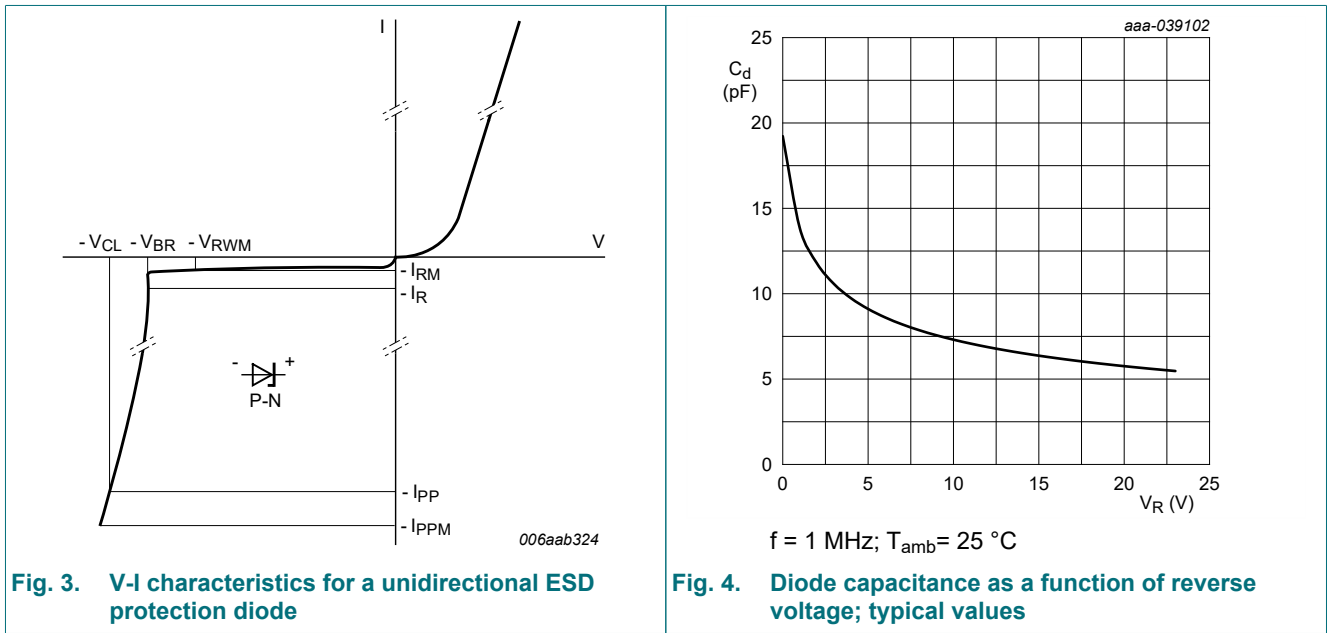


Fig. 3. V-I characteristics for a unidirectional ESD protection diode

Fig. 4. Diode capacitance as a function of reverse voltage; typical values

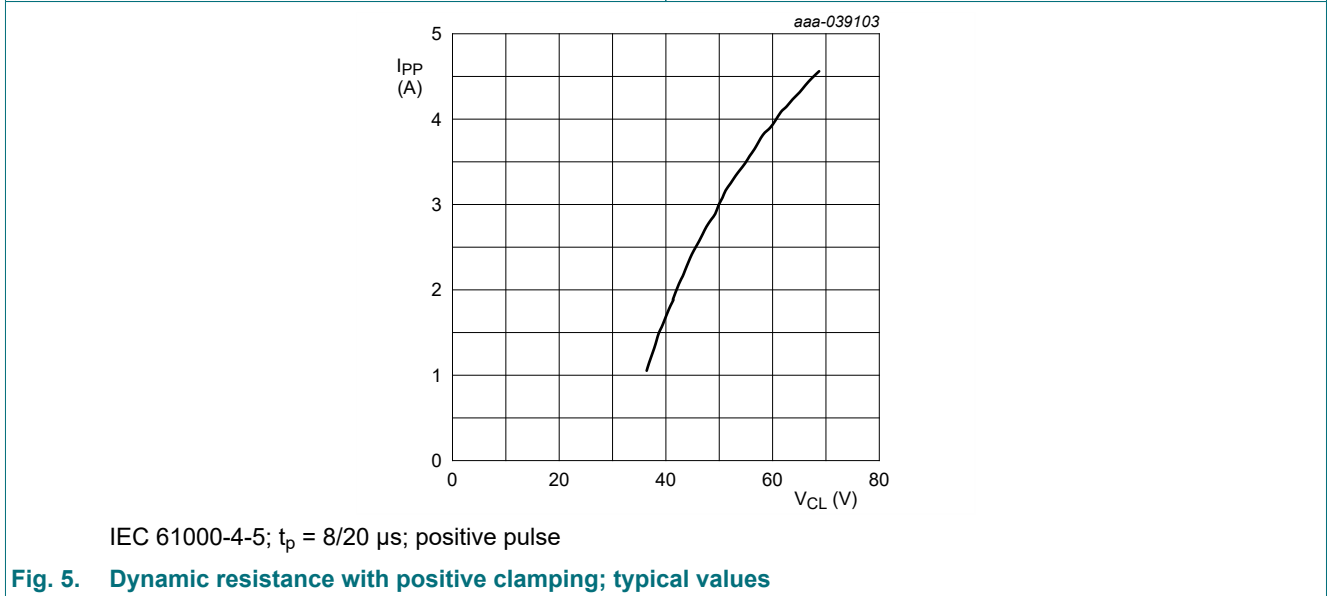


Fig. 5. Dynamic resistance with positive clamping; typical values

10. Application information

The device can protect up to two lines against damage caused by unidirectional ElectroStatic Discharge (ESD) and surge pulses. The device can protect lines whose signal polarities are below ground.

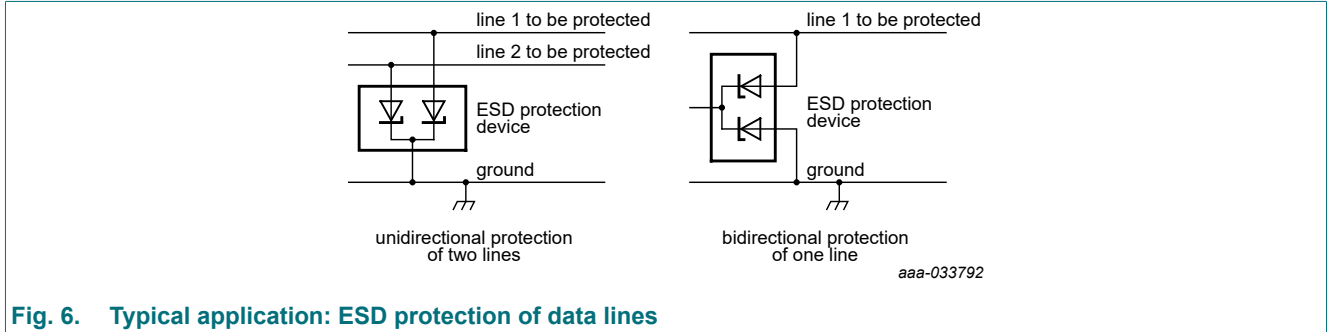


Fig. 6. Typical application: ESD protection of data lines

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

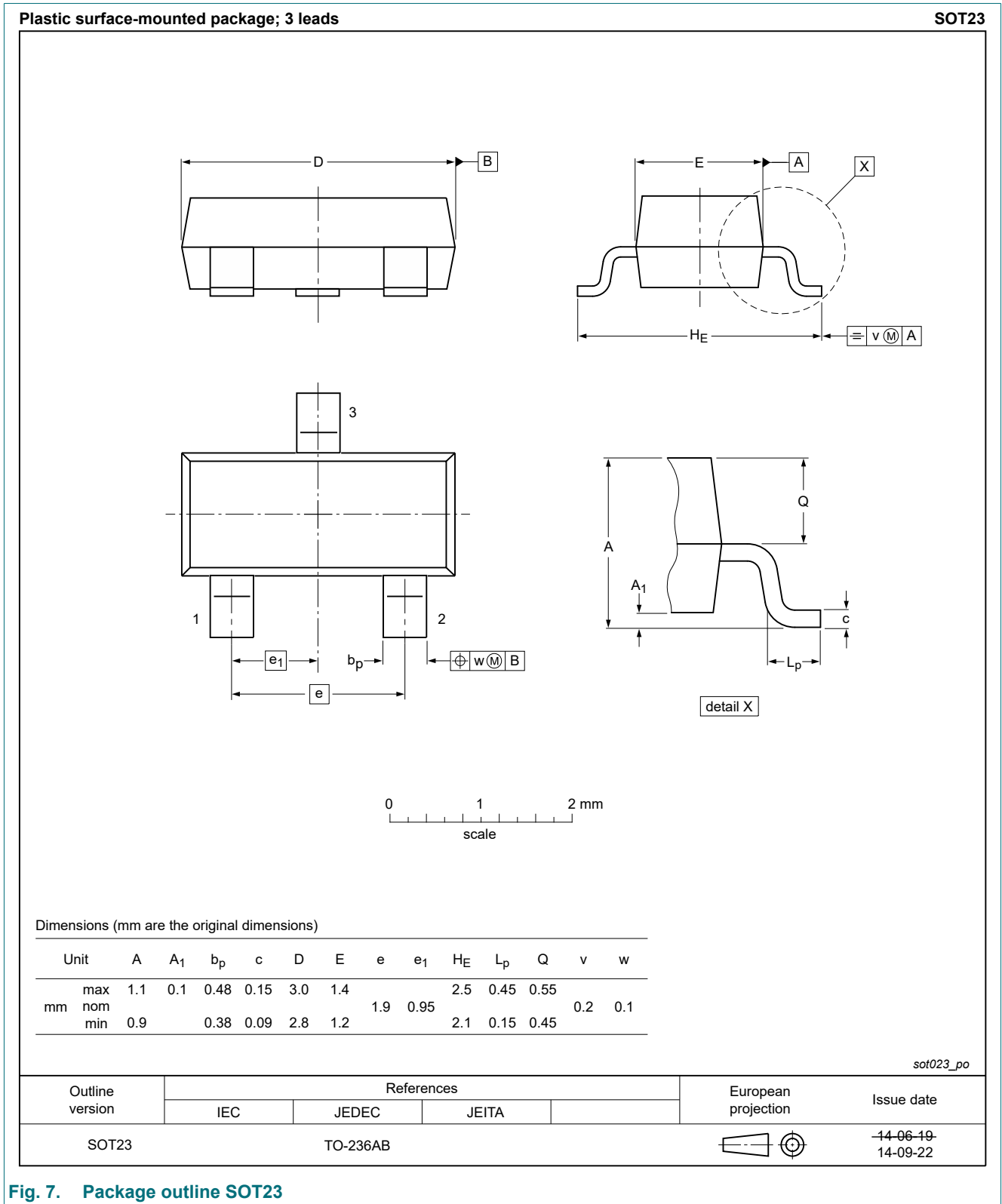


Fig. 7. Package outline SOT23

13. Soldering

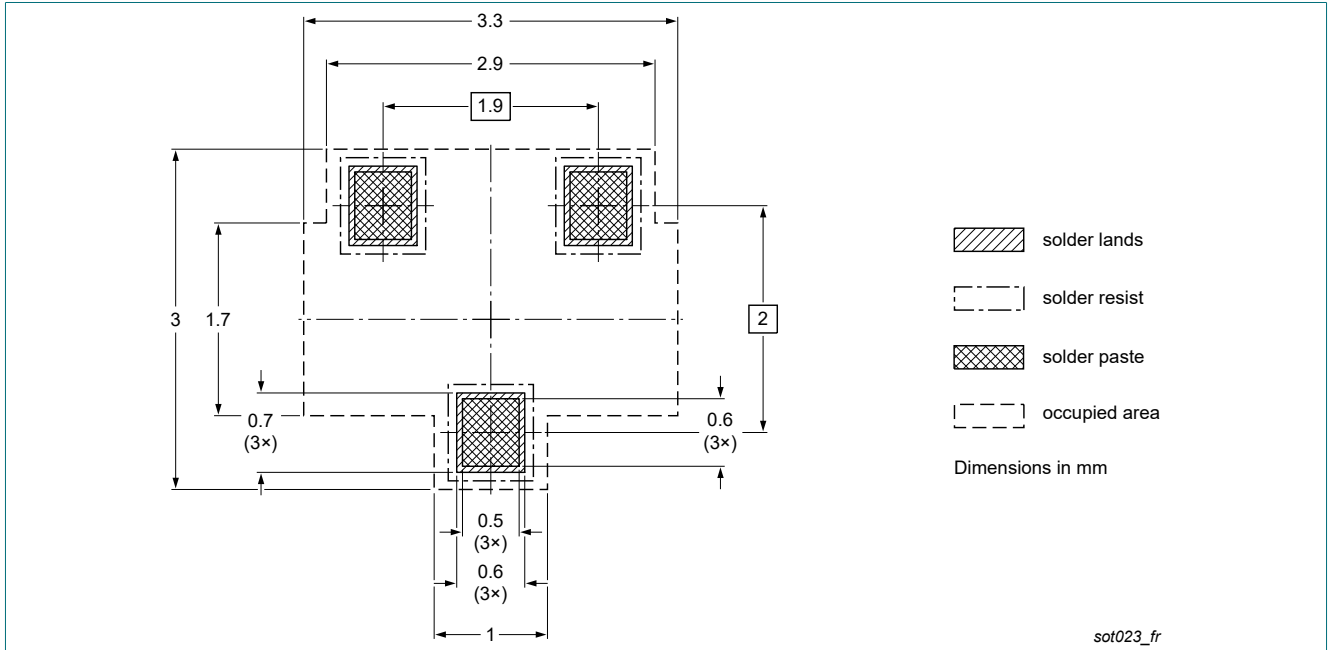


Fig. 8. Reflow soldering footprint for SOT23

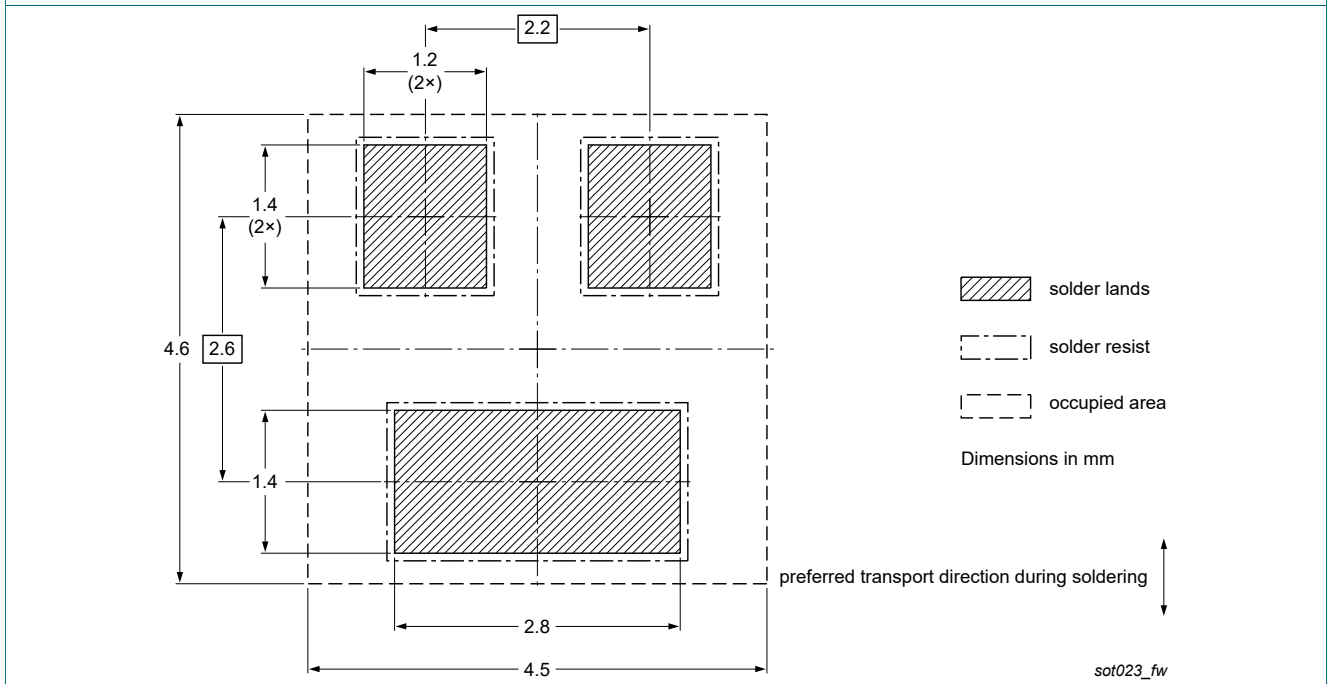


Fig. 9. Wave soldering footprint for SOT23

14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBZ33VCT-Q v.1	20240306	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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