

MMBZ20VCL

Double ESD protection diode for transient overvoltage suppression

17 August 2023

Product data sheet

1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a common cathode configuration, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package. The device is designed for ESD and transient overvoltage protection of up to two signal lines.

2. Features and benefits

- · Unidirectional ESD protection of two lines
- Bidirectional ESD protection of one line
- Low diode capacitance: C_d ≤ 80 pF
- Rated peak pulse power: P_{PPM} ≤ 40 W
- Ultra low leakage current: I_{RM} ≤ 5 nA
- ESD protection up to 30 kV (contact discharge)
- IEC 61000-4-2; level 4 (ESD)
- AEC-Q101 qualified

3. Applications

- · Computers and peripherals
- Automotive electronic control units
- Audio and video equipment
- Portable electronics
- · Cellular handsets and accessories

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|--------------------------|---|-----|-----|-----|------|
| V _{RWM} | reverse standoff voltage | T _{amb} = 25 °C | - | - | 17 | V |
| C _d | diode capacitance | f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C | - | 65 | 80 | pF |



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5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-----------------|--------------------|------------------|
| 1 | A1 | anode (diode 1) | 3 | 3 |
| 2 | A2 | anode (diode 2) | | |
| 3 | CC | common cathode | SOT23 | 1 2 006aaa150 |

6. Ordering information

Table 3. Ordering information

| Type number | Package | | | | |
|-------------|---------|--|---------|--|--|
| | Name | Description | Version | | |
| MMBZ20VCL | SOT23 | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23 | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| MMBZ20VCL | %MD |

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134)

| Parameter | Conditions | | Min | Max | Unit |
|---------------------------------|--|--|--|---|---|
| rated peak pulse power | t _p = 10/1000 μs | [1] [2] | - | 40 | W |
| rated peak pulse current | | [1] [2] | - | 1.4 | Α |
| total power dissipation | T _{amb} ≤ 25 °C | [3] | - | 350 | mW |
| | | [4] | - | 440 | mW |
| junction temperature | | | - | 150 | °C |
| ambient temperature | | | -55 | 150 | °C |
| storage temperature | | | -65 | 150 | °C |
| ratings | | | 1 | | |
| electrostatic discharge voltage | IEC 61000-4-2; contact discharge; T _{amb} = 25 °C | [5] [2] | - | 30 | kV |
| | IEC 61000-4-2; air discharge | | - | 15 | kV |
| | MIL-STD-883; human body model (HBM) | | - | 8 | kV |
| | rated peak pulse power rated peak pulse current total power dissipation junction temperature ambient temperature storage temperature ratings electrostatic discharge | rated peak pulse power rated peak pulse current total power dissipation $T_{amb} \le 25 ^{\circ}\text{C}$ junction temperature ambient temperature storage temperature ratings electrostatic discharge voltage $T_{amb} = 25 ^{\circ}\text{C}$ IEC 61000-4-2; contact discharge; $T_{amb} = 25 ^{\circ}\text{C}$ | rated peak pulse power $t_p = 10/1000 \mu s$ [1] [2] rated peak pulse current total power dissipation $T_{amb} \le 25 ^{\circ}C$ [3] [4] junction temperature ambient temperature storage temperature $T_{amb} = 25 ^{\circ}C$ [5] [2] rated peak pulse current $T_{amb} \le 25 ^{\circ}C$ [5] [2] rated peak pulse power $T_{amb} \le 25 ^{\circ}C$ [5] [2] rated peak power $T_{amb} \le 25 ^{\circ}C$ [5] [2] rated peak peak power $T_{amb} \le 25 ^{\circ}C$ [5] [2] rated peak peak peak peak peak peak peak peak | rated peak pulse power rated peak pulse current $t_p = 10/1000 \mu s$ $t_p = 10/1000 $ | rated peak pulse power rated peak pulse current $t_p = 10/1000 \mu s$ [1] [2] - 40 rated peak pulse current [1] [2] - 1.4 total power dissipation $T_{amb} \le 25 ^{\circ}C$ [3] - 350 [4] - 440 [4] - 440 junction temperature - 150 ambient temperature -55 150 storage temperature -65 150 ratings IEC 61000-4-2; contact discharge; $T_{amb} = 25 ^{\circ}C$ [5] [2] - 30 lectrostatic discharge voltage IEC 61000-4-2; air discharge - 15 |

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- 4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [5] Device stressed with ten non-repetitive ESD pulses.

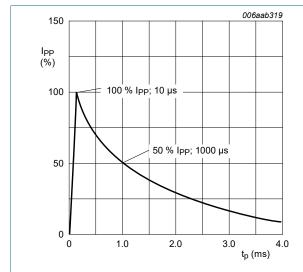


Fig. 1. 10/1000 µs pulse waveform according to IEC 61643-321

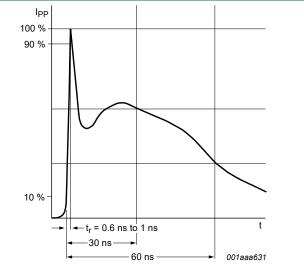


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

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9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|----------------------|--|------------|-----|-----|-----|-----|------|
| R _{th(j-a)} | thermal resistance from junction to ambient | | [1] | - | - | 350 | K/W |
| | | | [2] | - | - | 280 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | [3] | - | - | 60 | K/W |

- 1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Soldering point at pin 3.

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|-----------------|--------------------------|---|---------|-----|------|-----|------|
| V _F | forward voltage | I _F = 10 mA; T _{amb} = 25 °C | | - | - | 0.9 | V |
| V_{RWM} | reverse standoff voltage | T _{amb} = 25 °C | | - | - | 17 | V |
| V _{BR} | breakdown voltage | I _R = 1 mA; T _{amb} = 25 °C | | 19 | 20 | 21 | V |
| I _{RM} | reverse leakage current | V _{RWM} = 17 V; T _{amb} = 25 °C | | - | 0.1 | 5 | nA |
| C _d | diode capacitance | $f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$ | | - | 65 | 80 | pF |
| V _{CL} | clamping voltage | I_{PPM} = 1.4 A; t_p = 10/1000 µs; T_{amb} = 25 °C | [1] [2] | - | - | 28 | V |
| S _Z | temperature coefficient | I _Z = 1 mA | | - | 15.8 | - | mV/K |

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.

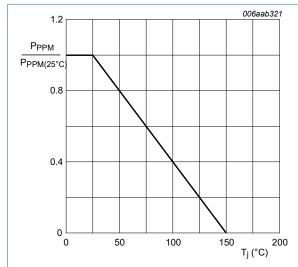


Fig. 3. Relative variation of rated peak pulse power as a function of junction temperature; typical values

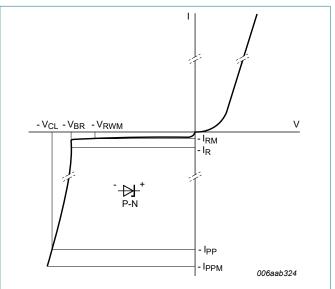


Fig. 4. V-I characteristics for a unidirectional ESD protection diode

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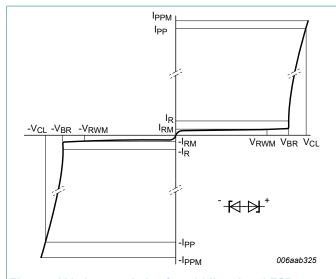


Fig. 5. V-I characteristics for a bidirectional ESD protection diode

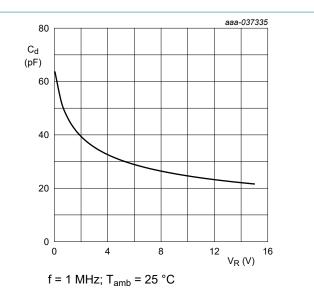


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

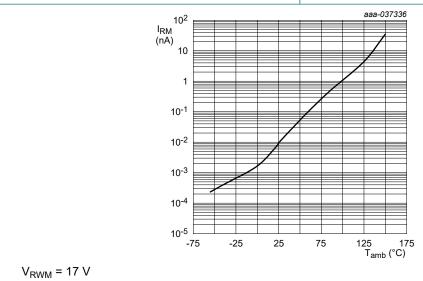
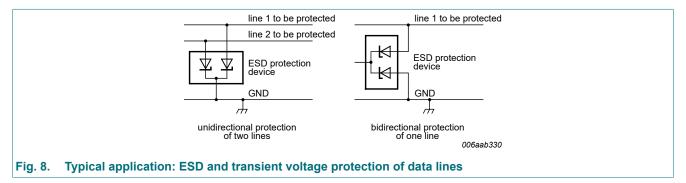


Fig. 7. Reverse leakage current as a function of ambient temperature; typical values

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11. Application information

The device is designed for the protection of up to two unidirectional data lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are either positive or negative with respect to ground.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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12. Package outline

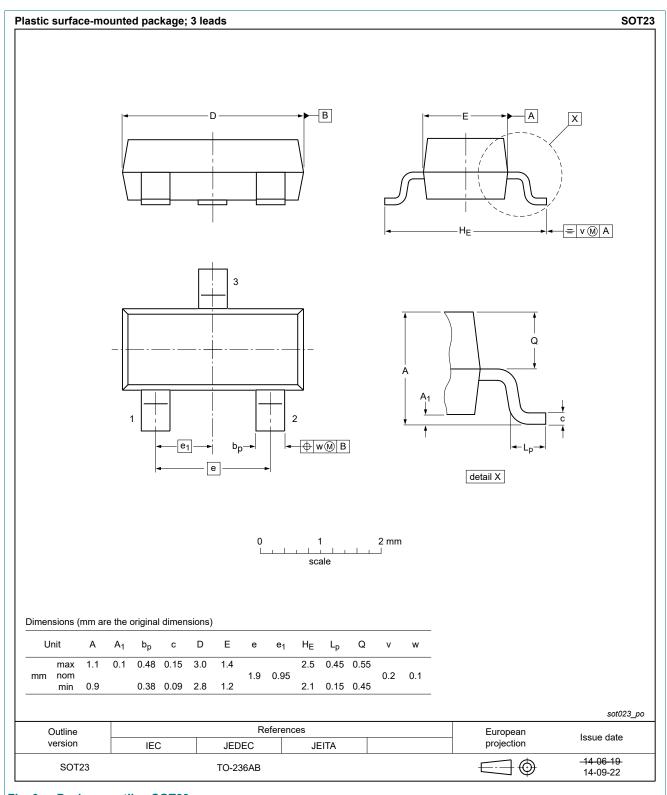
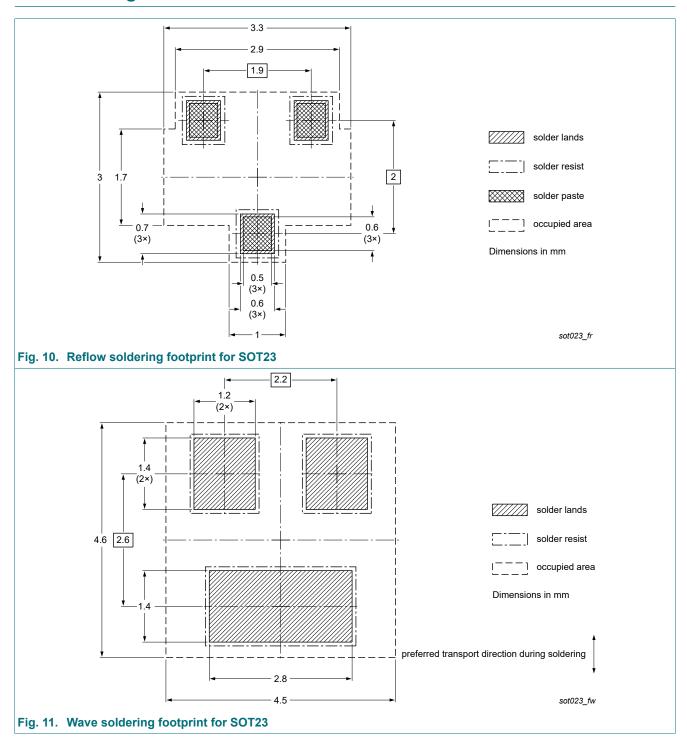


Fig. 9. Package outline SOT23

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13. Soldering



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14. Revision history

Table 8. Revision history

| Table 6. Revision mate | <u>''</u> y | | | | | | |
|------------------------------------|--|--------------------|---------------|------------------------------------|--|--|--|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| MMBZ20VCL v.2 | 20230817 | Product data sheet | - | MMBZXVCL_ MMBZXVDL_SER_1 v.1 | | | |
| Modifications: | The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia Legal texts have been adapted to the new company name where appropriate Family data sheet reduced to single type data sheet | | | | | | |
| MMBZXVCL_ MMBZXVDL_SER_1 v.1 | 20080903 | Product data sheet | - | - | | | |

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15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- Please consult the most recently issued document before initiating or completing a design.
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