



MMBZ18VCL

Double ESD protection diode for transient overvoltage suppression

17 August 2023

Product data sheet

1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a common cathode configuration, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package. The device is designed for ESD and transient overvoltage protection of up to two signal lines.

2. Features and benefits

- Unidirectional ESD protection of two lines
- Bidirectional ESD protection of one line
- Low diode capacitance: $C_d \leq 90$ pF
- Rated peak pulse power: $P_{PPM} \leq 40$ W
- Ultra low leakage current: $I_{RM} \leq 5$ nA
- ESD protection up to 30 kV (contact discharge)
- IEC 61000-4-2; level 4 (ESD)
- AEC-Q101 qualified

3. Applications

- Computers and peripherals
- Automotive electronic control units
- Audio and video equipment
- Portable electronics
- Cellular handsets and accessories

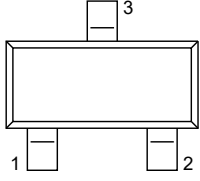
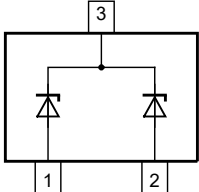
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25$ °C	-	-	14.5	V
C_d	diode capacitance	$f = 1$ MHz; $V_R = 0$ V; $T_{amb} = 25$ °C	-	70	90	pF

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	 <p>SOT23</p>	 <p>006aaa150</p>
2	A2	anode (diode 2)		
3	CC	common cathode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
MMBZ18VCL	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
MMBZ18VCL	%MC

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

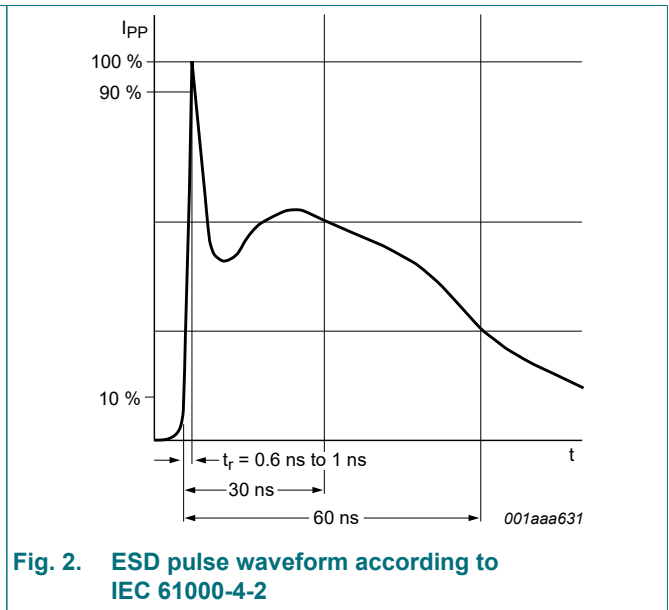
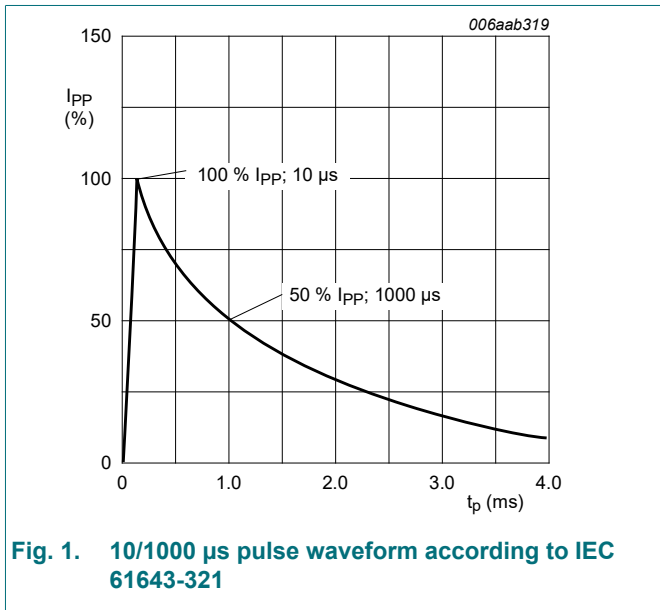
In accordance with the Absolute Maximum Rating System (IEC 60134)

Symbol	Parameter	Conditions		Min	Max	Unit
P_{PPM}	rated peak pulse power	$t_p = 10/1000 \mu s$	[1] [2]	-	40	W
I_{PPM}	rated peak pulse current		[1] [2]	-	1.6	A
P_{tot}	total power dissipation	$T_{amb} \leq 25 \text{ }^\circ\text{C}$	[3]	-	350	mW
			[4]	-	440	mW
T_j	junction temperature			-	150	$^\circ\text{C}$
T_{amb}	ambient temperature			-55	150	$^\circ\text{C}$
T_{stg}	storage temperature			-65	150	$^\circ\text{C}$

Double ESD protection diode for transient overvoltage suppression

Symbol	Parameter	Conditions		Min	Max	Unit
ESD maximum ratings						
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge; T _{amb} = 25 °C	[5] [2]	-	30	kV
		IEC 61000-4-2; air discharge		-	15	kV
		MIL-STD-883; human body model (HBM)		-	8	kV

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [5] Device stressed with ten non-repetitive ESD pulses.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	[1]	-	-	350	K/W
		[2]	-	-	280	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point	[3]	-	-	60	K/W

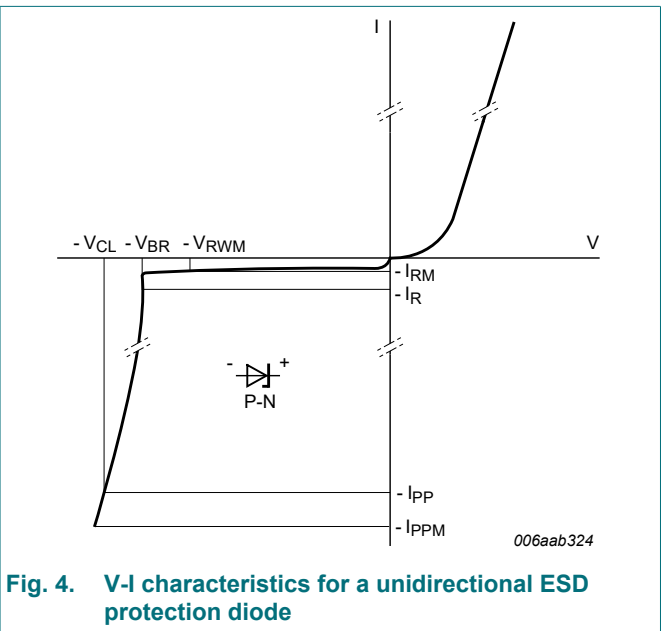
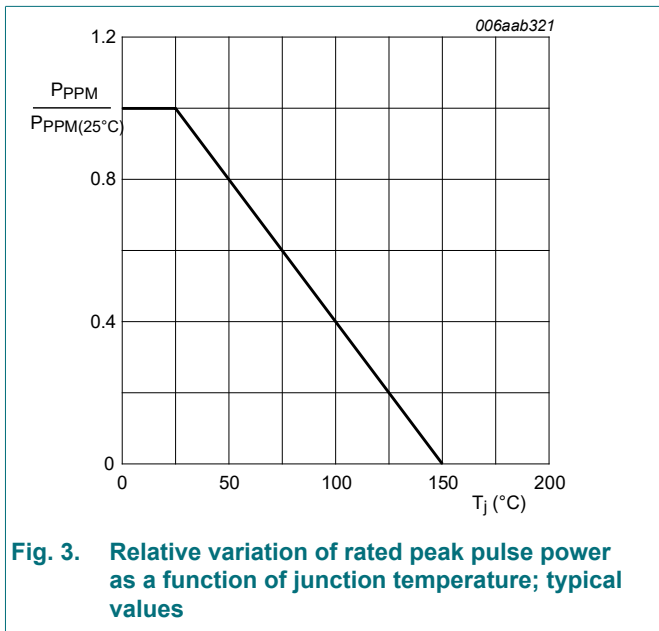
- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Soldering point at pin 3.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 10 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	0.9	V
V_{RWM}	reverse standoff voltage	$T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	14.5	V
V_{BR}	breakdown voltage	$I_R = 1 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	17.1	18	18.9	V
I_{RM}	reverse leakage current	$V_{RWM} = 14.5 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.1	5	nA
C_d	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	70	90	pF
V_{CL}	clamping voltage	$I_{PPM} = 1.6 \text{ A}; t_p = 10/1000 \text{ } \mu\text{s}; T_{amb} = 25 \text{ }^\circ\text{C}$	[1] [2]	-	25	V
S_Z	temperature coefficient	$I_Z = 1 \text{ mA}$	-	14	-	mV/K

- [1] In accordance with IEC 61643-321 (10/1000 μs current waveform).
- [2] Measured from pin 1 or 2 to pin 3.



Double ESD protection diode for transient overvoltage suppression

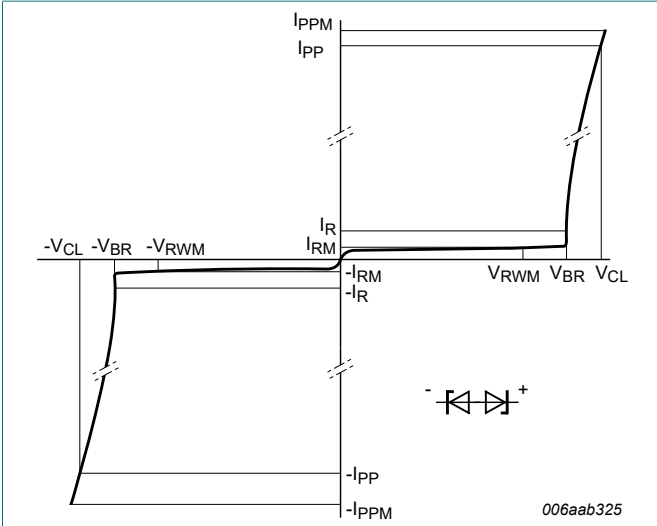


Fig. 5. V-I characteristics for a bidirectional ESD protection diode

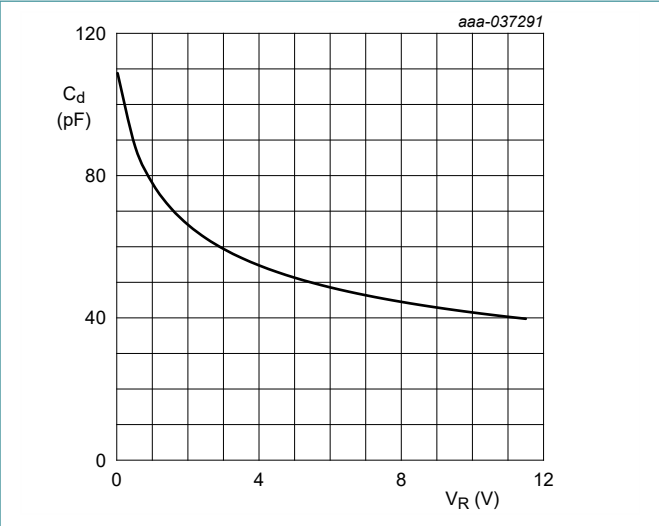
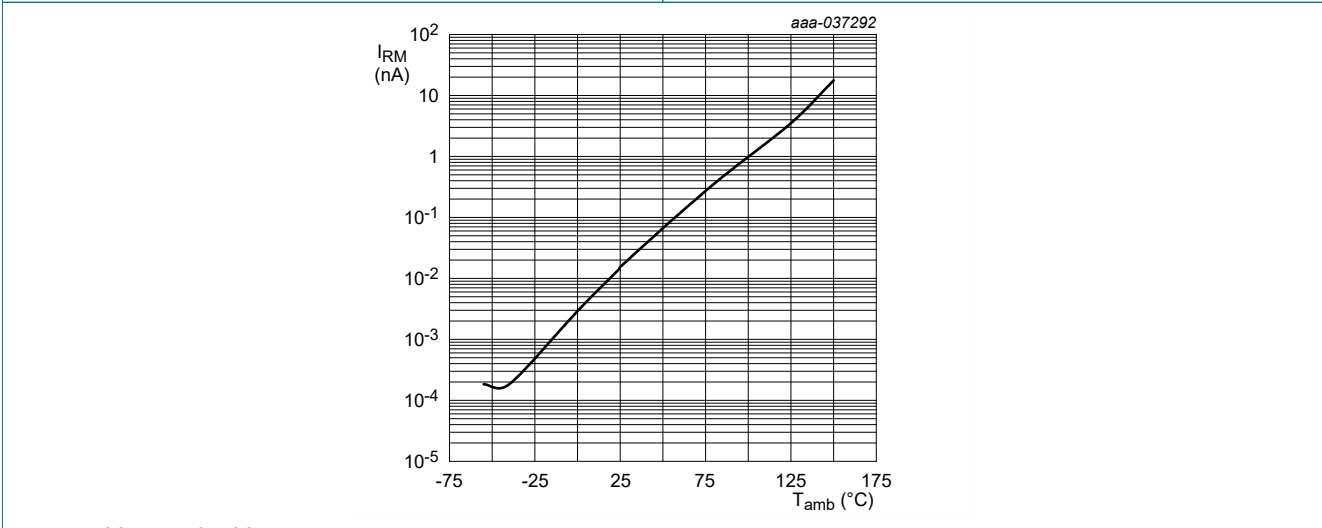


Fig. 6. Diode capacitance as a function of reverse voltage; typical values
 f = 1 MHz; T_{amb} = 25 °C



V_{RWM} = 8.5 V

Fig. 7. Reverse leakage current as a function of ambient temperature; typical values

11. Application information

The device is designed for the protection of up to two unidirectional data lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are either positive or negative with respect to ground.

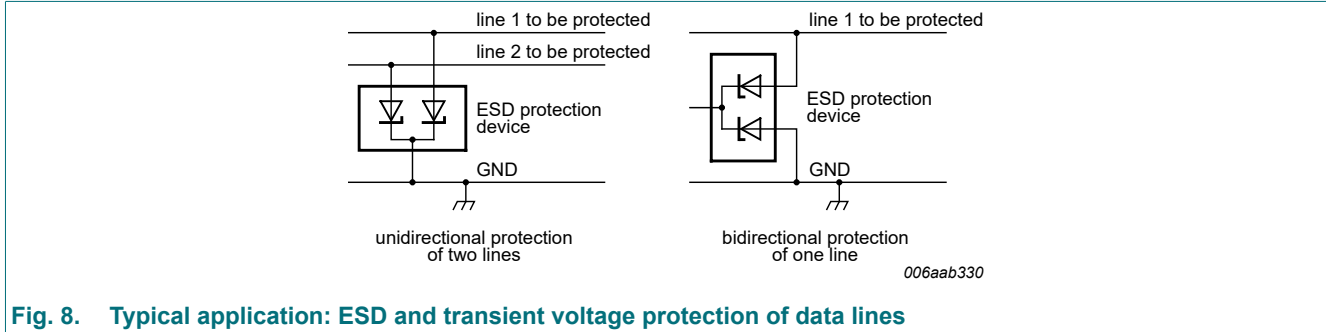


Fig. 8. Typical application: ESD and transient voltage protection of data lines

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

12. Package outline

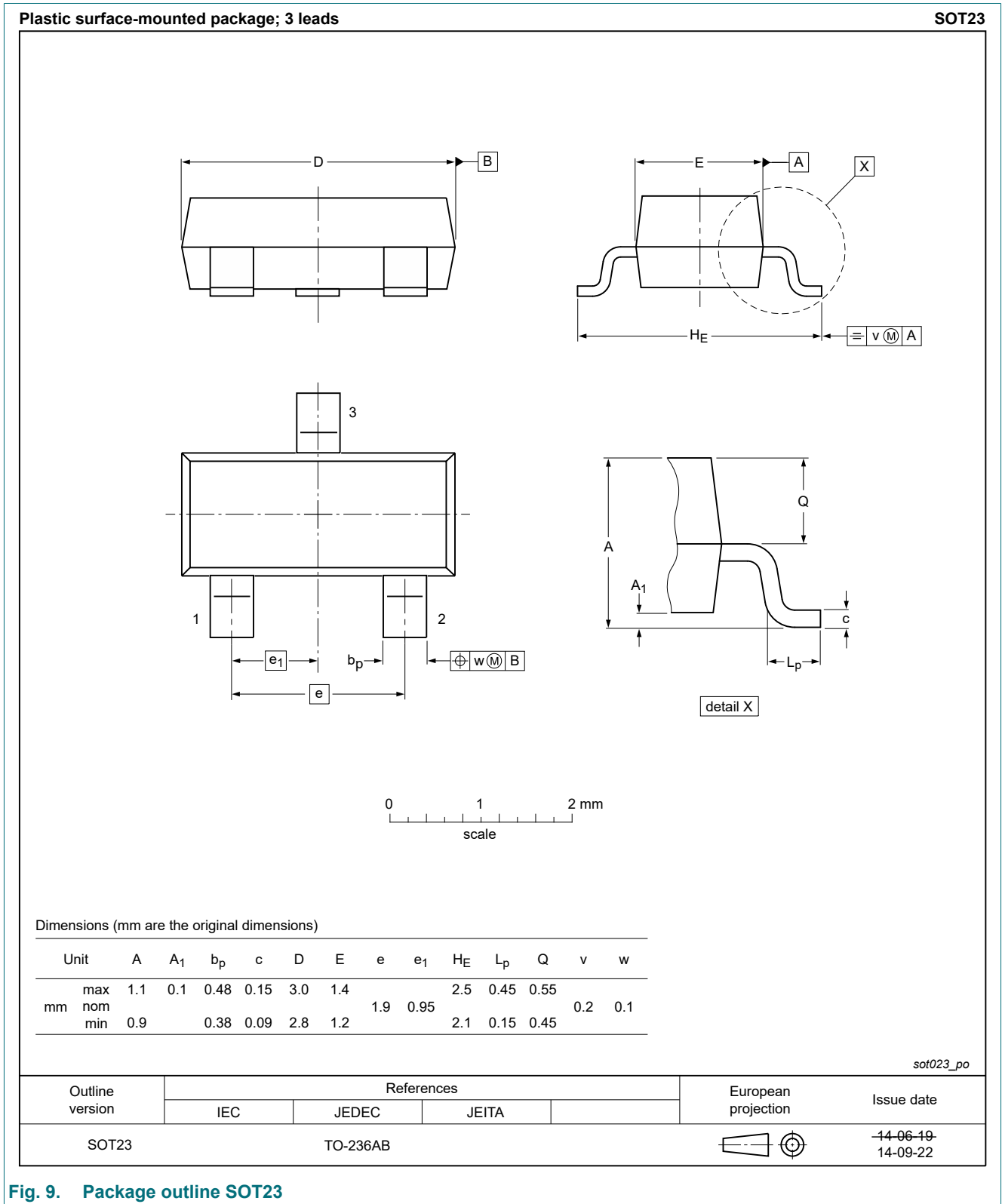


Fig. 9. Package outline SOT23

13. Soldering

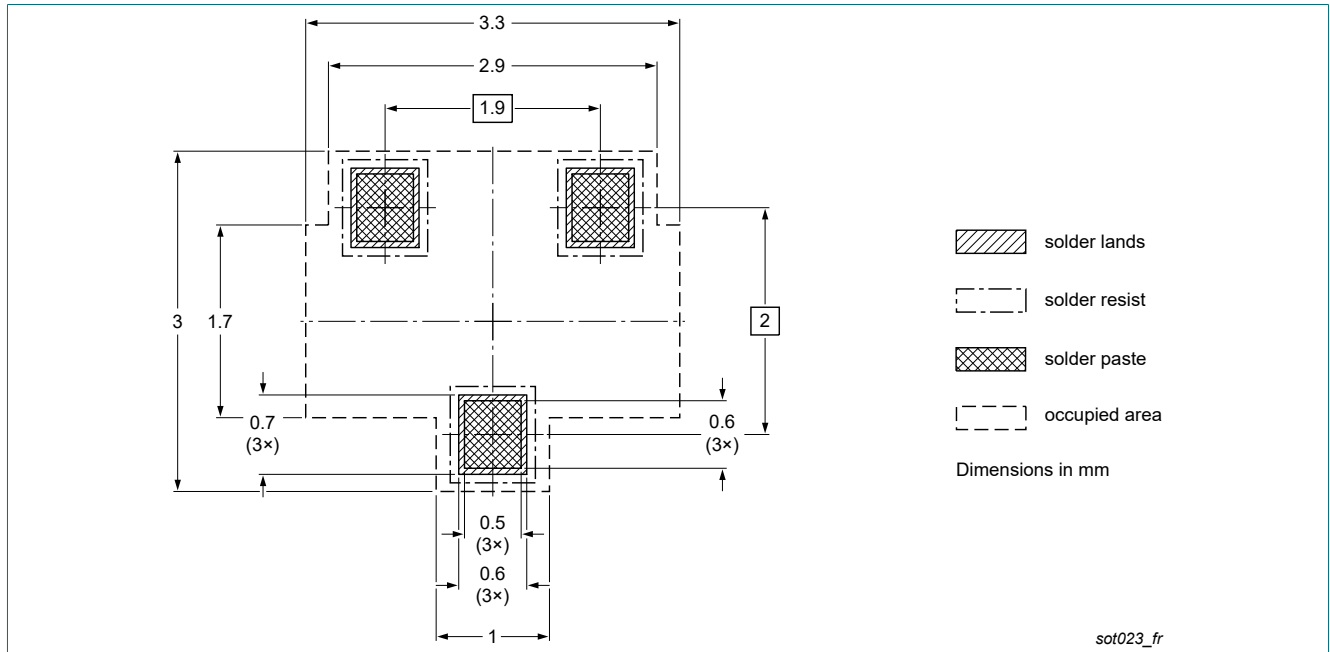


Fig. 10. Reflow soldering footprint for SOT23

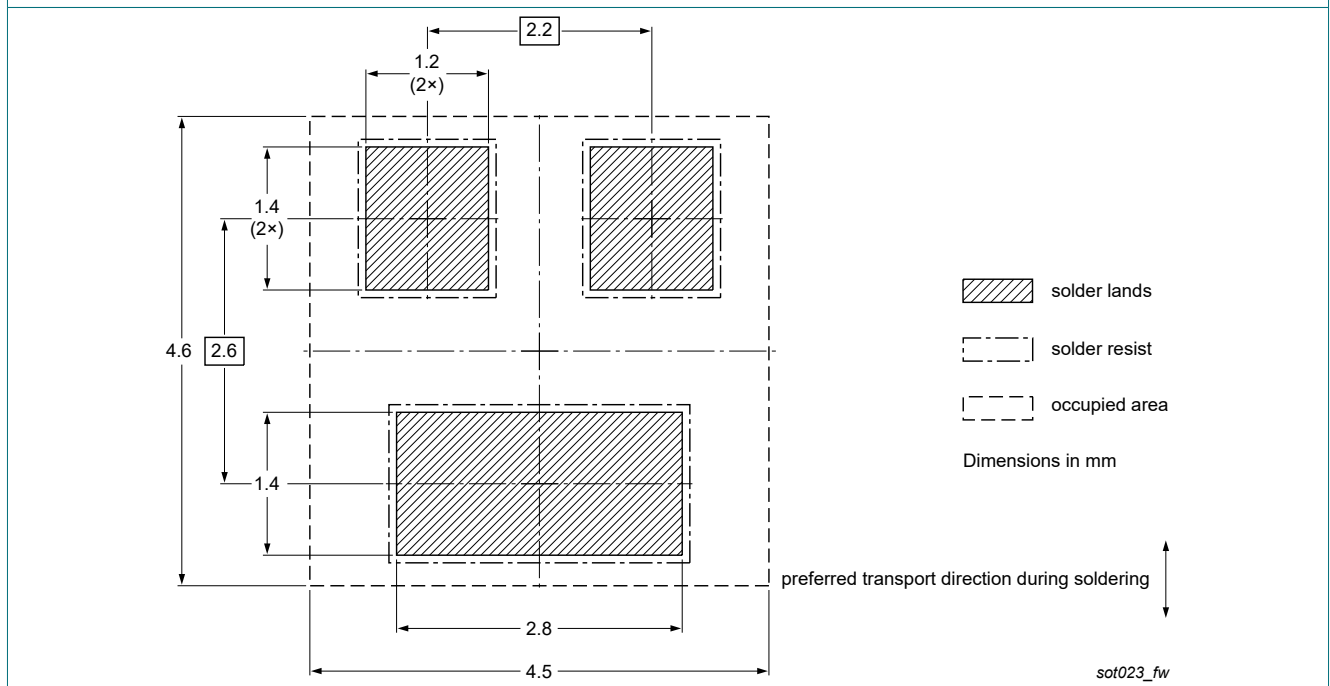


Fig. 11. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBZ18VCL v.2	20230817	Product data sheet	-	MMBZXVCL_ MMBZXVDL_SER_1 v.1
Modifications:	<ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia Legal texts have been adapted to the new company name where appropriate Family data sheet reduced to single type data sheet 			
MMBZXVCL_ MMBZXVDL_SER_1 v.1	20080903	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	2
9. Thermal characteristics.....	4
10. Characteristics.....	4
11. Application information.....	6
12. Package outline.....	7
13. Soldering.....	8
14. Revision history.....	9
15. Legal information.....	10

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