

40 V, 200 mA NPN switching transistor

11 April 2024

Product data sheet

1. General description

NPN switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package. PNP complement: MMBT3906

2. Features and benefits

- Collector current capability I_C = 200 mA
- Collector-emitter voltage V_{CEO} = 40 V
- AEC-Q101 qualified

3. Applications

General switching and amplification

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	200	mA
h _{FE}	DC current gain	V_{CE} = 1 V; I _C = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C		100	-	300	

5. Pinning information

Table 2	. Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	с
2	E	emitter		J
3	С	collector		в-К
				E
			SOT23	aaa-027673



6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
MMBT3904		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes					
Type number	Marking code[1]				
MMBT3904	7A%				

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	200	mA
I _{CM}	peak collector current			-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

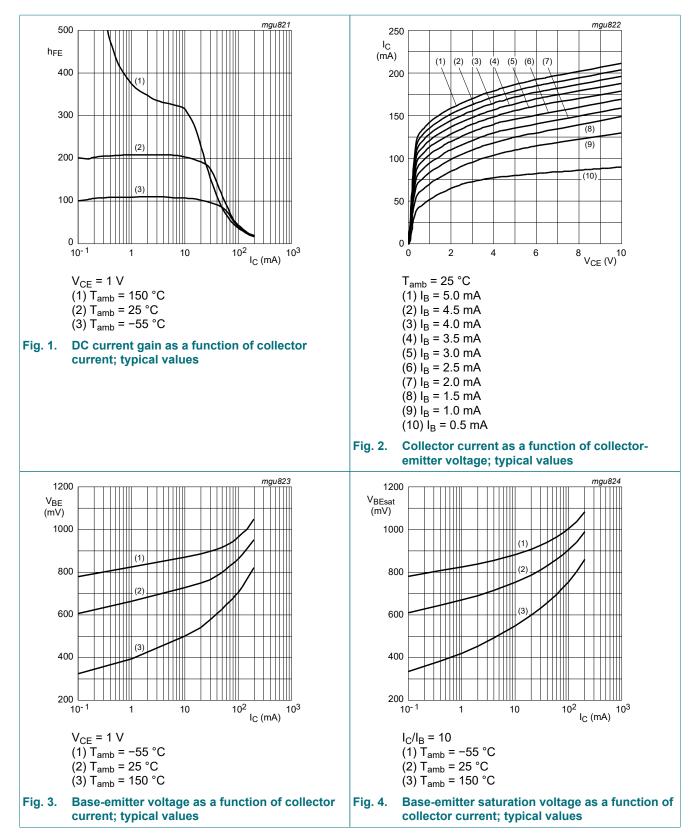
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

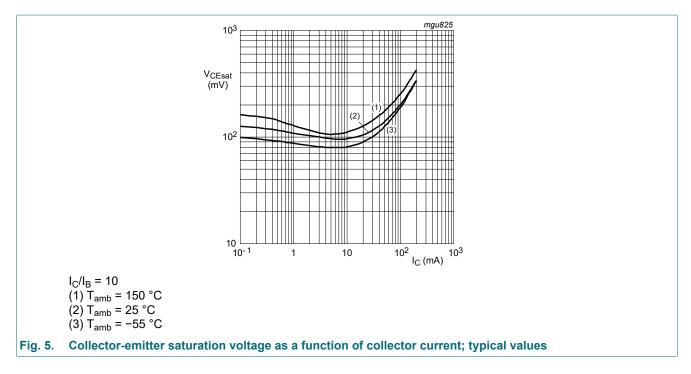
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = 30 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$	-	-	50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	50	nA
h _{FE}	DC current gain V_{CE} = 1 V; I _C = 0.1 mA; t _p ≤ 300 µs; δ 0.02; T _{amb} = 25 °C	V_{CE} = 1 V; I _C = 0.1 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	60	-	-	
		V_{CE} = 1 V; I _C = 1 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	80	-	-	
		V_{CE} = 1 V; I _C = 10 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	100	-	300	
		V_{CE} = 1 V; I _C = 50 mA; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C	60	-	-	
		$V_{CE} = 1 \text{ V}; \text{ I}_{C} = 100 \text{ mA}; \text{ t}_{p} \le 300 \mu\text{s};$ $\delta \le 0.02; \text{ T}_{amb} = 25 ^{\circ}\text{C}$	30	-	-	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C	-	-	200	mV
saturation volta	saturation voltage	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	-	300	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C	650	-	850	mV
	voltage	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	-	950	mV
C _c	collector capacitance	$V_{CB} = 5 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	-	-	4	pF
C _e	emitter capacitance	V _{EB} = 500 mV; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	8	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	300	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 100 μA; R _S = 1 kΩ; f = 10 Hz to 15.7 kHz; T _{amb} = 25 °C	-	-	5	dB
Switching t	imes (between 10% and 90	% levels);		•		
t _d	delay time	I _C = 10 mA; I _{Bon} = 1 mA; I _{Boff} = -1 mA;	-	-	35	ns
t _r	rise time	T _{amb} = 25 °C	-	-	35	ns
t _s	storage time		-	-	200	ns
t _f	fall time		-	-	50	ns

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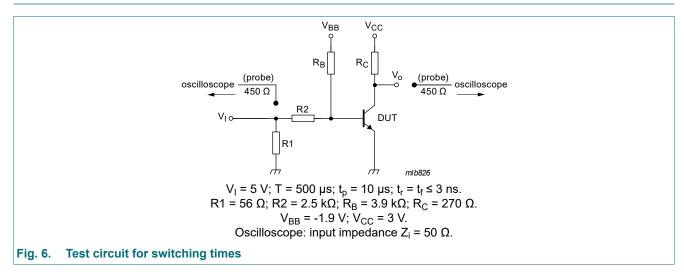


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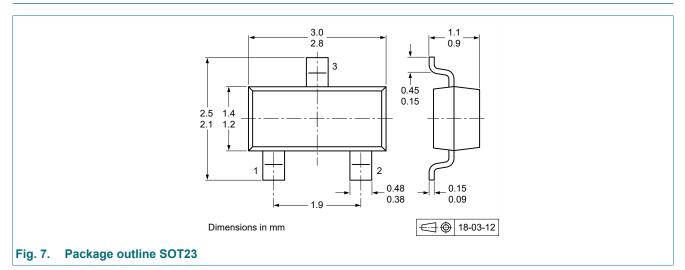
11. Test information



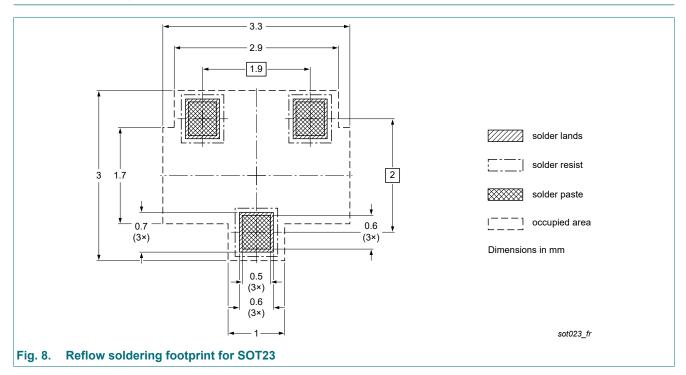
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

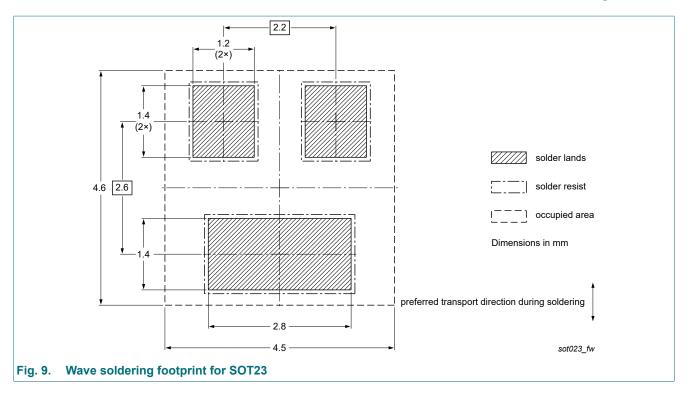
12. Package outline



13. Soldering



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14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBT3904 v.3	20240411	Product data sheet	-	MMBT3904 v.2
Modifications:	Nexperia.	his data sheet has been rede ve been adapted to the new c		
MMBT3904 v.2	20040203	Product data sheet	-	MMBT3904 v.1

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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