1. General description

Logic level N-channel MOSFET in an LFPAK56 (Power SO8) package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

2. Features and benefits

- Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True logic level gate with $V_{GS(th)}$ rating of greater than 0.5 V at 175 °C

3. Applications

- 12 V, 24 V and 48 V Automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

4. Quick reference data

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{DS}$</td>
<td>drain-source voltage</td>
<td>$25 , ^\circ \text{C} \leq T_j \leq 175 , ^\circ \text{C}$</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>V</td>
</tr>
<tr>
<td>$I_D$</td>
<td>drain current</td>
<td>$V_{GS} = 5 , \text{V}; T_{mb} = 25 , ^\circ \text{C}; \text{Fig.}$</td>
<td>-</td>
<td>-</td>
<td>69</td>
<td>A</td>
</tr>
<tr>
<td>$P_{tot}$</td>
<td>total power dissipation</td>
<td>$T_{mb} = 25 , ^\circ \text{C}; \text{Fig. 1}$</td>
<td>-</td>
<td>-</td>
<td>195</td>
<td>W</td>
</tr>
</tbody>
</table>

**Static characteristics**

| $R_{DSon}$ | drain-source on-state resistance | $V_{DS} = 5 \, \text{V}; I_D = 20 \, \text{A}; T_j = 25 \, ^\circ \text{C}; \text{Fig. 11}$ | - | 12.1 | 15 | mΩ |

**Dynamic characteristics**

| $Q_{GD}$ | gate-drain charge | $I_D = 20 \, \text{A}; V_{DS} = 80 \, \text{V}; V_{GS} = 5 \, \text{V}; \text{Fig. 13; Fig. 14}$ | - | 16 | - | nC |
5. Pinning information

Table 2. Pinning information

<table>
<thead>
<tr>
<th>Pin</th>
<th>Symbol</th>
<th>Description</th>
<th>Simplified outline</th>
<th>Graphic symbol</th>
</tr>
</thead>
<tbody>
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<td>1</td>
<td>S</td>
<td>source</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>S</td>
<td>source</td>
<td></td>
<td></td>
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<tr>
<td>3</td>
<td>S</td>
<td>source</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>G</td>
<td>gate</td>
<td></td>
<td></td>
</tr>
<tr>
<td>mb</td>
<td>D</td>
<td>mounting base; connected to drain</td>
<td><img src="image" alt="Simplified outline" /></td>
<td><img src="image" alt="Graphic symbol" /></td>
</tr>
</tbody>
</table>

6. Ordering information

Table 3. Ordering information

<table>
<thead>
<tr>
<th>Type number</th>
<th>Package</th>
<th>Description</th>
<th>Version</th>
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</thead>
<tbody>
<tr>
<td>BUK9Y15-100E</td>
<td>LFPAK56; Power-SO8</td>
<td>Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads</td>
<td>SOT669</td>
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7. Marking

Table 4. Marking codes

<table>
<thead>
<tr>
<th>Type number</th>
<th>Marking code</th>
</tr>
</thead>
<tbody>
<tr>
<td>BUK9Y15-100E</td>
<td>91510E</td>
</tr>
</tbody>
</table>

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{DS}$</td>
<td>drain-source voltage</td>
<td>$25 , ^\circ C \leq T_j \leq 175 , ^\circ C$</td>
<td>-</td>
<td>100</td>
<td>V</td>
</tr>
<tr>
<td>$V_{DGR}$</td>
<td>drain-gate voltage</td>
<td>$R_{GS} = 20 , k\Omega$</td>
<td>-</td>
<td>100</td>
<td>V</td>
</tr>
<tr>
<td>$V_{GS}$</td>
<td>gate-source voltage</td>
<td>$DC; \ T_j \leq 175 , ^\circ C$</td>
<td>-10</td>
<td>10</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Pulsed; $T_j \leq 175 , ^\circ C$</td>
<td>-15</td>
<td>15</td>
<td>V</td>
</tr>
<tr>
<td>$P_{tot}$</td>
<td>total power dissipation</td>
<td>$T_{mb} = 25 , ^\circ C; \ Fig. \ 1$</td>
<td>-</td>
<td>195</td>
<td>W</td>
</tr>
<tr>
<td>$I_D$</td>
<td>drain current</td>
<td>$V_{GS} = 5 , V; \ T_{mb} = 25 , ^\circ C; \ Fig. \ 2$</td>
<td>-</td>
<td>69</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{GS} = 5 , V; \ T_{mb} = 100 , ^\circ C; \ Fig. \ 2$</td>
<td>-</td>
<td>49</td>
<td>A</td>
</tr>
<tr>
<td>$I_{DM}$</td>
<td>peak drain current</td>
<td>pulsed; $t_p \leq 10 , \mu s; \ T_{mb} = 25 , ^\circ C; \ Fig. \ 3$</td>
<td>-</td>
<td>274</td>
<td>A</td>
</tr>
</tbody>
</table>
**Symbol** | **Parameter** | **Conditions** | **Min** | **Max** | **Unit**
---|---|---|---|---|---
$T_{stg}$ | storage temperature |  | -55 | 175 | °C
$T_j$ | junction temperature |  | -55 | 175 | °C

**Source-drain diode**

<table>
<thead>
<tr>
<th><strong>Symbol</strong></th>
<th><strong>Parameter</strong></th>
<th><strong>Conditions</strong></th>
</tr>
</thead>
</table>
$I_S$ | source current | $T_{mb} = 25 \, ^\circ C$ |
$I_{SM}$ | peak source current | pulsed; $t_p \leq 10 \, \mu s$; $T_{mb} = 25 \, ^\circ C$ |

**Avalanche ruggedness**

$E_{DS(AlS)}$ | non-repetitive drain-source avalanche energy | $I_D = 69 \, A$; $V_{sup} \leq 100 \, V$; $R_{GS} = 50 \, \Omega$; $V_{GS} = 5 \, V$; $T_{j(init)} = 25 \, ^\circ C$; unclamped; Fig. 4 |

$[1]$ Accumulated pulse duration up to 50 hours delivers zero defect ppm
$[2]$ Significantly longer life times are achieved by lowering $T_j$ and or $V_{GS}$
$[3]$ Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.
$[4]$ Refer to application note AN10273 for further information.

**Fig. 1.** Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ C)}} \times 100 \, \%$$

**Fig. 2.** Continuous drain current as a function of mounting base temperature

$V_{GS} \geq 5V$
Fig. 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

\[ T_{\text{mb}} = 25^\circ \text{C}; \ I_{\text{DM}} \text{ is a single pulse} \]

Fig. 4. Avalanche rating; avalanche current as a function of avalanche time

\[ (1) \ T_f(\text{init}) = 25^\circ \text{C}; \ (2) \ T_f(\text{init}) = 150^\circ \text{C}; \ (3) \text{ Repetitive Avalanche} \]

9. Thermal characteristics

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter Description</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( R_{\text{th(j-mb)}} )</td>
<td>thermal resistance from junction to mounting base</td>
<td>Fig. 5</td>
<td>-</td>
<td>-</td>
<td>0.77</td>
<td>K/W</td>
</tr>
</tbody>
</table>
**10. Characteristics**

**Table 7. Characteristics**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{(BR)DSS}$</td>
<td>drain-source breakdown voltage</td>
<td>$I_D = 250 \mu A; V_{GS} = 0 V; T_J = 25 \degree C$</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_D = 250 \mu A; V_{GS} = 0 V; T_J = -55 \degree C$</td>
<td>90</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>$V_{GS(th)}$</td>
<td>gate-source threshold voltage</td>
<td>$I_D = 1 mA; V_{DS}=V_{GS}; T_J = 25 \degree C$</td>
<td>1.4</td>
<td>1.7</td>
<td>2.1</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_D = 1 mA; V_{DS}=V_{GS}; T_J = -55 \degree C$</td>
<td>-</td>
<td>-</td>
<td>2.45</td>
<td>V</td>
</tr>
<tr>
<td>$I_{DSS}$</td>
<td>drain leakage current</td>
<td>$V_{DS} = 100 V; V_{GS} = 0 V; T_J = 25 \degree C$</td>
<td>-</td>
<td>0.11</td>
<td>10</td>
<td>$\mu A$</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{DS} = 100 V; V_{GS} = 0 V; T_J = 175 \degree C$</td>
<td>-</td>
<td>-</td>
<td>500</td>
<td>$\mu A$</td>
</tr>
<tr>
<td>$I_{GSS}$</td>
<td>gate leakage current</td>
<td>$V_{GS} = 10 V; V_{DS} = 0 V; T_J = 25 \degree C$</td>
<td>-</td>
<td>2</td>
<td>100</td>
<td>nA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{GS} = -10 V; V_{DS} = 0 V; T_J = 25 \degree C$</td>
<td>-</td>
<td>2</td>
<td>100</td>
<td>nA</td>
</tr>
<tr>
<td>$R_{DS(on)}$</td>
<td>drain-source on-state resistance</td>
<td>$V_{GS} = 5 V; I_D = 20 A; T_J = 25 \degree C$</td>
<td>-</td>
<td>12.1</td>
<td>15</td>
<td>m$\Omega$</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{GS} = 10 V; I_D = 20 A; T_J = 25 \degree C$</td>
<td>-</td>
<td>11.6</td>
<td>14.7</td>
<td>m$\Omega$</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{GS} = 5 V; I_D = 20 A; T_J = 175 \degree C$</td>
<td>-</td>
<td>-</td>
<td>41.4</td>
<td>m$\Omega$</td>
</tr>
</tbody>
</table>

**Diagram:**

Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

---

**Figure 5:**

- **Graph:** Transient thermal impedance from junction to mounting base as a function of pulse duration.
- **Axes:**
  - X-axis: $t_p$ (s)
  - Y-axis: $Z_{th(j\to mb)}$ (K/W)
- **Legend:**
  - Single shot
  - Delta = 0.5
  - Delta = 0.2
  - Delta = 0.1
  - Delta = 0.05
  - Delta = 0.02

---

**Static characteristics**

- $V_{(BR)DSS}$: Drain-source breakdown voltage
  - $I_D = 250 \mu A; V_{GS} = 0 V; T_J = 25 \degree C$ (Min: 100, Typ: - , Max: - , Unit: V)
  - $I_D = 250 \mu A; V_{GS} = 0 V; T_J = -55 \degree C$ (Min: 90, Typ: - , Max: - , Unit: V)

- $V_{GS(th)}$: Gate-source threshold voltage
  - $I_D = 1 mA; V_{DS}=V_{GS}; T_J = 25 \degree C$ (Min: 1.4, Typ: 1.7, Max: 2.1, Unit: V)
  - $I_D = 1 mA; V_{DS}=V_{GS}; T_J = -55 \degree C$ (Min: -, Typ: -, Max: 2.45, Unit: V)

- $I_{DSS}$: Drain leakage current
  - $V_{DS} = 100 V; V_{GS} = 0 V; T_J = 25 \degree C$ (Min: -, Typ: 0.11, Max: 10, Unit: $\mu A$)
  - $V_{DS} = 100 V; V_{GS} = 0 V; T_J = 175 \degree C$ (Min: -, Typ: -, Max: 500, Unit: $\mu A$)

- $I_{GSS}$: Gate leakage current
  - $V_{GS} = 10 V; V_{DS} = 0 V; T_J = 25 \degree C$ (Min: -, Typ: 2, Max: 100, Unit: nA)
  - $V_{GS} = -10 V; V_{DS} = 0 V; T_J = 25 \degree C$ (Min: -, Typ: 2, Max: 100, Unit: nA)

- $R_{DS(on)}$: Drain-source on-state resistance
  - $V_{GS} = 5 V; I_D = 20 A; T_J = 25 \degree C$ (Min: -, Typ: 12.1, Max: 15, Unit: m$\Omega$)
  - $V_{GS} = 10 V; I_D = 20 A; T_J = 25 \degree C$ (Min: -, Typ: 11.6, Max: 14.7, Unit: m$\Omega$)
  - $V_{GS} = 5 V; I_D = 20 A; T_J = 175 \degree C$ (Min: -, Typ: -, Max: 41.4, Unit: m$\Omega$)

---

**Dynamic characteristics**

- $Q_{G(tot)}$: Total gate charge
  - $I_D = 20 A; V_{DS} = 80 V; V_{GS} = 5 V$ (Min: -, Typ: 45.8, Max: -, Unit: nC)

- $Q_{GS}$: Gate-source charge
  - $I_D = 20 A; V_{DS} = 80 V; V_{GS} = 5 V$ (Min: -, Typ: 11, Max: -, Unit: nC)

- $Q_{GD}$: Gate-drain charge
  - $I_D = 20 A; V_{DS} = 80 V; V_{GS} = 5 V$ (Min: -, Typ: 16, Max: -, Unit: nC)
# N-channel 100 V, 15 mΩ logic level MOSFET in LFPAK56

## Symbol | Parameter | Conditions | Min | Typ | Max | Unit
---|---|---|---|---|---|---
\(C_{iss}\) | input capacitance | \(V_{DS} = 25\, V; V_{GS} = 0\, V; f = 1\, MHz; T_j = 25\, ^{\circ}\, C; \text{Fig. 15}\) | - | 4604 | 6139 | pF
\(C_{oss}\) | output capacitance | - | 269 | 323 | pF
\(C_{rss}\) | reverse transfer capacitance | - | 156 | 213 | pF
\(t_{d(on)}\) | turn-on delay time | \(V_{DS} = 80\, V; R_L = 4\, \Omega; V_{GS} = 5\, V; R_G(\text{ext}) = 5\, \Omega\) | - | 21 | - | ns
\(t_r\) | rise time | - | 32 | - | ns
\(t_{d(off)}\) | turn-off delay time | - | 85 | - | ns
\(t_f\) | fall time | - | 59 | - | ns

### Source-drain diode

| Parameter | Conditions | Min | Typ | Max | Unit |
---|---|---|---|---|---|
\(V_{SD}\) | source-drain voltage | \(I_S = 20\, A; V_{GS} = 0\, V; T_j = 25\, ^{\circ}\, C; \text{Fig. 16}\) | - | 0.8 | 1.2 | V
\(t_{rr}\) | reverse recovery time | \(I_S = 20\, A; dl_S/dt = -100\, A/\mu s; V_{GS} = 0\, V; V_{DS} = 25\, V\) | - | 44 | - | ns
\(Q_r\) | recovered charge | - | 79 | - | nC

---

**Fig. 6.** Output characteristics; drain current as a function of drain-source voltage; typical values

**Fig. 7.** Drain-source on-state resistance as a function of gate-source voltage; typical values

\[ T_j = 25\, ^{\circ}\, C; I_D = 20\, A \]
Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

\[ V_{DS} = 10\text{V} \]

Fig. 9. Gate-source threshold voltage as a function of junction temperature

\[ I_D = 1\text{mA}; \ V_{DS} = V_{GS} \]

Fig. 10. Sub-threshold drain current as a function of gate-source voltage

\[ T_J = 25^\circ\text{C}; \ V_{DS} = 5\text{V} \]

Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

\[ T_J = 25^\circ\text{C}; \ t_p = 300\mu\text{s} \]
Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

\[ a = \frac{R_{DS(on)}}{R_{DS(on)(25 \, ^\circ C)}} \]

Fig. 13. Gate charge waveform definitions

Fig. 14. Gate-source voltage as a function of gate charge; typical values

\[ T_J = 25^\circ C; \quad I_D = 20A \]

Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

\[ V_{GS} = 0V; \quad f = 1MHz \]
Fig. 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

\[ V_{GS} = 0V \]
11. Package outline

Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads

Dimensions (mm are the original dimensions)

<table>
<thead>
<tr>
<th>Unit</th>
<th>A</th>
<th>A1</th>
<th>A2</th>
<th>A3</th>
<th>b</th>
<th>b2</th>
<th>b3</th>
<th>c</th>
<th>c2</th>
<th>D(1)</th>
<th>D1(1)</th>
<th>E(1)</th>
<th>E1(1)</th>
<th>e</th>
<th>H</th>
<th>L</th>
<th>L1</th>
<th>L2</th>
<th>w</th>
<th>y</th>
</tr>
</thead>
<tbody>
<tr>
<td>mm</td>
<td>max</td>
<td>1.20</td>
<td>0.15</td>
<td>1.10</td>
<td>0.50</td>
<td>4.41</td>
<td>2.2</td>
<td>0.9</td>
<td>0.25</td>
<td>0.30</td>
<td>4.10</td>
<td>4.20</td>
<td>5.0</td>
<td>3.3</td>
<td>6.2</td>
<td>0.85</td>
<td>1.3</td>
<td>1.3</td>
<td>0.25</td>
<td>0.1</td>
</tr>
<tr>
<td>nom</td>
<td>1.01</td>
<td>0.00</td>
<td>0.96</td>
<td>0.35</td>
<td>3.62</td>
<td>2.0</td>
<td>0.7</td>
<td>0.19</td>
<td>0.24</td>
<td>3.80</td>
<td>4.8</td>
<td>3.1</td>
<td>5.8</td>
<td>0.40</td>
<td>0.8</td>
<td>0.8</td>
<td>0.8</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Note
1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

Fig. 17. Package outline LFPAK56; Power-SO8 (SOT669)
12. Legal information

12.1 Data sheet status

<table>
<thead>
<tr>
<th>Document status</th>
<th>Product status</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Objective [short] data sheet</td>
<td>Development</td>
<td>This document contains data from the objective specification for product development.</td>
</tr>
<tr>
<td>Preliminary [short] data sheet</td>
<td>Qualification</td>
<td>This document contains data from the preliminary specification.</td>
</tr>
<tr>
<td>Product [short] data sheet</td>
<td>Production</td>
<td>This document contains the product specification.</td>
</tr>
</tbody>
</table>

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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