**Product data sheet** 

# 1. General description

NPN Darlington transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: BST61

## 2. Features and benefits

- · Integrated diode and resistor
- AEC-Q101 qualified

## 3. Applications

- Industrial switching applications such as:
  - Print hammer
  - Solenoid
  - Relay and lamp driving

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	60	V
Ic	collector current		-	-	1	Α
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 10 V; $I_{C}$ = 150 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02; $T_{amb}$ = 25 °C	1000	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		С
2	С	collector		В
3	В	base	3 2 1 SOT89	E sym080



## **NPN Darlington transistor**

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
BST51		plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BST51	AS2

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	1	Α
I <sub>CM</sub>	peak collector current			-	2	Α
I <sub>B</sub>	base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.3	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

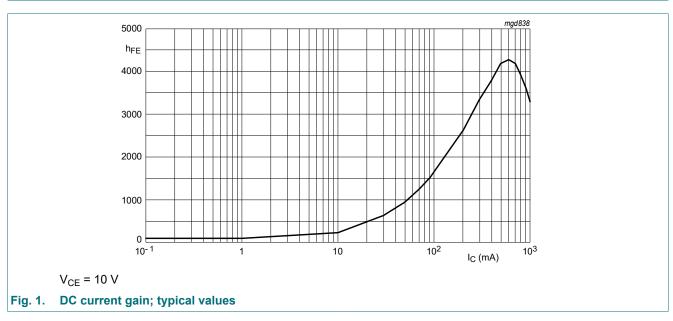
<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

### **NPN Darlington transistor**

## 10. Characteristics

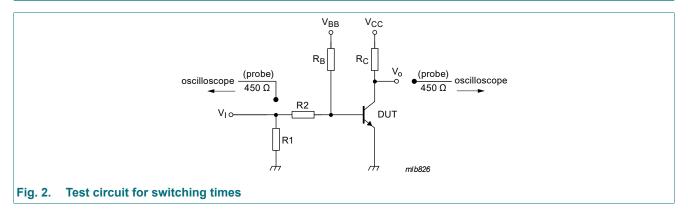
**Table 7. Characteristics** 

Symbol	Parameter	Conditions	ı	Vlin	Тур	Max	Unit
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-		-	50	nA
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 60 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	•	-	50	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 10 V; $I_{C}$ = 150 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	1	1000	-	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 500 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	2	2000	-	-	
V <sub>CEsat</sub> collector-emi	collector-emitter	$I_C = 500 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	-	-	-	1.3	V
	saturation voltage	$I_C$ = 500 mA; $I_B$ = 0.5 mA; $T_j$ = 150 °C	-	•	-	1.3	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-		-	1.9	V
f <sub>T</sub>	transition frequency	$V_{CE}$ = 5 V; $I_{C}$ = 500 mA; f = 100 MHz; $T_{amb}$ = 25 °C	-	-	200	-	MHz
Switching ti	imes (between 10% and 90	% levels)					
t <sub>on</sub>	turn-on time $I_{Bon} = 0.5 \text{ mA}$ ; $I_{Boff} = -0.5 \text{ mA}$ ; $I_{Con} = 500$	-		400	-	ns	
t <sub>off</sub>	turn-off time	mA; T <sub>amb</sub> = 25 °C			1500	-	ns



**NPN Darlington transistor** 

## 11. Test information

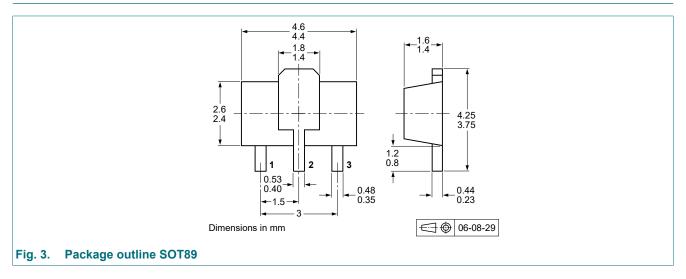


 $V_i$  = 10 V; T = 200 μs; tp = 6 μs;  $t_r$  =  $t_f$  ≤ 3 ns R1 = 56 Ω; R2 = 10 kΩ; R<sub>B</sub> = 10 kΩ; R<sub>C</sub> = 18 Ω  $V_{BB}$  = -1.8 V;  $V_{CC}$  = 10.7 V Oscilloscope: input impedance  $Z_i$  = 50 Ω

### **Quality information**

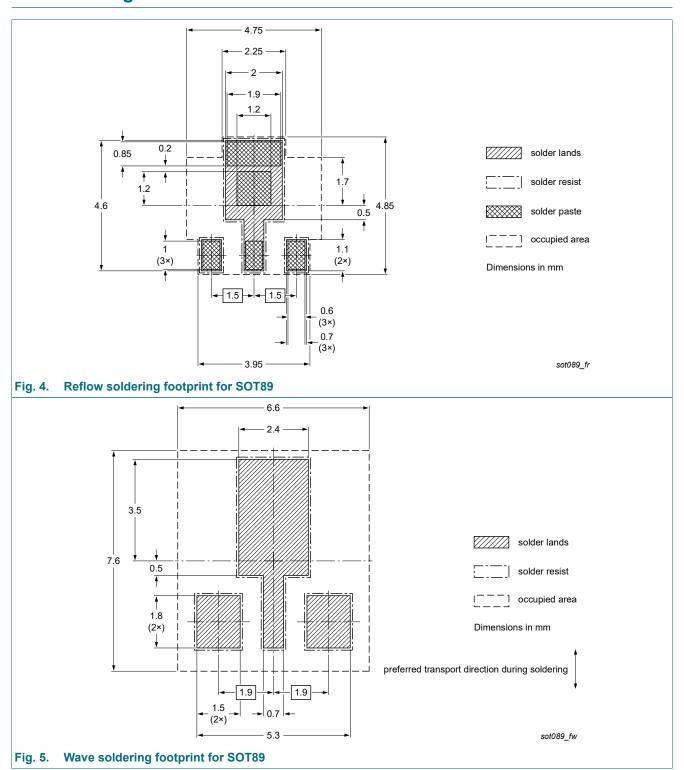
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



#### **NPN Darlington transistor**

# 13. Soldering



## **NPN Darlington transistor**

# 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
BST51 v.3	20230905	Product data sheet	-	BST50_51_52 v.2	
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guid of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Family data sheet splitted to single type data sheet.</li> </ul>				
BST50_51_52 v.2	20041209	Product data sheet	-	BST50_51_52 v.1	
BST50_51_52 v.1	20010220	Product specification	-	-	

### **NPN Darlington transistor**

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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### **NPN Darlington transistor**

## **Contents**

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	1
6. Ordering information	2
7. Marking	2
8. Limiting values	2
9. Thermal characteristics	2
10. Characteristics	3
11. Test information	4
12. Package outline	4
13. Soldering	5
14. Revision history	6
15. Legal information	7

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