



# BSR43

80 V, 1 A NPN medium power transistor

22 July 2025

Product data sheet

## 1. General description

NPN general-purpose transistor in a medium power SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package. PNP complement: BSR33.

## 2. Features and benefits

- High current (max. 1 A)
- Low voltage (max. 80 V)

## 3. Applications

- Linear voltage regulators
- Low-side switches
- Battery-driven devices
- Power management
- MOSFET drivers
- Amplifiers

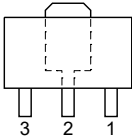
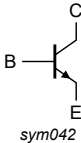
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CE0}$	collector-emitter voltage	open base	-	-	80	V
$I_C$	collector current		-	-	1	A
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	2	A
$h_{FE}$	DC current gain	$V_{CE} = 5$ V; $I_C = 100$ $\mu$ A; pulsed; $t_p \leq 300$ $\mu$ s; $\delta \leq 0.01$ ; $T_{amb} = 25$ °C	30	-	-	
		$V_{CE} = 5$ V; $I_C = 100$ mA; pulsed; $t_p \leq 300$ $\mu$ s; $\delta \leq 0.01$ ; $T_{amb} = 25$ °C	100	-	300	
		$V_{CE} = 5$ V; $I_C = 500$ mA; pulsed; $t_p \leq 300$ $\mu$ s; $\delta \leq 0.01$ ; $T_{amb} = 25$ °C	50	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 SOT89	 sym042
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BSR43	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code
BSR43	AR 4

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	90	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	1	A
I <sub>CM</sub>	peak collector current			-	2	A
I <sub>BM</sub>	peak base current			-	0.2	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.35	W
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	93	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	13	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

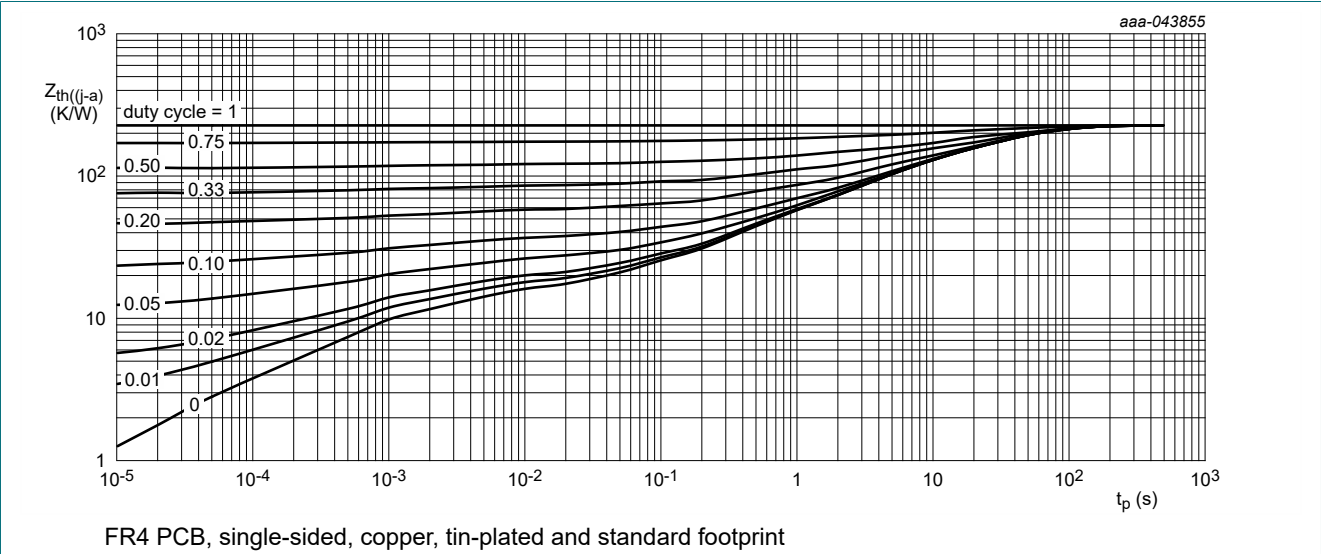


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

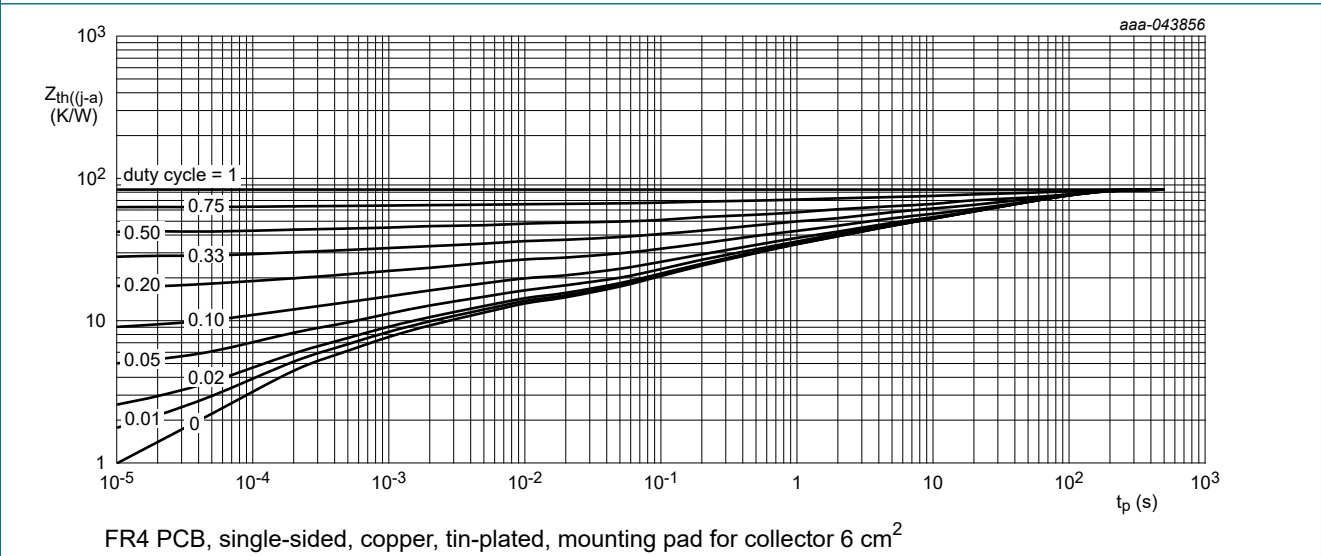


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current (emitter open)	V <sub>CB</sub> = 60 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
		V <sub>CB</sub> = 60 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	50	μA
I <sub>EBO</sub>	emitter-base cut-off current (collector open)	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 μA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		30	-	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		100	-	300	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 500 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		50	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		-	-	250	mV
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		-	-	500	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		-	-	1	V
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.01; T <sub>amb</sub> = 25 °C		-	-	1.2	V
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	-	12	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = 0.5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	-	90	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 50 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		100	-	-	MHz
Switching times (between 10% and 90% levels)							
t <sub>on</sub>	turn-on time	I <sub>C</sub> = 100 mA; I <sub>Bon</sub> = 5 mA; I <sub>Boff</sub> = -5 mA; T <sub>amb</sub> = 25 °C		-	-	250	ns
t <sub>off</sub>	turn-off time			-	-	1	μs

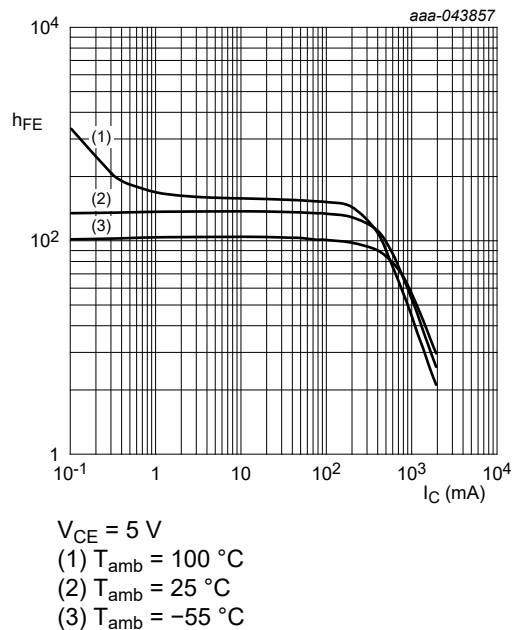


Fig. 3. DC current gain as a function of collector current; typical values

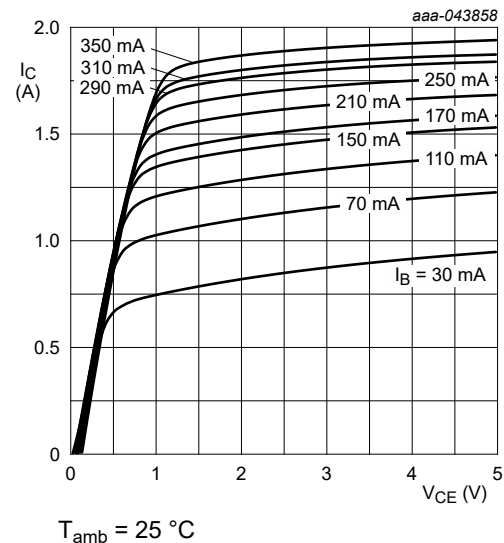


Fig. 4. Collector current as a function of collector-emitter voltage; typical values

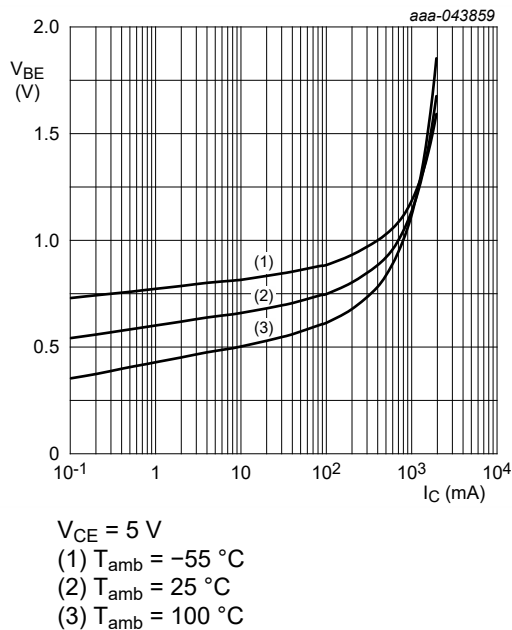


Fig. 5. Base-emitter voltage as a function of collector current; typical values

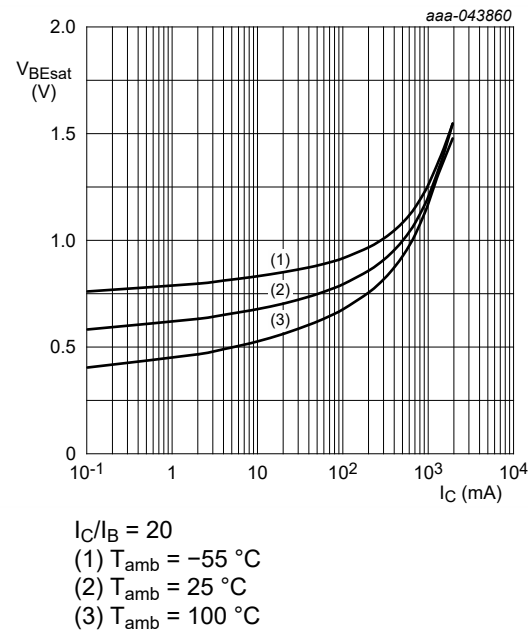
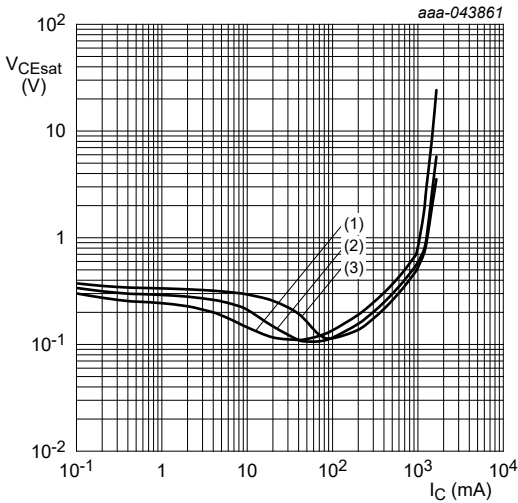


Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values



$I_C/I_B = 20$   
(1)  $T_{amb} = 100\text{ }^{\circ}\text{C}$   
(2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
(3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

11. Package outline

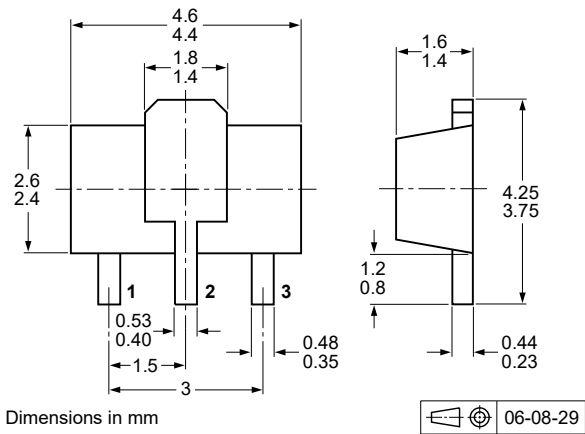


Fig. 8. Package outline SOT89

12. Soldering

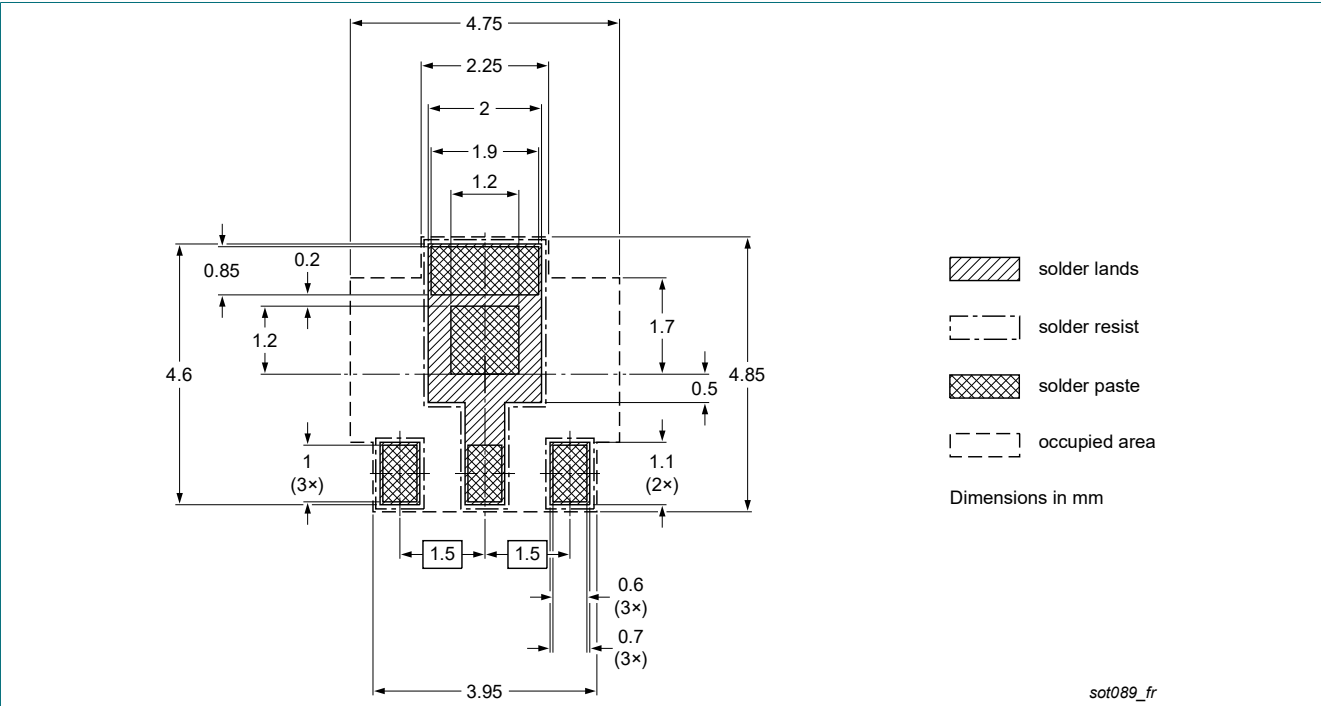


Fig. 9. Reflow soldering footprint for SOT89

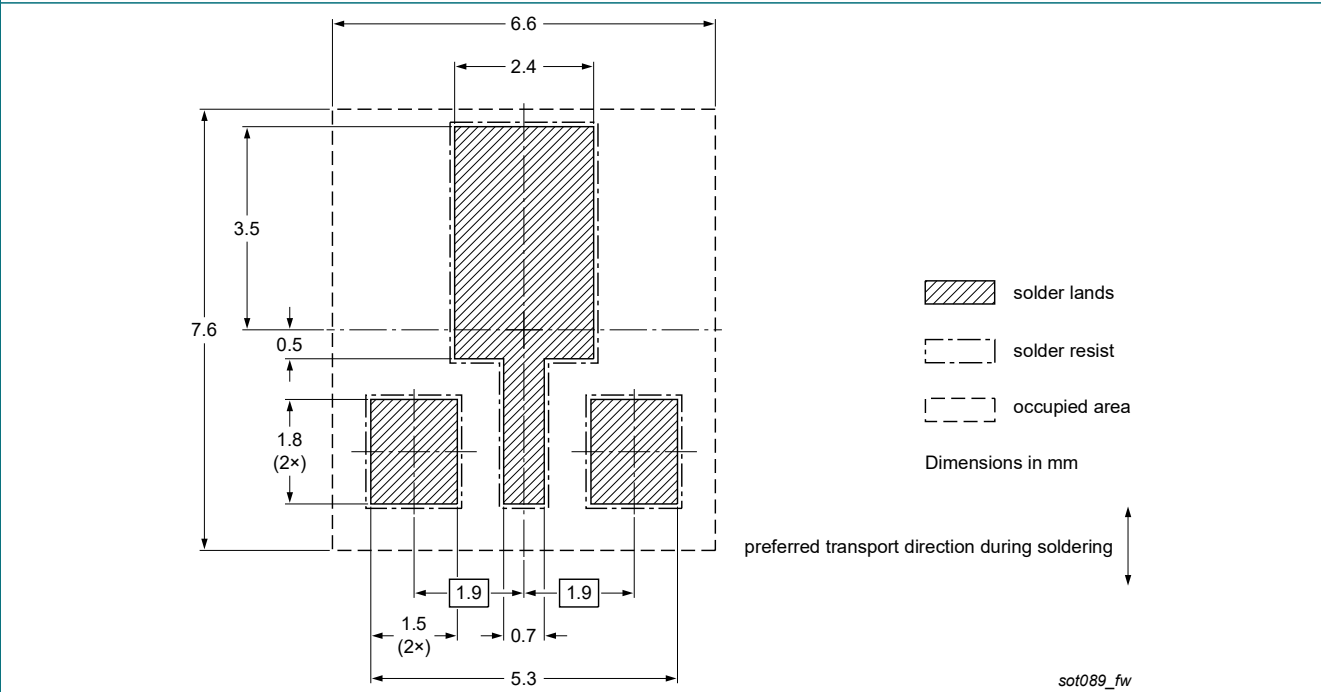


Fig. 10. Wave soldering footprint for SOT89

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSR43 v.4	20250722	Product data sheet	-	BSR43 v.3
Modifications:	• Figures 1 - 7 added			
BSR43 v.3	20221001	Product data sheet	-	BSR43 v.2
BSR43 v.2	20041213	Product data sheet	-	BSR43 v.1
BSR43 v.1	19990428	Product specification	-	-



# 14. Legal information

## Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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