**Product data sheet** 

# 1. General description

NPN Darlington transistor in an SOT223 plastic package.

PNP complement: BSP61

## 2. Features and benefits

- High current of 1 A
- Low voltage of 60 V
- Integrated diode and resistor
- AEC-Q101 qualified

## 3. Applications

Industrial high gain amplification

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-	80	V
V <sub>CES</sub>	collector-emitter voltage	base short-circuited to emitter		-	-	60	V
I <sub>C</sub>	collector current			-	-	1	Α
I <sub>CM</sub>	peak collector current			-	-	2	Α
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 150 mA	[1]	1000	-	-	

[1] Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 



**NPN Darlington transistor** 

# **5. Pinning information**

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	C
2	С	collector		B
3	E	emitter		
4	С	collector		
				I E aaa-027580

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
BSP51	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 4.6 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BSP51	BSP51

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# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	80	V
V <sub>CES</sub>	collector-emitter voltage	base short-circuited to emitter		-	60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	1	Α
I <sub>CM</sub>	peak collector current			-	2	Α
I <sub>Blim</sub>	limiting base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.25	W
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

#### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	96	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	17	K/W

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

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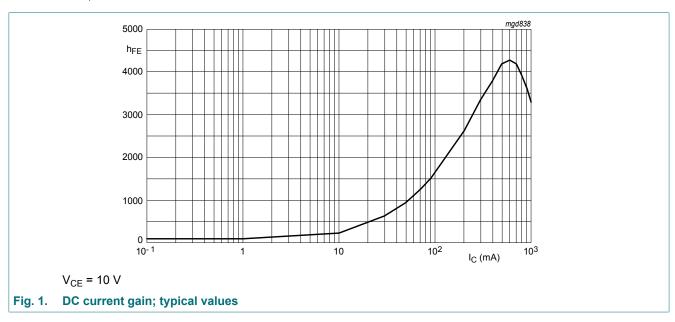
## 10. Characteristics

**Table 7. Characteristics** 

 $T_i$  = 25 °C unless otherwise specified

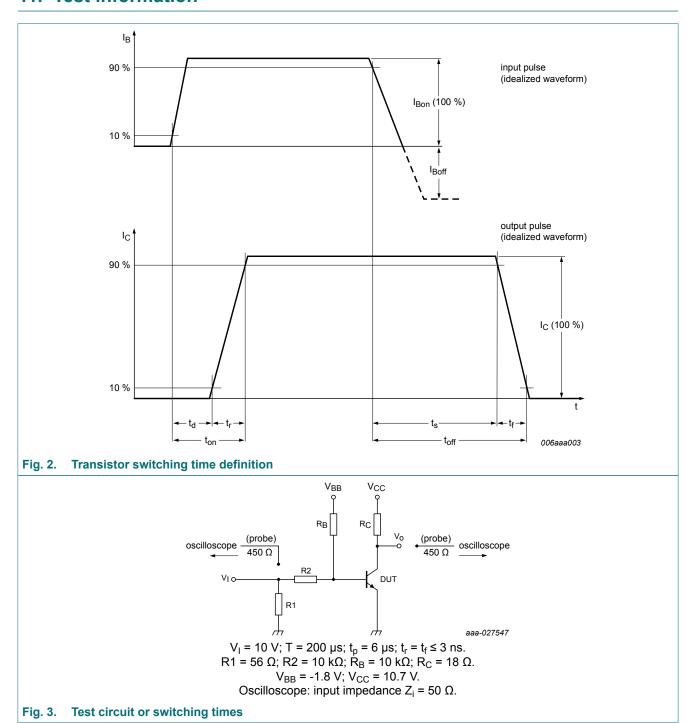
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		80	-	-	V
$V_{(BR)CES}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; V_{BE} = 0 \text{ V}$		60	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I <sub>C</sub> = 0 A; I <sub>E</sub> = 100 μA		5	-	-	V
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>BE</sub> = 0 V; V <sub>CE</sub> = 60 V		-	-	50	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A		-	-	50	nA
h <sub>FE</sub> DC	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 150 mA	[1]	1000	-	-	
		V <sub>CE</sub> = 10 V; I <sub>C</sub> = 500 mA	[1]	2000	-	-	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 0.5 mA		-	-	1.3	V
	saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 0.5 mA; T <sub>j</sub> = 150 °C		-	-	1.3	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 0.5 \text{ mA}$		-	-	1.9	V
t <sub>on</sub>	turn-on time	I <sub>C</sub> = 500 mA; I <sub>Bon</sub> = 0.5 mA;		-	500	-	ns
t <sub>off</sub>	turn-off time I <sub>Boff</sub> = -0.5 mA		-	1300	-	ns	
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 500 mA; f = 100 MHz		-	200	-	MHz

#### [1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$



**NPN Darlington transistor** 

### 11. Test information

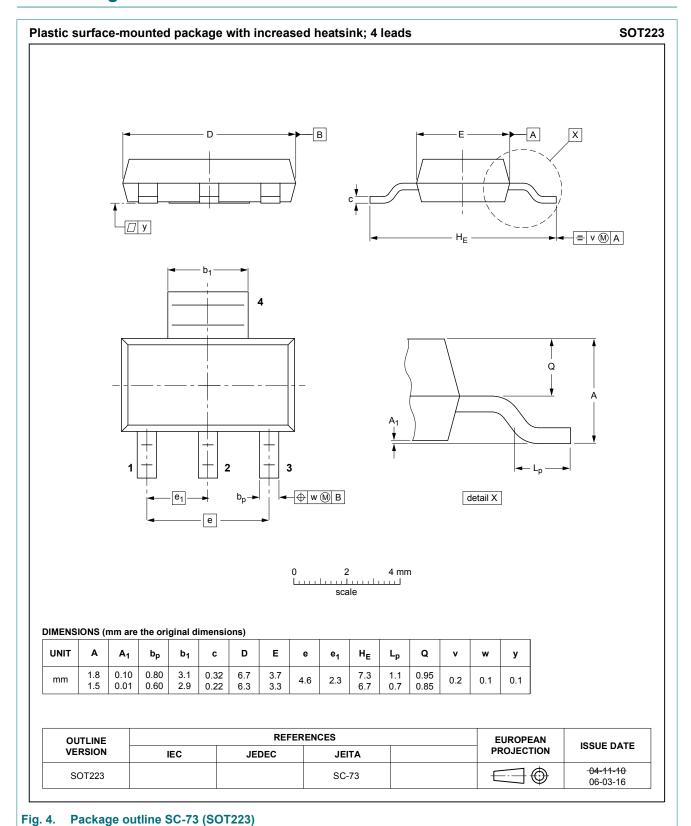


#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

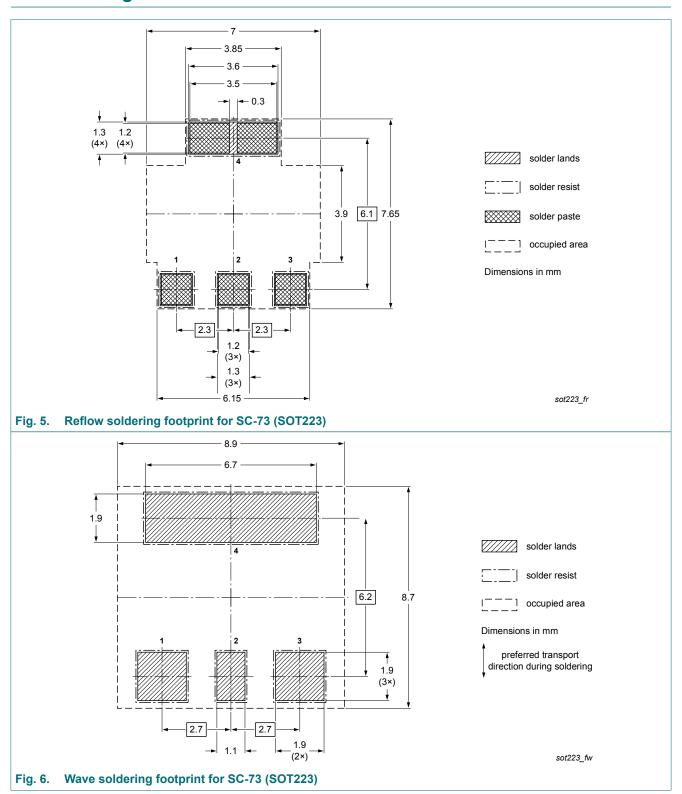
### **NPN Darlington transistor**

# 12. Package outline



#### **NPN Darlington transistor**

## 13. Soldering



**NPN Darlington transistor** 

# 14. Revision history

#### Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSP51 v.3	20180713	Product data sheet	-	BSP50_51_52 v.2
Modifications:	The format of this day Nexperia.	-Q101 qualified. s transformed to single da ata sheet has been redes en adapted to the new co	igned to comply with the	
BSP50_51_52 v.2	19990423	Product data sheet	-	BSP50_51_52 v.1
BSP50_51_52 v.1	19970422	Product data sheet	-	-

#### **NPN Darlington transistor**

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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