Product data sheet

1. General description

PNP medium power transistors in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High current
- Three current gain selections
- · High power dissipation capability
- Exposed heatsink for excellent thermal and electrical conductivity
- AEC-Q101 qualified

3. Applications

- Linear voltage regulators
- High-side switches
- · Battery-driven devices
- Power management
- MOSFET drivers
- Amplifiers

4. Quick reference data

Table 1. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V_{CEO}	collector-emitter voltage	open base		-	-	-60	V	
Ic	collector current			-	-	-1	Α	
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-2	Α	
h _{FE}	DC current gain							
	BCX52	V _{CE} = -2 V; I _C = -150 mA	[1]	63	-	250		
	BCX52-10	T _{amb} = 25 °C	[1]	63	-	160		
	BCX52-16		[1]	100	-	250		

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



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5. Pinning information

Table 2. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		C
2	С	collector		B —
3	В	base		
			$\overline{3}$ $\overline{2}$ $\overline{1}$	E
				006aaa231

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BCX52	SOT89	1 3, 1 , 1	SOT89			
BCX52-10		x 2.5 mm x 1.5 mm body				
BCX52-16						

7. Marking

Table 4. Marking

Type number	Marking code					
BCX52	AE					
BCX52-10	AG					
BCX52-16	AM					

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8. Limiting values

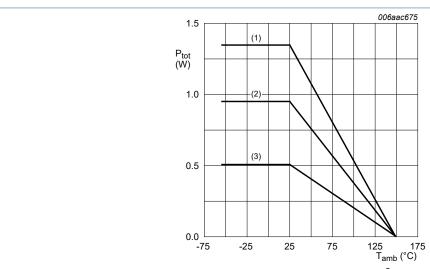
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		-2	Α
I _B	base current			-	-0.3	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-0.3	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.50	W
			[2]	-	0.95	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm².



- (1) FR4 PCB, single-sided copper, mounting pad for collector 6 cm²
- (2) FR4 PCB, single-sided copper, mounting pad for collector 1 cm²
- (3) FR4 PCB, single-sided copper, standard footprint

Fig. 1. Power derating curves

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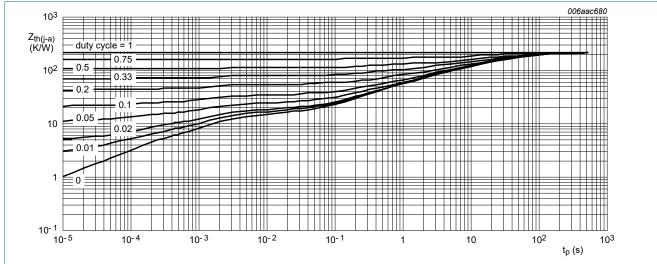
9. Thermal characteristics

Table 6. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

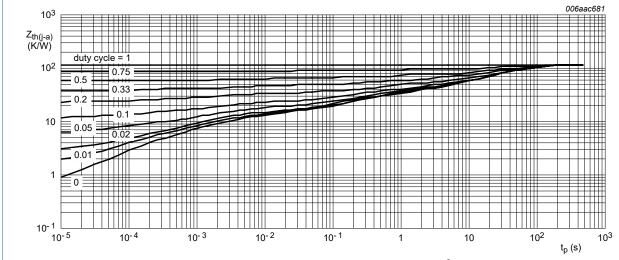
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]	-	-	132	K/W
			[3]	-	-	93	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm².



FR4 PCB; single-sided copper; tin-plated and standard footprint

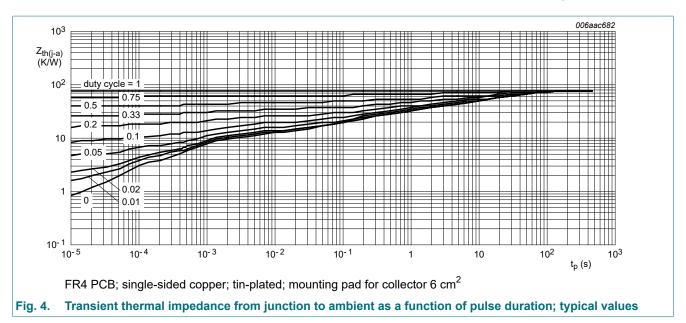
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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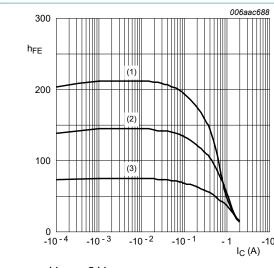
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -30 V; I _E = 0 A T _{amb} = 25 °C		-	-	-100	nA
		V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C		-	-	-10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A T _{amb} = 25 °C		-	-	-100	nA
h _{FE}	DC current gain			·		'	
	BCX52	V _{CE} = -2 V; I _C = -5 mA T _{amb} = 25 °C	[1]	63	-	-	
		V _{CE} = -2 V; I _C = -150 mA T _{amb} = 25 °C		63	-	250	
		V _{CE} = -2 V; I _C = -500 mA T _{amb} = 25 °C		40	-	-	
	BCX52-10	V _{CE} = -2 V; I _C = -5 mA T _{amb} = 25 °C	[1]	63	-	-	
		V _{CE} = -2 V; I _C = -150 mA T _{amb} = 25 °C		63	-	160	
		V _{CE} = -2 V; I _C = -500 mA T _{amb} = 25 °C		40	-	-	
	BCX52-16	V _{CE} = -2 V; I _C = -5 mA T _{amb} = 25 °C	[1]	63	-	-	
		V _{CE} = -2 V; I _C = -150 mA T _{amb} = 25 °C		100	-	250	
		V _{CE} = -2 V; I _C = -500 mA T _{amb} = 25 °C		40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -500 mA; I_B = -50 mA T_{amb} = 25 °C	[1]	-	-	-0.5	V
V_{BE}	base-emitter voltage	V _{CE} = -2 V; I _C = -500 mA T _{amb} = 25 °C	[1]	-	-	-1	V
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz T _{amb} = 25 °C		-	15	-	pF
f _T	transition frequency	V _{CE} = -5 V; I _C = -50 mA; f = 100 MHz T _{amb} = 25 °C		-	145	-	MHz

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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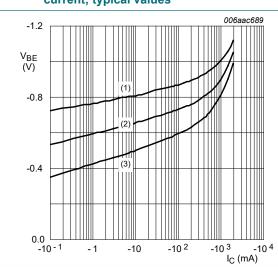
$$V_{CE} = -2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 5. DC current gain as a function of collector current; typical values



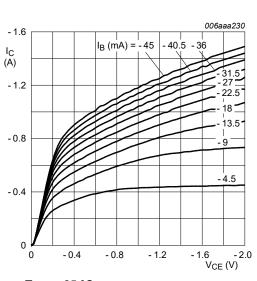
$$V_{CE} = -2 V$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb}$$
 = 25 °C

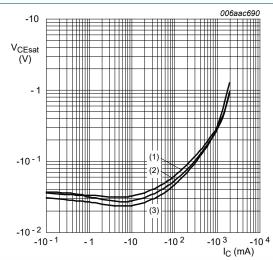
(3)
$$T_{amb}$$
 = 100 °C

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

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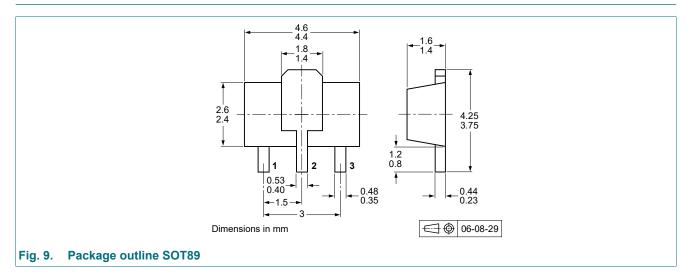
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11. Test information

11.1. Quality information

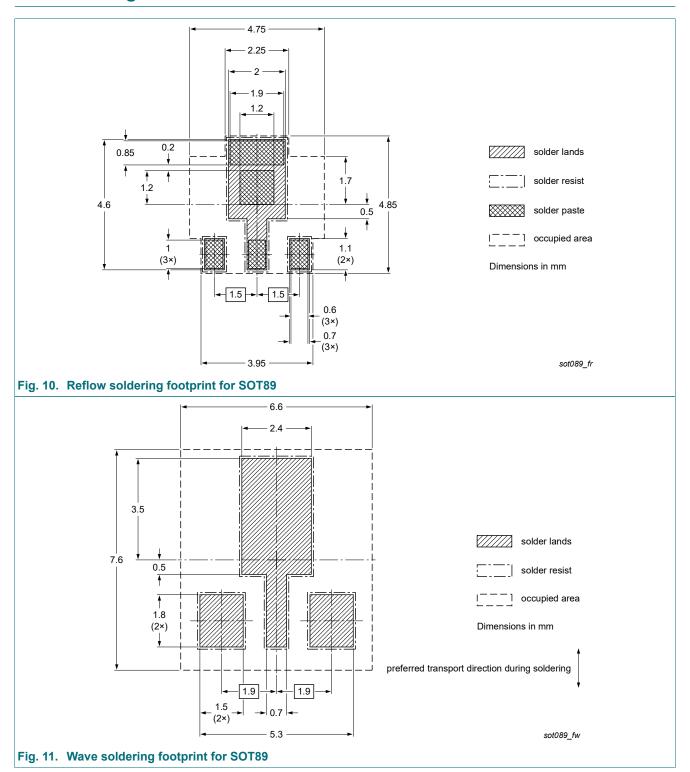
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BXP52_SER v.10	20240530	Product data sheet	-	BCP52_BCX52_BC52PA v.9
Modifications:		arated into 3 data sheets g information" removed		
BCP52_BCX52_BC52PA v.9	20111018	Product data sheet	-	BCP52_BCX52 v.8
BCP52_BCX52 v.8	20080225	Product data sheet	-	BC638_BCP52_BCX52 v.7
BC638_BCP52_BCX52 v.7	20070626	Product data sheet	-	BC638_BCP52_BCX52 v.6
BC638_BCP52_BCX52 v.6	20060329	Product data sheet	CPCN200405 029	BC636_638_640 v.5 BCP51_52_53 v.5 BCX51_52_53 v.4
BC636_638_640 v.5	20041011	Product specification	-	BCX51_52_53 v.5
BCX51_52_53 v.5	20030206	Product specification	-	BCX51_52_53 v.4
BCX51_52_53 v.4	20011010	Product specification	-	BCX51_52_53 v.3

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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BCX52_SER

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