# ne<mark>x</mark>peria

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Dear Customer,

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In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

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Should be replaced with:

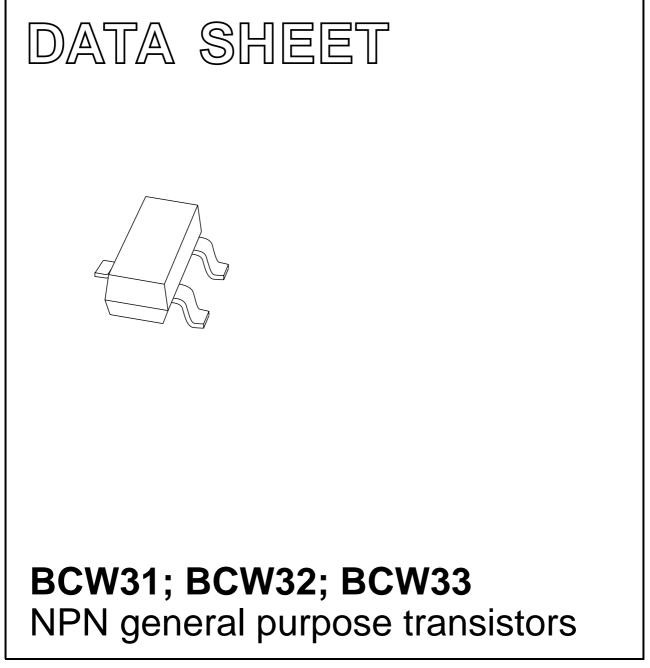
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If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 2000 Jul 04 2004 Feb 06



## FEATURES

- Low current (100 mA)
- Low voltage (32 V).

# APPLICATIONS

• General purpose switching and amplification.

## DESCRIPTION

NPN transistors in a plastic SOT23 package. PNP complements: BCW29 and BCW30.

#### MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
BCW31	D1*
BCW32	D2*
BCW33	D3*

## Note

- 1. \* = p : Made in Hong Kong.
  - \* = t : Made in Malaysia.
  - \* = W : Made in China.

# **ORDERING INFORMATION**

# TYPE NUMBER NAME PACKAGE BCW31 plastic surface mounted package; 3 leads SOT23 BCW32 BCW33 plastic surface mounted package; 3 leads SOT23

### LIMITING VALUES

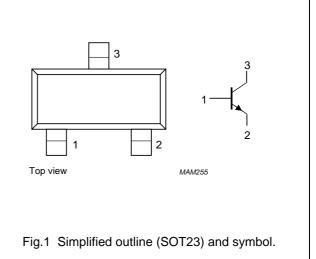
In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	32	V
V <sub>CEO</sub>	collector-emitter voltage	open base; I <sub>C</sub> = 2 mA	-	32	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	5	V
I <sub>C</sub>	collector current (DC)		-	100	mA
I <sub>CM</sub>	peak collector current		-	200	mA
I <sub>BM</sub>	peak base current		-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

# BCW31; BCW32; BCW33

PINNING
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PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



# BCW31; BCW32; BCW33

# THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

## Note

1. Transistor mounted on an FR4 printed-circuit board.

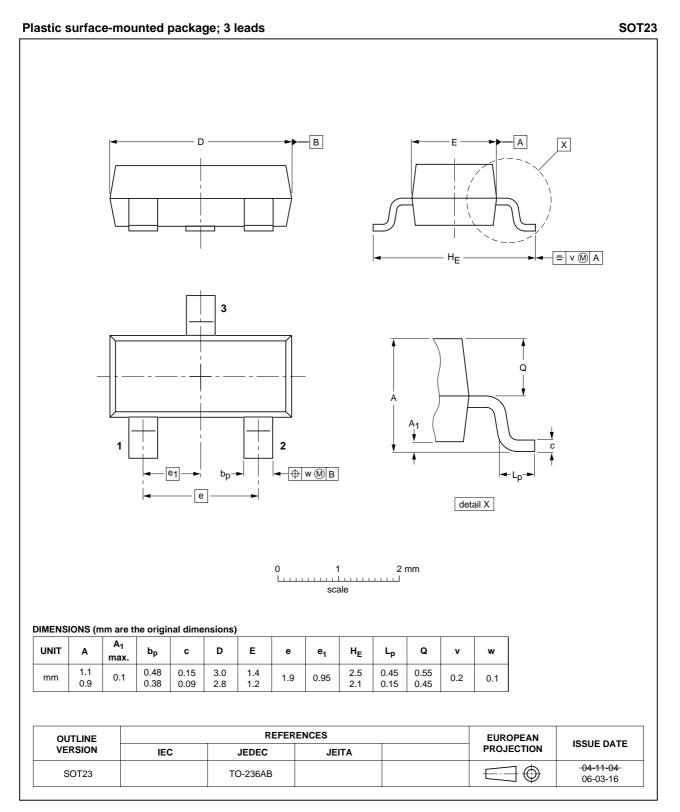
## CHARACTERISTICS

 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 32 V	_	-	100	nA
		$I_E = 0; V_{CB} = 32 V; T_j = 100 \ ^{\circ}C$	_	_	10	μA
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	-	-	100	nA
h <sub>FE</sub>	DC current gain	$I_{C} = 10 \ \mu A; V_{CE} = 5 \ V$				
	BCW31		-	190	-	
	BCW32		-	330	_	
	BCW33		-	600	_	
	DC current gain	I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V				
	BCW31		110	_	220	
	BCW32		200	_	450	
	BCW33		420	_	800	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{\rm C}$ = 10 mA; $I_{\rm B}$ = 0.5 mA	_	120	250	mV
		I <sub>C</sub> = 50 mA; I <sub>B</sub> = 2.5 mA	_	210	_	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA	_	750	_	mV
		$I_{\rm C} = 50 \text{ mA}; I_{\rm B} = 2.5 \text{ mA}$	_	850	_	mV
V <sub>BE</sub>	base-emitter voltage	I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V	550	_	700	mV
C <sub>c</sub>	collector capacitance	$I_E = I_e = 0; V_{CB} = 10 V; f = 1 MHz$	_	2.5	_	pF
f <sub>T</sub>	transition frequency	$I_{C} = 10 \text{ mA}; V_{CE} = 5 \text{ V};$ f = 100 MHz	100	_	-	MHz
F	noise figure	$    I_{C} = 200 \ \mu\text{A}; \ V_{CE} = 5 \ \text{V}; \\ R_{S} = 2 \ \text{k}\Omega; \ \text{f} = 1 \ \text{kHz}; \ \text{B} = 200 \ \text{Hz} $	-	-	10	dB

# BCW31; BCW32; BCW33

# PACKAGE OUTLINE



BCW31; BCW32; BCW33

# DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

### Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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# NXP Semiconductors

## **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

### **Contact information**

For additional information please visit: http://www.nxp.com For sales offices addresses send e-mail to: salesaddresses@nxp.com

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