**Product data sheet** 

## 1. General description

PNP medium power transistors in a SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- High current
- Three current gain selections
- · High power dissipation capability
- AEC-Q101 qualified

# 3. Applications

- Linear voltage regulators
- · High-side switches
- Battery-driven devices
- · Power management
- MOSFET drivers
- Amplifiers

## 4. Quick reference data

#### Table 1. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base		-	-	-60	V
I <sub>C</sub>	collector current			-	-	-1	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	-2	Α
h <sub>FE</sub>	DC current gain						
	BCP52	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	[1]	63	-	250	
	BCP52-10	T <sub>amb</sub> = 25 °C	[1]	63	-	160	
	BCP52-16		[1]	100	-	250	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 



## 60 V, 1 A PNP medium power transistors

# 5. Pinning information

### Table 2. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	C
2	С	collector		p /
3	E	emitter		□ ¬¬¬
4	С	collector	∃1 ∃2 ∃3	Ė
				sym028

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
BCP52	-	plastic, surface-mounted package with increased heatsink;	SOT223		
BCP52-10		4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body			
BCP52-16					

# 7. Marking

### Table 4. Marking

Type number	Marking code
BCP52	BCP52
BCP52-10	BCP52/10
BCP52-16	BCP52/16

## 60 V, 1 A PNP medium power transistors

## 8. Limiting values

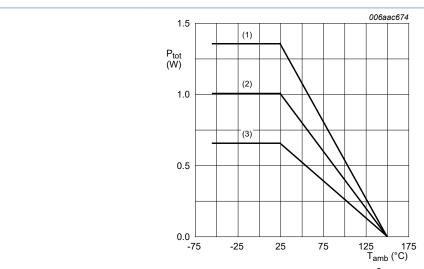
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	open emitter		-60	V
$V_{CEO}$	collector-emitter voltage	open base		-	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-1	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-2	Α
I <sub>B</sub>	base current			-	-0.3	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-0.3	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB, single-sided copper, mounting pad for collector 6 cm<sup>2</sup>
- (2) FR4 PCB, single-sided copper, mounting pad for collector 1 cm<sup>2</sup>
- (3) FR4 PCB, single-sided copper, standard footprint

Fig. 1. Power derating curves

#### 60 V, 1 A PNP medium power transistors

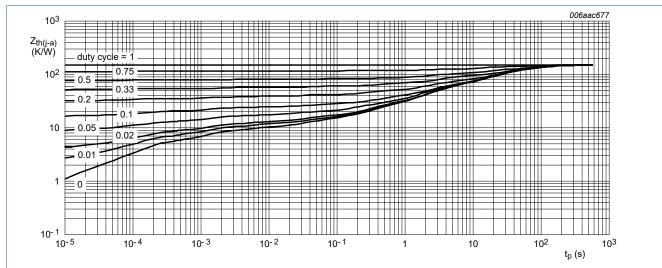
## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

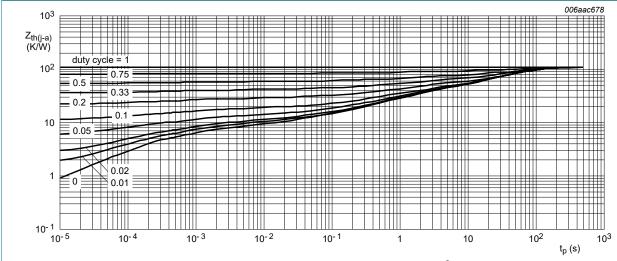
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]	-	-	125	K/W
			[3]	-	-	93	K/W
R <sub>(j-sp)</sub>	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
- Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



FR4 PCB; single-sided copper; tin-plated and standard footprint

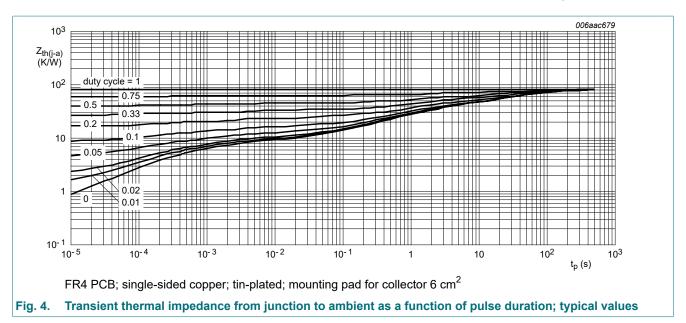
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 60 V, 1 A PNP medium power transistors



## 60 V, 1 A PNP medium power transistors

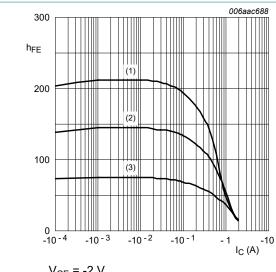
# 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A T <sub>amb</sub> = 25 °C		-	-	-100	nA
		V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A T <sub>amb</sub> = 25 °C		-	-	-100	nA
h <sub>FE</sub>	DC current gain					'	'
	BCP52	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -5 mA T <sub>amb</sub> = 25 °C	[1]	63	-	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA T <sub>amb</sub> = 25 °C		63	-	250	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA T <sub>amb</sub> = 25 °C		40	-	-	
	BCP52-10	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -5 mA T <sub>amb</sub> = 25 °C	[1]	63	-	-	
		$V_{CE}$ = -2 V; $I_{C}$ = -150 mA $T_{amb}$ = 25 °C		63	-	160	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA T <sub>amb</sub> = 25 °C		40	-	-	
	BCP52-16	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -5 mA T <sub>amb</sub> = 25 °C	[1]	63	-	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA T <sub>amb</sub> = 25 °C		100	-	250	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA T <sub>amb</sub> = 25 °C		40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C$ = -500 mA; $I_B$ = -50 mA $T_{amb}$ = 25 °C	[1]	-	-	-0.5	V
$V_{BE}$	base-emitter voltage	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA T <sub>amb</sub> = 25 °C	[1]	-	-	-1	V
C <sub>c</sub>	collector capacitance	$V_{CB}$ = -10 V; $I_{E}$ = $i_{e}$ = 0 A; f = 1 MHz $T_{amb}$ = 25 °C		-	15	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -50 mA; f = 100 MHz T <sub>amb</sub> = 25 °C		-	145	-	MHz

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

### 60 V, 1 A PNP medium power transistors

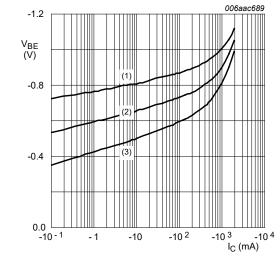


$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

DC current gain as a function of collector Fig. 5. current; typical values



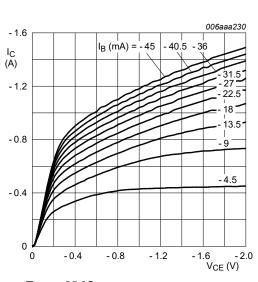
$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb}$$
 = 25 °C

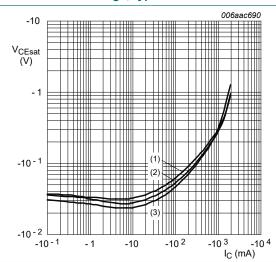
(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

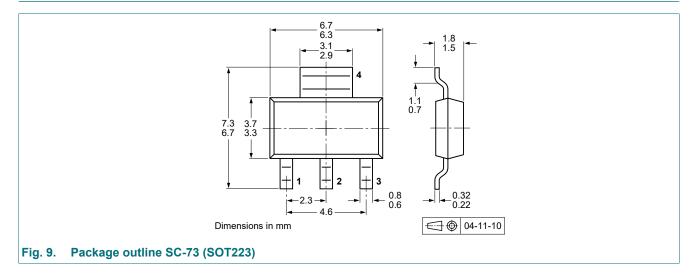
60 V, 1 A PNP medium power transistors

## 11. Test information

## 11.1. Quality information

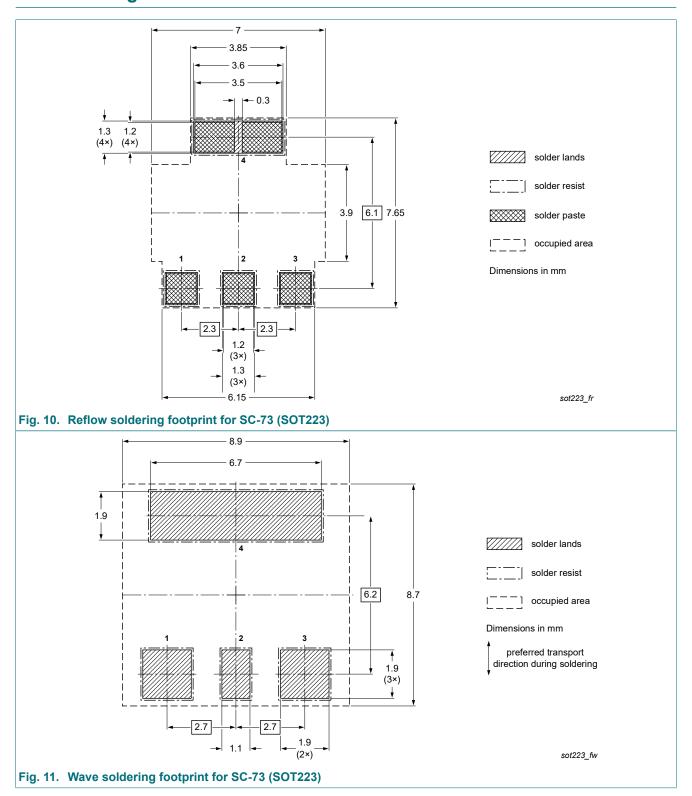
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



### 60 V, 1 A PNP medium power transistors

# 13. Soldering



## 60 V, 1 A PNP medium power transistors

# 14. Revision history

#### **Table 8. Revision history**

Tubic o. Itevision mistory		ı	T			
Document ID	Release date	Data sheet status	Change notice	Supersedes		
BCP52_SER v.10	20240530	Product data sheet	-	BCP52_BCX52_BC52PA v.9		
Modifications:	<ul><li>Data sheet separated into 3 data sheets</li><li>Section "Packing information" removed</li></ul>					
BCP52_BCX52_BC52PA v.9	20111018	Product data sheet	-	BCP52_BCX52 v.8		
BCP52_BCX52 v.8	20080225	Product data sheet	-	BC638_BCP52_BCX52 v.7		
BC638_BCP52_BCX52 v.7	20070626	Product data sheet	-	BC638_BCP52_BCX52 v.6		
BC638_BCP52_BCX52 v.6	20060329	Product data sheet	CPCN200405 029	BC636_638_640 v.5 BCP51_52_53 v.5 BCX51_52_53 v.4		
BC636_638_640 v.5	20041011	Product specification	-	BCX51_52_53 v.5		
BCX51_52_53 v.5	20030206	Product specification	-	BCX51_52_53 v.4		
BCX51_52_53 v.4	20011010	Product specification	-	BCX51_52_53 v.3		

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BCP52\_SER

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