



BCM856BS

65 V, 100 mA PNP/PNP matched double transistors

18 October 2024

Product data sheet

1. General description

PNP/PNP matched double transistors in SOT363 (SC-88) small Surface-Mounted Device (SMD) plastic package. The transistors are fully isolated internally.

2. Features and benefits

- Current gain matching
- Base-emitter voltage matching
- Drop-in replacement for standard double transistors
- AEC-Q101 qualified

3. Applications

- Current mirror
- Differential amplifier

4. Quick reference data

Table 1. Quick reference data

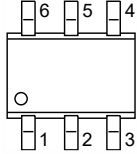
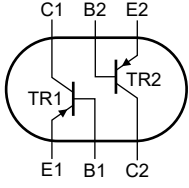
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
V_{CE0}	collector-emitter voltage	open base	-	-	-65	V
I_C	collector current		-	-	-100	mA
h_{FE}	DC current gain	$V_{CE} = -5\text{ V}; I_C = -2\text{ mA}; T_{amb} = 25\text{ °C}$	200	290	450	
Per device						
h_{FE1}/h_{FE2}	DC current gain matching	$V_{CE} = -5\text{ V}; I_C = -2\text{ mA}; T_{amb} = 25\text{ °C}$	[1]	0.9	1	-
$V_{BE1}-V_{BE2}$	base-emitter voltage matching		[2]	-	-	2 mV

[1] The smaller of the two values is taken as the numerator.

[2] The smaller of the two values is subtracted from the larger value.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	 <p>TSSOP6 (SOT363)</p>	 <p>sym018</p>
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCM856BS	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363

7. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
BCM856BS	%BS

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transistor						
V_{CBO}	collector-base voltage	open emitter		-	-80	V
V_{CEO}	collector-emitter voltage	open base		-	-65	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-100	mA
I_{CM}	peak collector current	$t_p \leq 1$ ms; single pulse		-	-200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	200	mW
Per device						
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	300	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
I_{CBO}	collector-base cut-off current	$V_{CB} = -30\text{ V}; I_E = 0\text{ A}; T_{amb} = 25\text{ }^\circ\text{C}$	-	-	-15	nA
		$V_{CB} = -30\text{ V}; I_E = 0\text{ A}; T_j = 150\text{ }^\circ\text{C}$	-	-	-5	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -5\text{ V}; I_C = 0\text{ A}; T_{amb} = 25\text{ }^\circ\text{C}$	-	-	-100	nA
h_{FE}	DC current gain	$V_{CE} = -5\text{ V}; I_C = -10\text{ }\mu\text{A}; T_{amb} = 25\text{ }^\circ\text{C}$	-	250	-	
		$V_{CE} = -5\text{ V}; I_C = -2\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$	200	290	450	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -10\text{ mA}; I_B = -0.5\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$	-	-50	-200	mV
		$I_C = -100\text{ mA}; I_B = -5\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$	-	-200	-400	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = -10\text{ mA}; I_B = -0.5\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$ [1]	-	-760	-	mV
		$I_C = -100\text{ mA}; I_B = -5\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$ [1]	-	-920	-	mV
V_{BE}	base-emitter voltage	$V_{CE} = -5\text{ V}; I_C = -2\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$ [2]	-600	-650	-700	mV
		$V_{CE} = -5\text{ V}; I_C = -10\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$ [2]	-	-	-760	mV
C_c	collector capacitance	$V_{CB} = -10\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A};$ $f = 1\text{ MHz}; T_{amb} = 25\text{ }^\circ\text{C}$	-	-	2.2	pF
C_e	emitter capacitance	$V_{EB} = -0.5\text{ V}; I_C = 0\text{ A}; i_c = 0\text{ A};$ $f = 1\text{ MHz}; T_{amb} = 25\text{ }^\circ\text{C}$	-	10	-	pF
f_T	transition frequency	$V_{CE} = -5\text{ V}; I_C = -10\text{ mA}; f = 100\text{ MHz};$ $T_{amb} = 25\text{ }^\circ\text{C}$	100	175	-	MHz
NF	noise figure	$V_{CE} = -5\text{ V}; I_C = -0.2\text{ mA}; R_S = 2\text{ k}\Omega; f =$ $10\text{ Hz to }15.7\text{ kHz}$	-	1.6	-	dB
		$V_{CE} = -5\text{ V}; I_C = -0.2\text{ mA}; R_S = 2\text{ k}\Omega;$ $B = 200\text{ Hz}; f = 1\text{ kHz}$	-	3.1	-	dB
Per device						
h_{FE1}/h_{FE2}	DC current gain matching	$V_{CE} = -5\text{ V}; I_C = -2\text{ mA}; T_{amb} = 25\text{ }^\circ\text{C}$	[3]	0.9	1	-
$V_{BE1}-V_{BE2}$	base-emitter voltage matching		[4]	-	-	2 mV

[1] V_{BEsat} decreases by about 1.7 mV/K with increasing temperature.

[2] V_{BE} decreases by about 2 mV/K with increasing temperature.

[3] The smaller of the two values is taken as the numerator.

[4] The smaller of the two values is subtracted from the larger value.

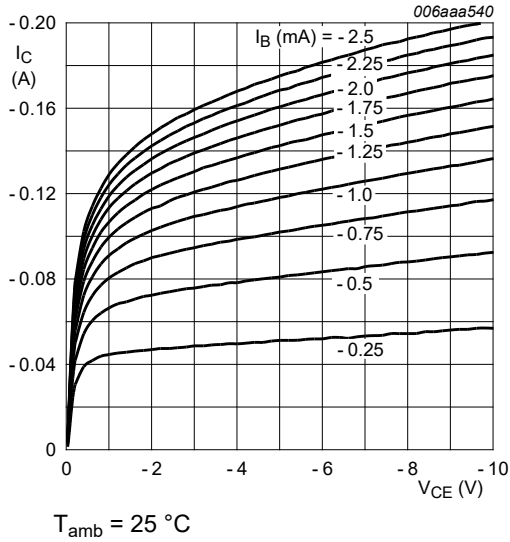


Fig. 1. Collector current as a function of collector-emitter voltage; typical values

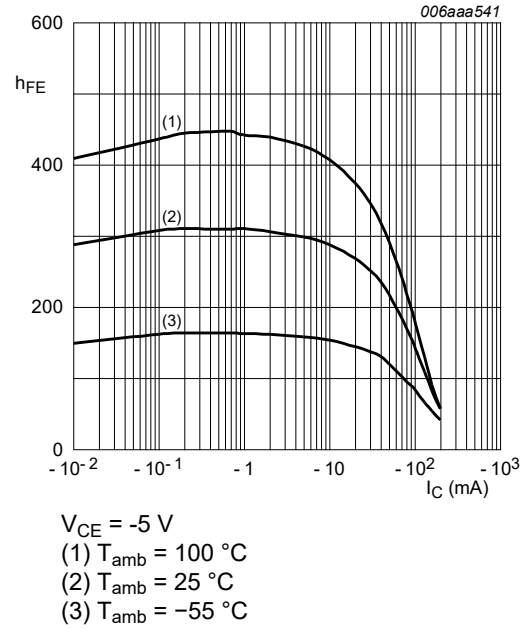


Fig. 2. DC current gain as a function of collector current; typical values

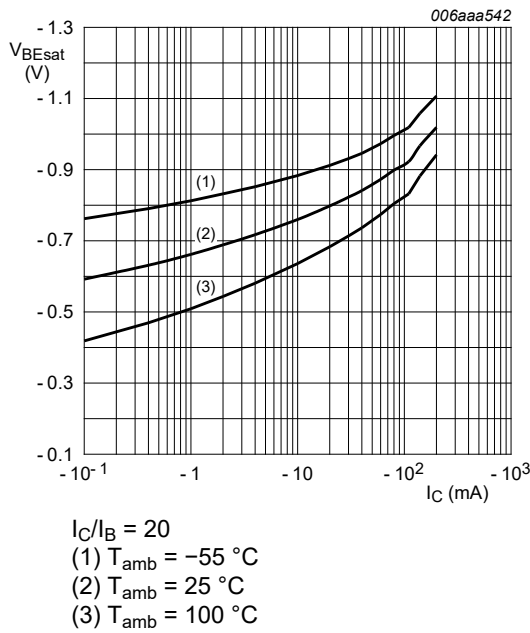


Fig. 3. Base-emitter saturation voltage as a function of collector current; typical values

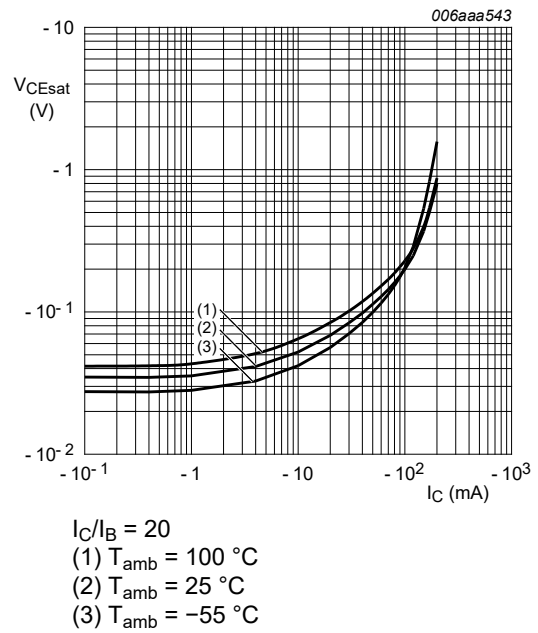
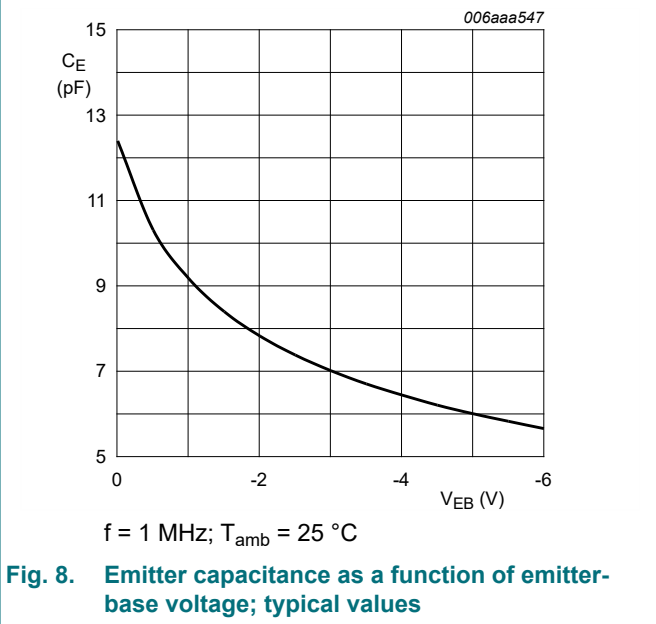
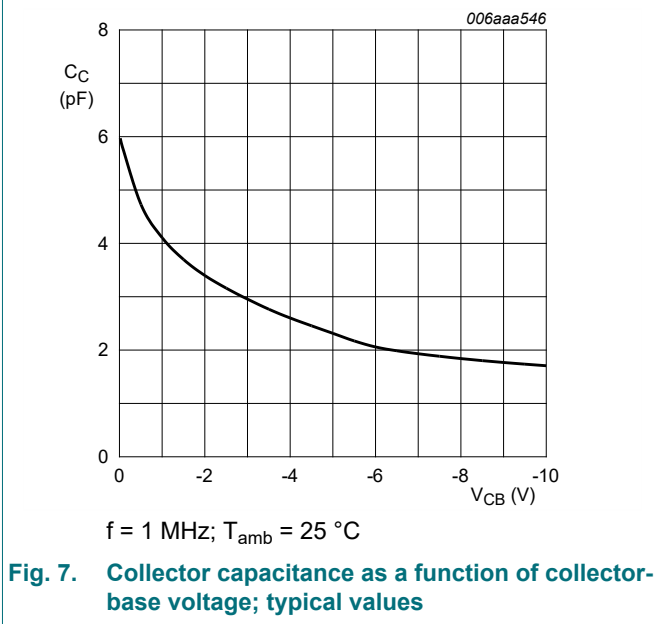
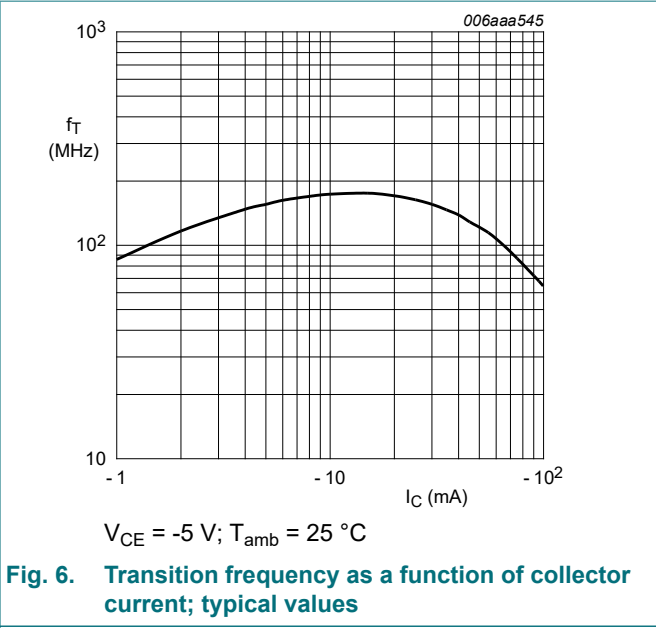
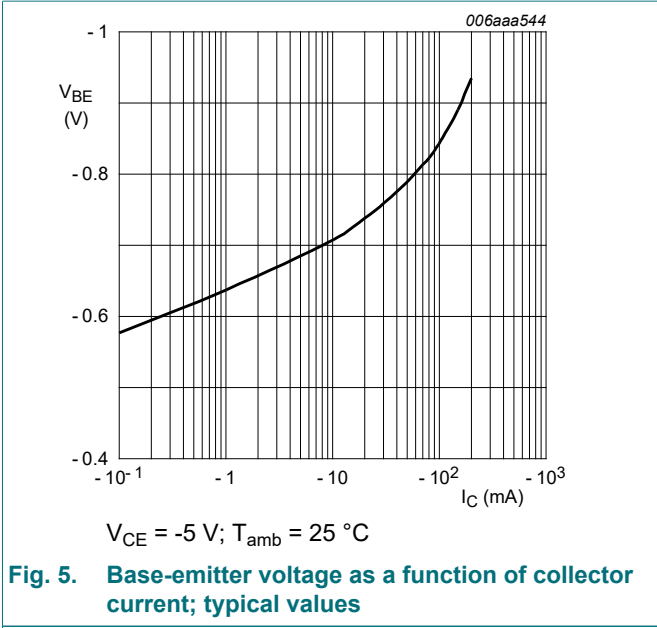


Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values



11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

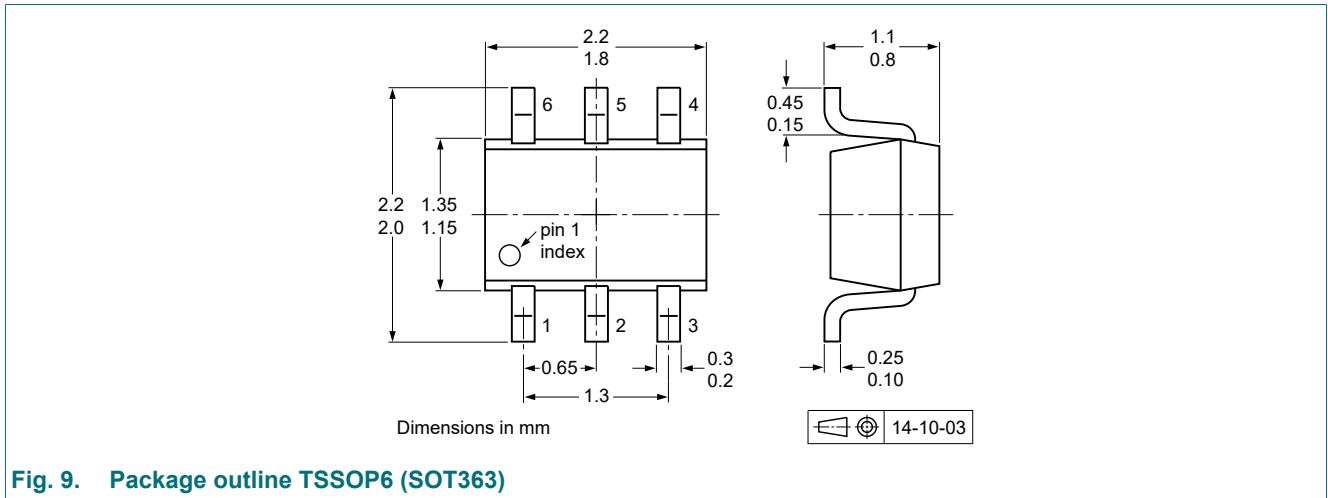


Fig. 9. Package outline TSSOP6 (SOT363)

13. Soldering

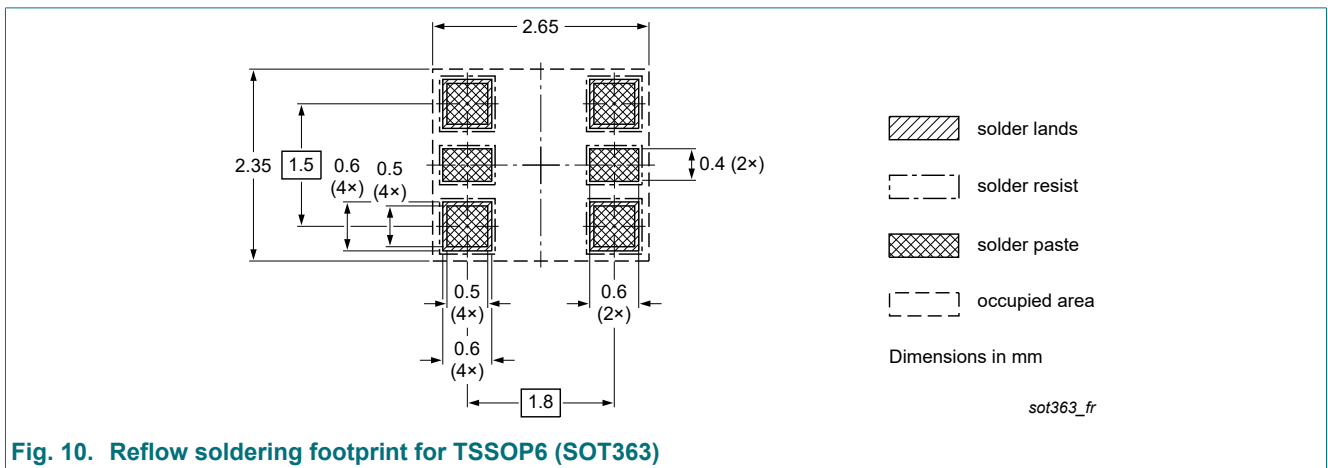


Fig. 10. Reflow soldering footprint for TSSOP6 (SOT363)

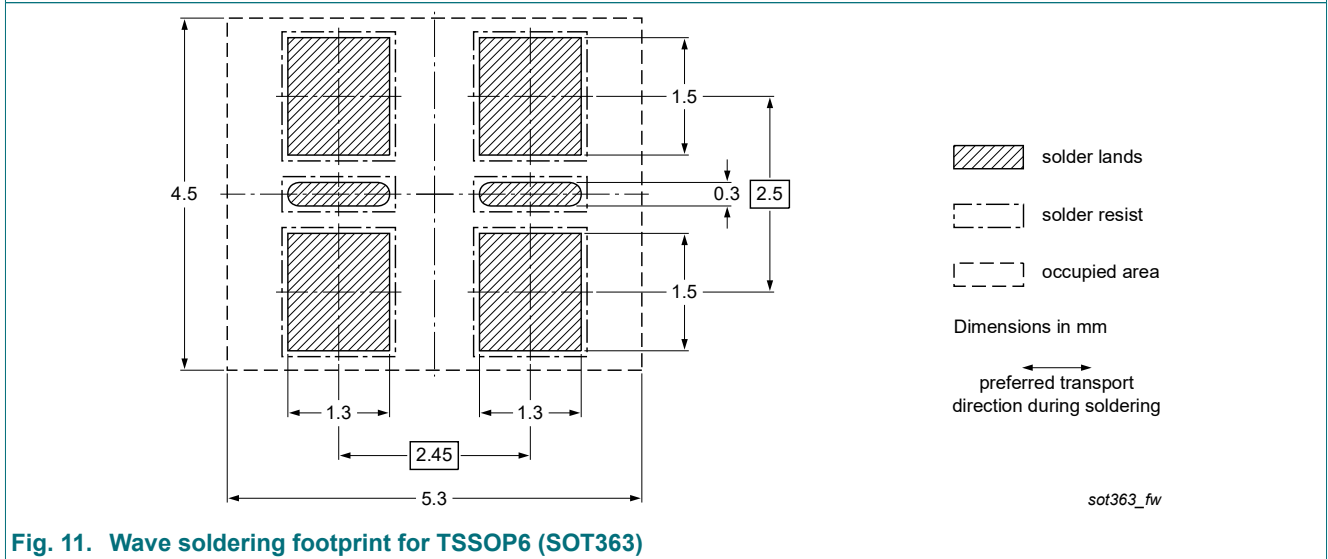


Fig. 11. Wave soldering footprint for TSSOP6 (SOT363)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCM856BS v.2	20241018	Product data sheet	-	BCM856BS_BCM856D S_1
Modifications:	<ul style="list-style-type: none">Family data sheet reduced to single type data sheet.Section "Packing information" removed.			
BCM856BS_BCM856D S_1	20080807	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	3
9. Thermal characteristics.....	3
10. Characteristics.....	4
11. Test information.....	6
12. Package outline.....	7
13. Soldering.....	7
14. Revision history.....	8
15. Legal information.....	9

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