Product data sheet

1. General description

NPN double transistor in a very small SOT363 (SC-88) plastic six lead package.

2. Features and benefits

- Two transistors in one package
- · Reduces number of components and board space
- · No mutual interference between the transistors

3. Applications

· General purpose switching and small signal amplification.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
V _{CEO}	collector-emitter voltage	open base	-	-	65	V
I _C	collector current		-	-	100	mA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	110	-	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1	6 5 4	
3	C2	collector TR2		(TR1) TR2)
4	E2	emitter TR2		
5	B2	base TR2	∐1 ∐2 ∐3	
6	C1	collector TR1	TSSOP6 (SOT363)	sym140



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6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC846S		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BC846S	4F%

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transiste	or					
V _{CBO}	collector-base voltage	open emitter		-	80	V
V _{CEO}	collector-emitter voltage	open base		-	65	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
Per device					•	
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW

 $^{[1] \}quad \text{Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, } 35~\mu\text{m copper, tin-plated and standard footprint.}$

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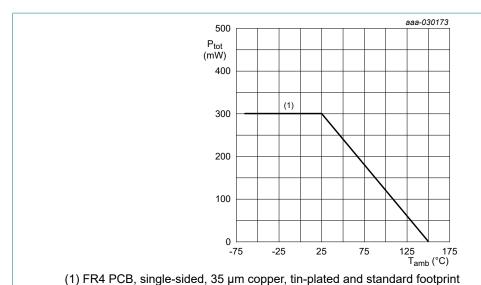


Fig. 1. Per device: Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W
Per transist	tor			'			
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	568	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	230	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

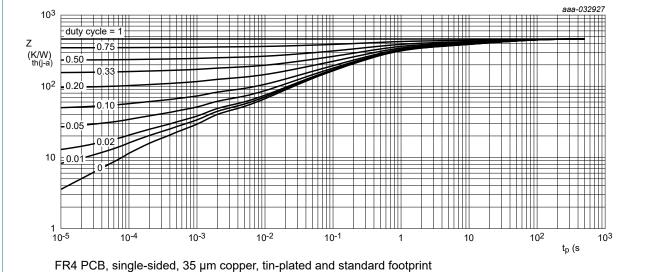


Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	tor		l			
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$	80	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	65	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	$I_C = 0 \text{ A}; I_E = 100 \mu\text{A}; T_{amb} = 25 \text{ °C}$	6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	110	-	-	
V _{CEsat} collector-emitter saturation voltage		I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-	100	mV
	saturation voltage	I_C = 100 mA; I_B = 5 mA; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	-	300	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	770	-	mV
V_{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	580	-	700	mV
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 ^{\circ}\text{C}$	-	-	1.5	pF
f _T	transition frequency	$V_{CE} = 5 \text{ V; } I_{C} = 10 \text{ mA; } f = 100 \text{ MHz;}$ $T_{amb} = 25 \text{ °C}$	100	-	-	MHz

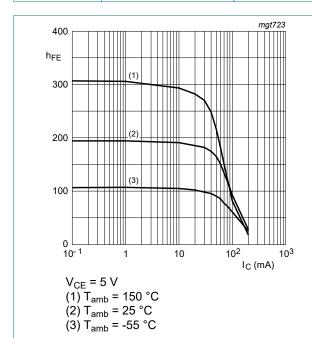


Fig. 3. DC current gain as a function of collector current; typical values

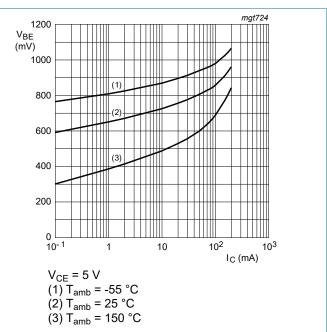


Fig. 4. Base-emitter voltage as a function of collector current; typical values

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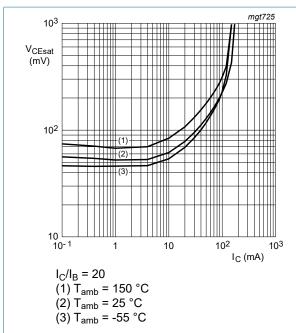


Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values

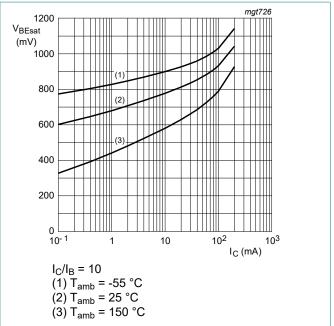
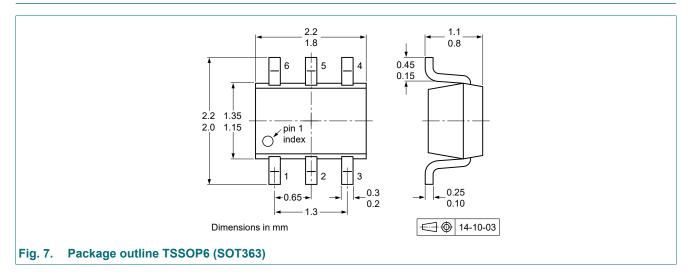


Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

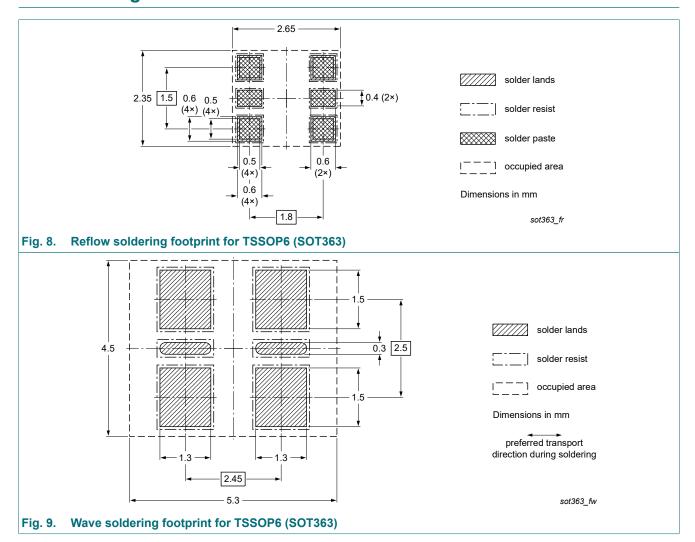
11. Package outline



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12. Soldering



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13. Revision history

Table 8. Revision history

Date of Interest In		D. ()	01	0		
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BC846S v.3	20221001	Product data sheet	-	BC846S v.2		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines Nexperia. Legal texts have been adapted to the new company name where appropriate. Product changed to non-automotive qualification. Please refer to nexperia.com for automotive(-Q) product alternative(s). 					
BC846S v.2	19990901	Product data sheet	-	BC846S v.1		
BC846S v.1	19990528	Product specification	-	-		

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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