



BAT54SW

Schottky barrier diode

1 April 2023

Product data sheet

1. General description

Planar Schottky barrier diode with an integrated guard ring for stress protection, encapsulated in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- Low capacitance

3. Applications

- Ultra high-speed switching
- Line termination
- Voltage clamping
- Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
V_R	reverse voltage	$T_j = 25\text{ °C}$	-	-	30	V
V_F	forward voltage	$I_F = 100\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ °C}$	-	-	800	mV
I_R	reverse current	$V_R = 25\text{ V}$; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25\text{ °C}$	-	-	2	μA

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)	<p>SC-70 (SOT323)</p>	<p>006aaa437</p>
2	K2	cathode (diode 2)		
3	K1, A2	cathode (diode 1) and anode (diode 2)		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BAT54SW	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BAT54SW	44%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode						
V_R	reverse voltage	$T_j = 25\text{ °C}$		-	30	V
I_F	forward current			-	200	mA
I_{FRM}	repetitive peak forward current	$t_p \leq 1\text{ s}; \delta \leq 0.5$		-	300	mA
I_{FSM}	non-repetitive peak forward current	$t_p < 10\text{ ms}; T_{j(\text{init})} = 25\text{ °C}$	[1]	-	600	mA
Per device; one diode loaded						
P_{tot}	total power dissipation	$T_{\text{amb}} \leq 25\text{ °C}$	[2]	-	200	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] $T_j = 25\text{ °C}$ before surge.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

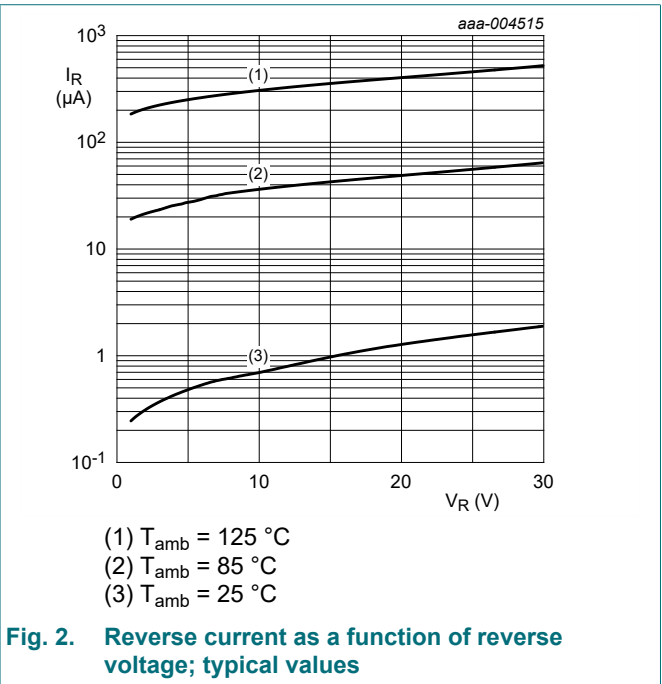
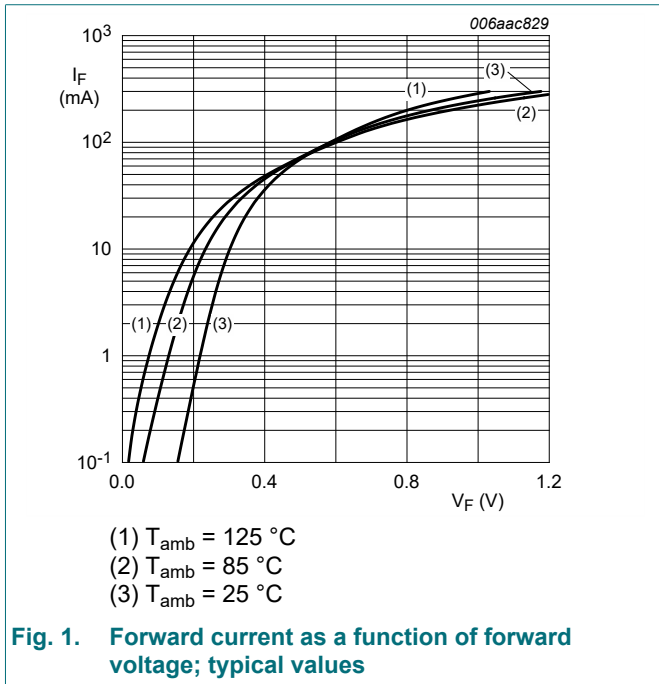
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per device; one diode loaded							
$R_{\text{th}(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

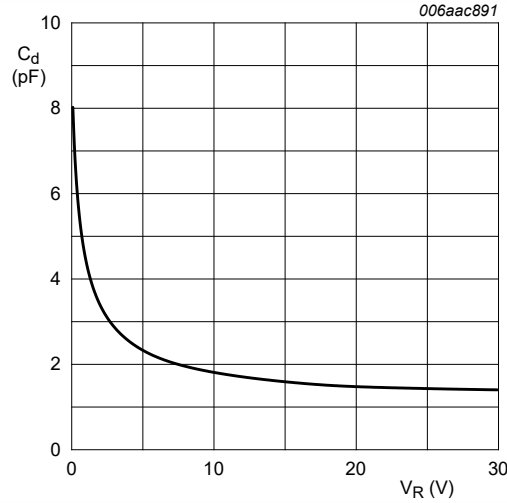
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
V_F	forward voltage	$I_F = 0.1 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	240	mV
		$I_F = 1 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	320	mV
		$I_F = 10 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	400	mV
		$I_F = 30 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	500	mV
		$I_F = 100 \text{ mA}$; pulsed; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	800	mV
I_R	reverse current	$V_R = 25 \text{ V}$; $t_p \leq 300 \mu\text{s}$; $\delta \leq 0.02$; pulsed; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	2	μA
C_d	diode capacitance	$V_R = 1 \text{ V}$; $f = 1 \text{ MHz}$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	10	pF
t_{rr}	reverse recovery time	$I_F = 10 \text{ mA}$; $I_R = 10 \text{ mA}$; $I_{R(\text{meas})} = 1 \text{ mA}$; $R_L = 100 \Omega$; $T_{\text{amb}} = 25 \text{ }^\circ\text{C}$	-	-	5	ns





T_{amb} = 25 °C; f = 1 MHz

Fig. 3. Diode capacitance as a function of reverse voltage; typical values

11. Test information

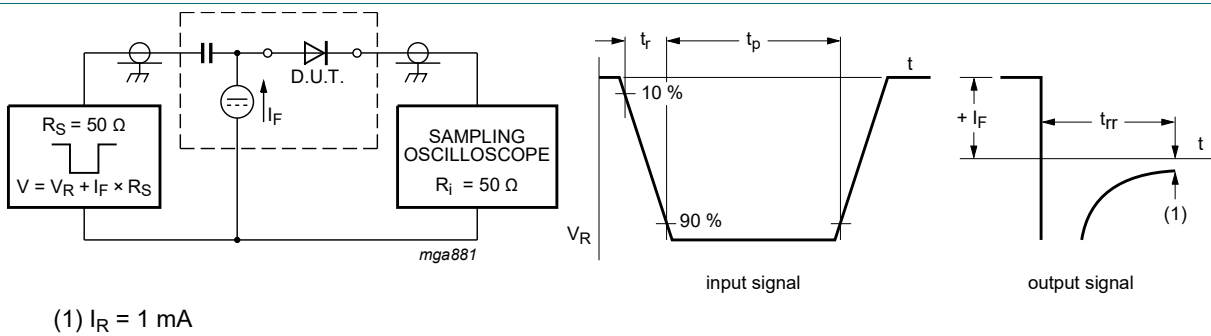


Fig. 4. Reverse recovery time test circuit and waveforms

12. Package outline

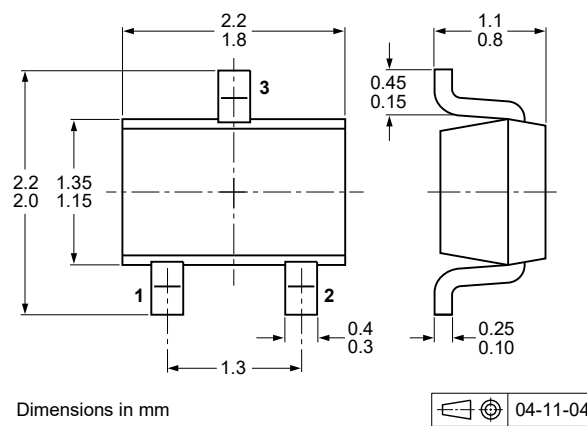


Fig. 5. Package outline SC-70 (SOT323)

13. Soldering

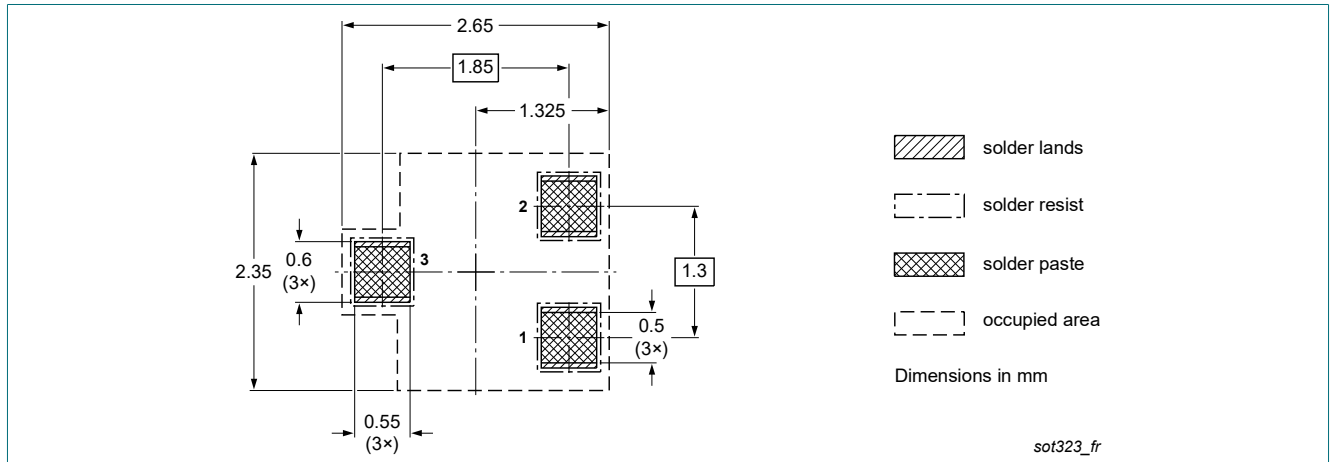


Fig. 6. Reflow soldering footprint for SC-70 (SOT323)

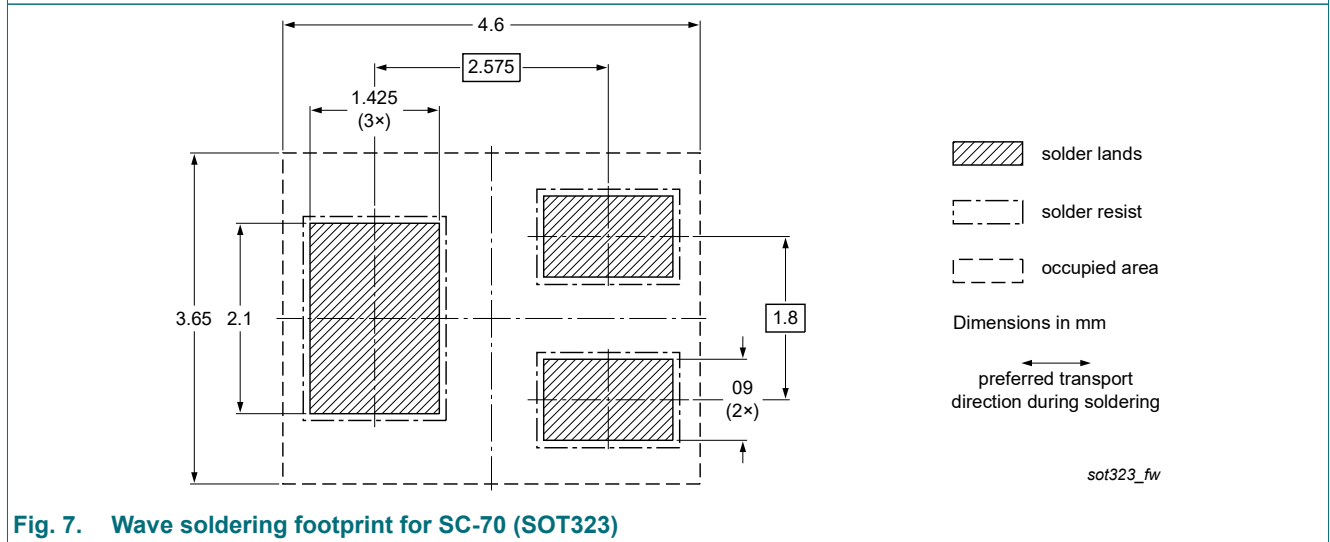


Fig. 7. Wave soldering footprint for SC-70 (SOT323)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT54SW v.4	20230401	Product data sheet	-	BAT54W_SER v.3
Modifications:	<ul style="list-style-type: none">• Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).• Family data sheet splitted to single type data sheets.• Packing information removed.			
BAT54W_SER v.3	20121120	Product data sheet	-	BAT54W v.2
BAT54W v.2	9960319	Product specification	-	BAT54W v.1

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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