Product data sheet

1. General description

Two planar Schottky barrier dual diodes with common cathodes and an integrated guard ring for stress protection encapsulated in a SOT363 ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low forward voltage
- Low capacitance
- Ultra small and flat lead SMD plastic package
- · Excellent coplanarity and improved thermal behavior
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Ultra high-speed switching
- Voltage clamping
- · Line termination
- · Reverse polarity protection

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode			'		'	'	
I _F	forward current		[1]	-	-	200	mA
V_R	reverse voltage			-	-	30	V
V _F	forward voltage	I_F = 0.1 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_{amb} = 25 °C		-	-	240	mV
		I_F = 1 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C		-	-	320	mV
		I_F = 10 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_{amb} = 25 °C		-	-	400	mV
		I_F = 30 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_{amb} = 25 °C		-	-	500	mV
		I_F = 100 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C		-	-	800	mV

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



Two Schottky barrier dual diodes

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode (diode 1)		
2	A2	anode (diode 2)	☐6 ☐5 ☐4	K1; K2 A4 A3
3	K3: K4	common cathode (diode 3 and diode 4)		<u> </u>
4	A3	anode (diode 3)		本 本
5	A4	anode (diode 4)	☐1 ☐2 ☐3	A1 A2 K3; K4
6	K1: K2	common cathode (diode 1 and diode 2)	TSSOP6 (SOT363)	006aaa446

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BAT54CY-Q		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BAT54CY-Q	K8%

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per diode			·		•	
V _R	reverse voltage			-	30	V
IF	forward current		[1]	-	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 10 \text{ ms}; \delta \le 0.5$		-	900	mA
I _{FSM}	non-repetitive peak	t_p = 50 µs; square wave; $T_{j(init)}$ = 25 °C		-	11	А
	forward current	t _p = 10 ms; square wave; T _{j(init)} = 25 °C		-	1.5	А
Per device; one	e diode loaded		·			
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

BAT54CY-Q

Two Schottky barrier dual diodes

9. Thermal characteristics

Table 6. Thermal characteristics

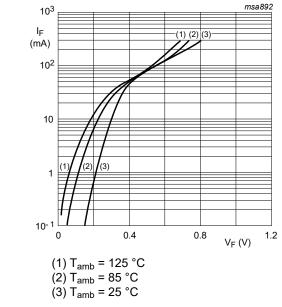
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	-	450	K/W
junction to ambient		[2]	-	-	400	K/W	
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[3]	-	-	140	K/W

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- Soldering points at pins 4, 5 and 6.

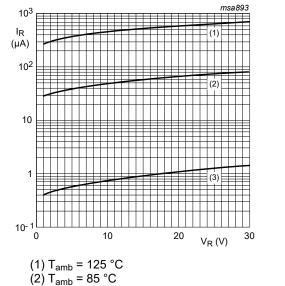
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode		·	•	'		·
V _F	forward voltage	I_F = 0.1 mA; pulsed; $t_p \le 300 \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	-	-	240	mV
		I_F = 1 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	320	mV
		I_F = 10 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	400	mV
		I_F = 30 mA; pulsed; $t_p \le 300$ μs; $δ \le 0.02$; T_{amb} = 25 °C	-	-	500	mV
		I_F = 100 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_{amb} = 25 °C	-	-	800	mV
I _R	reverse current	V _R = 25 V; T _{amb} = 25 °C	-	-	2	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _{amb} = 25 °C	-	-	10	pF



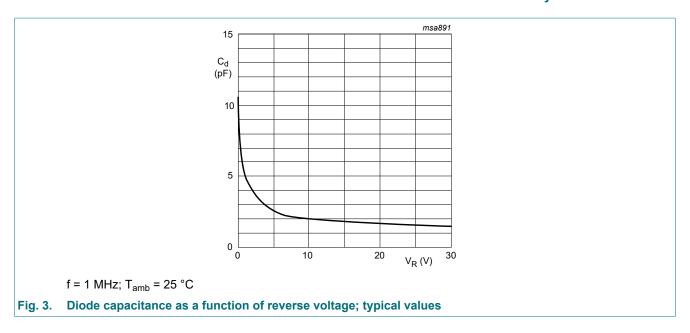




- $(3) T_{amb} = 25 °C$

Fig. 2. Reverse current as a function of reverse voltage; typical values

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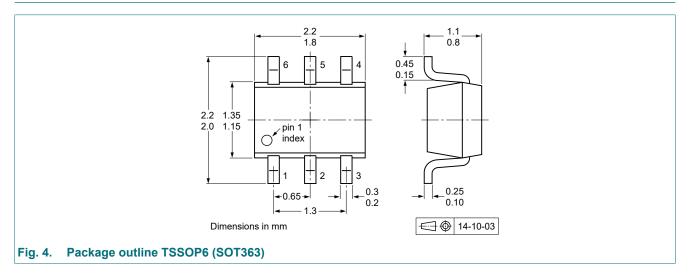


11. Test information

Quality information

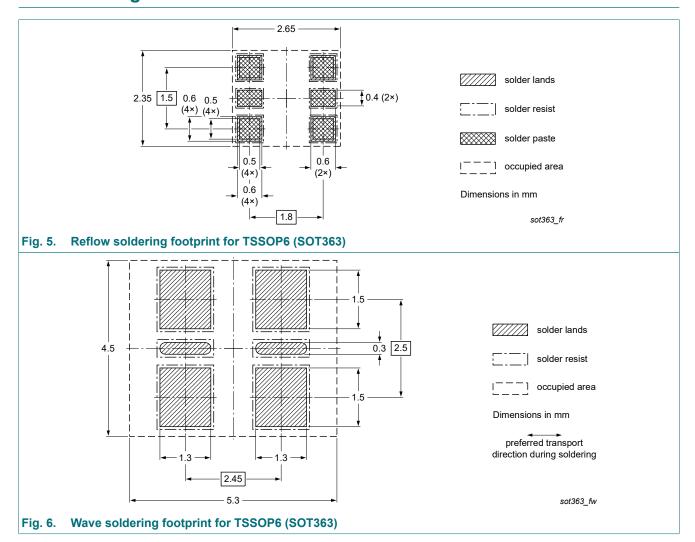
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



Two Schottky barrier dual diodes

13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT54CY-Q v.1	20230420	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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