**Product data sheet** 

# 1. General description

Planar Schottky barrier diode with an integrated guard ring for stress protection, encapsulated in an SOD123 small Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Low forward voltage: V<sub>F</sub> ≤ 850 mV
- Low leakage current: I<sub>R</sub> ≤ 4 μA
- Reverse voltage V<sub>R</sub> ≤ 100 V
- Low capacitance
- Small SMD plastic package
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- · High-speed switching
- · Line termination
- · Voltage clamping
- · Reverse polarity protection

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C	-	-	100	V
I <sub>F</sub>	forward current		-	-	250	mA
V <sub>F</sub>	forward voltage	$I_F$ = 250 mA; $t_p \le 300 \mu s$ ; δ ≤ 0.02; $T_j$ = 25 °C	-	710	850	mV
I <sub>R</sub>	reverse current	$V_R = 75 \text{ V}$ ; pulsed; $T_j = 25 \text{ °C}$	-	1	4	μΑ

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	1 2	к <b>-<del>](  </del> -</b> А
2	Α	anode	SOD123	sym001

[1] The marking bar indicates the cathode.



100 V, 250 mA Schottky barrier diode

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package	ge				
	Name	Description	Version			
BAT46GW-Q	SOD123	plastic, surface-mounted package; 2 leads; 2.675 mm x 1.6 mm x 1.15 mm body	SOD123			

## 7. Marking

### Table 4. Marking codes

Type number	Marking code
BAT46GW-Q	G8

## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	100	V
I <sub>F</sub>	forward current			-	250	mA
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ < 10 ms; square wave; $T_{j(init)}$ = 25 °C		-	2.5	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	390	mW
			[2]	-	660	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
111(J-a)	thermal resistance from junction to ambient	-	[1]	-	-	320	K/W
			[2]	-	-	190	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[3]	-	-	35	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[3]</sup> Soldering point of cathode tab.

### 100 V, 250 mA Schottky barrier diode

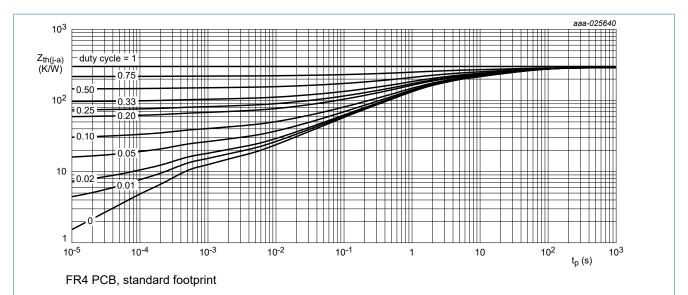


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

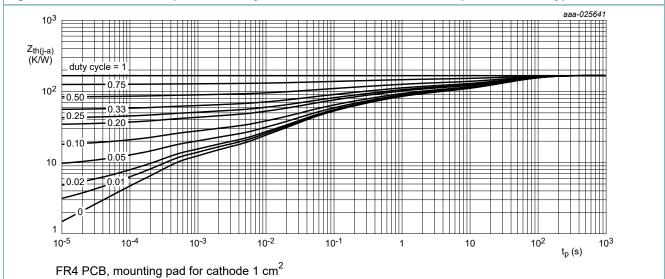


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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# 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	$I_R$ = 1 mA; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C	100	-	-	V
V <sub>F</sub>	forward voltage	$I_F$ = 0.1 mA; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	175	200	mV
		$I_F$ = 10 mA; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	315	350	mV
		$I_F$ = 10 mA; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = -40 °C	-	-	470	mV
		$I_F$ = 50 mA; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = 25 °C	-	415	475	mV
		$I_F$ = 50 mA; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_j$ = -40 °C	-	-	560	mV
		$I_F$ = 250 mA; $t_p \le 300$ μs; $δ \le 0.02$ ; $T_j$ = 25 °C	-	710	850	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 1.5 V; T <sub>j</sub> = 25 °C	-	0.2	0.5	μA
		V <sub>R</sub> = 1.5 V; pulsed; T <sub>j</sub> = 60 °C	-	-	12	μA
		V <sub>R</sub> = 10 V; pulsed; T <sub>j</sub> = 25 °C	-	0.3	0.8	μΑ
		V <sub>R</sub> = 10 V; pulsed; T <sub>j</sub> = 60 °C	-	-	20	μA
		V <sub>R</sub> = 50 V; pulsed; T <sub>j</sub> = 25 °C	-	0.7	2	μA
		V <sub>R</sub> = 50 V; pulsed; T <sub>j</sub> = 60 °C	-	-	44	μA
		V <sub>R</sub> = 75 V; pulsed; T <sub>j</sub> = 25 °C	-	1	4	μA
		V <sub>R</sub> = 75 V; pulsed; T <sub>j</sub> = 60 °C	-	-	80	μA
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 25 °C	-	2	9	μΑ
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 60 °C	-	-	120	μA
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 85 °C	-	-	600	μΑ
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 0 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	-	39	pF
		V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	-	21	pF
t <sub>rr</sub>	reverse recovery time	$I_F$ = 10 mA; $I_R$ = 10 mA; $I_{R(meas)}$ = 1 mA; $I_{L}$ = 100 Ω; $I_{L}$ = 25 °C	-	5.9	-	ns

### 100 V, 250 mA Schottky barrier diode

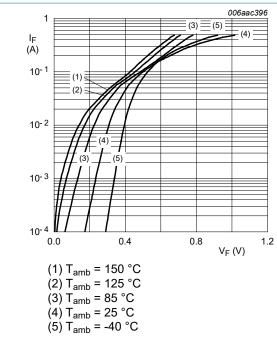
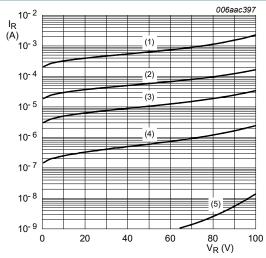


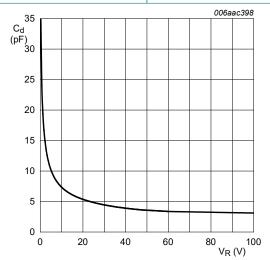
Fig. 3. Forward current as a function of forward voltage; typical values



(1)  $T_{amb}$  = 125 °C

(2) T<sub>amb</sub> = 85 °C (3) T<sub>amb</sub> = 60 °C (4) T<sub>amb</sub> = 25 °C (5) T<sub>amb</sub> = -40 °C

Reverse current as a function of reverse Fig. 4. voltage; typical values

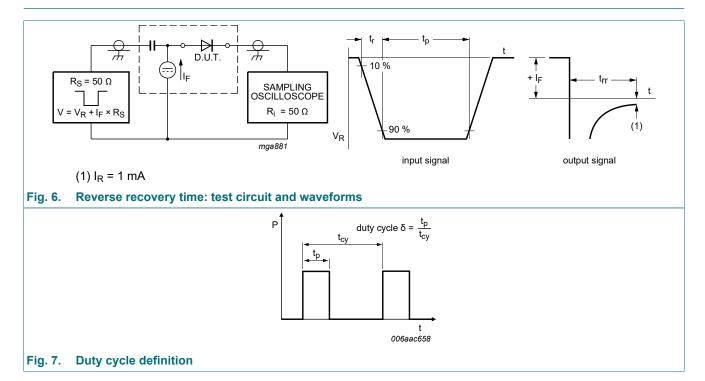


 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ °C}$ 

Fig. 5. Diode capacitance as a function of reverse voltage; typical values

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## 11. Test information

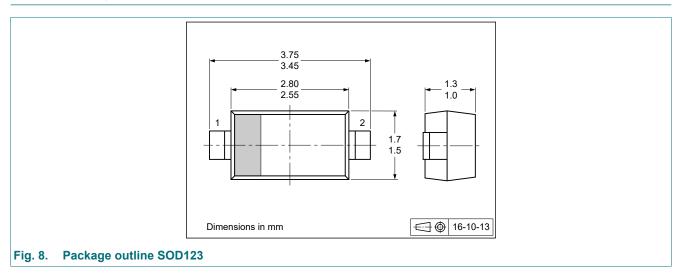


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

## **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

## 12. Package outline



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# 13. Soldering

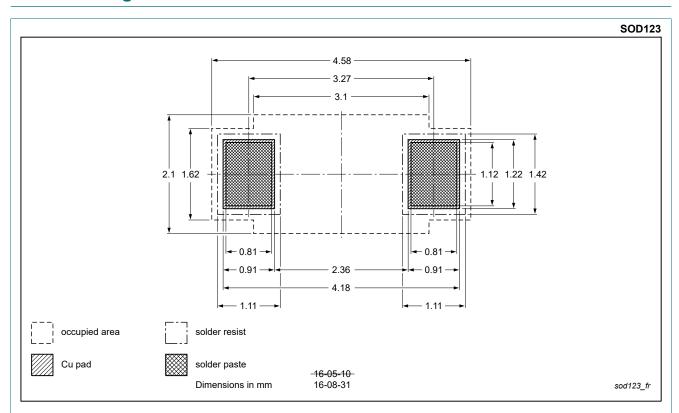


Fig. 9. Reflow soldering footprint for SOD123

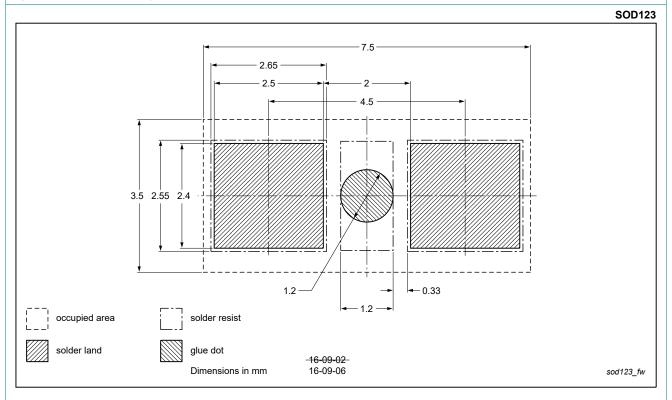


Fig. 10. Wave soldering footprint for SOD123

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# 14. Revision history

### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAT46GW-Q v.1	20230627	Product data sheet	-	-

## 100 V, 250 mA Schottky barrier diode

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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## 100 V, 250 mA Schottky barrier diode

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