



BAS21LD-Q

High-voltage switching diode

29 January 2025

Product data sheet

1. General description

High-voltage switching diode, encapsulated in an leadless ultra small DFN1006D-2 (SOD882D) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

2. Features and benefits

- High switching speed: $t_{rr} \leq 50$ ns
- Low leakage current: $I_R \leq 100$ nA
- High reverse voltage $V_R \leq 200$ V
- Low capacitance: $C_d \leq 2$ pF
- Ultra small and leadless SMD plastic package
- Solderable side pads
- Package height typ. 0.37 mm
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- High-speed switching
- General-purpose switching
- Voltage clamping
- Reverse polarity protection

4. Quick reference data

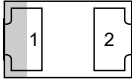

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|-----------|---------------------------------|---|-----|-----|-----|------|------|
| I_F | forward current | $T_j = 25$ °C | [1] | - | - | 330 | mA |
| V_R | reverse voltage | | | - | - | 200 | V |
| V_{RRM} | repetitive peak reverse voltage | | | - | - | 250 | V |
| V_F | forward voltage | $I_F = 200$ mA; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_j = 25$ °C | | - | - | 1.25 | V |
| I_R | reverse current | $V_R = 200$ V; pulsed; $T_j = 25$ °C | | - | - | 100 | nA |
| t_{rr} | reverse recovery time | $I_F = 30$ mA; $I_R = 30$ mA; $R_L = 100$ Ω ; $I_{R(meas)} = 3$ mA; $T_j = 25$ °C | | - | - | 50 | ns |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|---|--|
| 1 | K | cathode |  Transparent top view DFN1006D-2 (SOD882D) |  <i>aaa-028035</i> |
| 2 | A | anode | | |

6. Ordering information

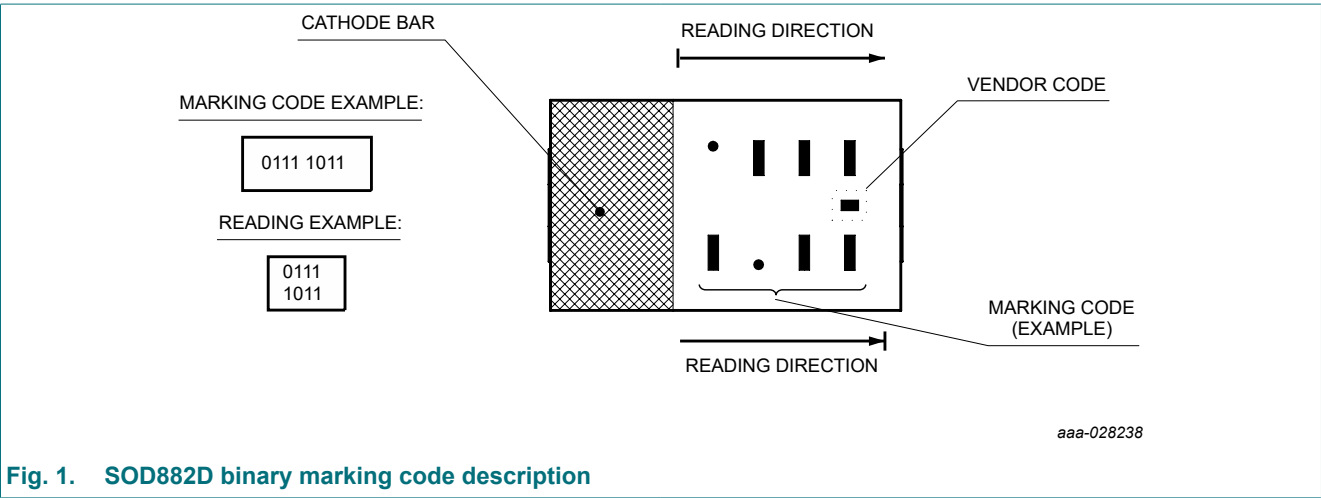
Table 3. Ordering information

| Type number | Package | | |
|-------------|------------|---|---------|
| | Name | Description | Version |
| BAS21LD-Q | DFN1006D-2 | leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.4 mm body | SOD882D |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| BAS21LD-Q | 0110 0001 |



8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating Sytem (IEC 60134)

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|-------------------------------------|---|-----|-----|-----|------|
| V _{RRM} | repetitive peak reverse voltage | T _j = 25 °C | | - | 250 | V |
| V _R | reverse voltage | | | - | 200 | V |
| I _F | forward current | | [1] | - | 330 | mA |
| I _{FSM} | non-repetitive peak forward current | t _p = 50 µs; square wave; T _{j(init)} = 25 °C | | - | 9.4 | A |
| | | t _p = 10 ms; square wave; T _{j(init)} = 25 °C | | - | 1.7 | A |
| I _{FRM} | repetitive peak forward current | t _p ≤ 1 ms; δ ≤ 0.25 | | - | 900 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 335 | mW |
| | | | [2] | - | 610 | mW |
| T _j | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated mounting pad for cathode 1cm².

9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|----------------|--|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | In free air | [1] | - | - | 375 | K/W |
| | | | [2] | - | - | 205 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | [3] | - | - | 40 | K/W |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for cathode 1cm².
[3] Soldering point of cathode tab.

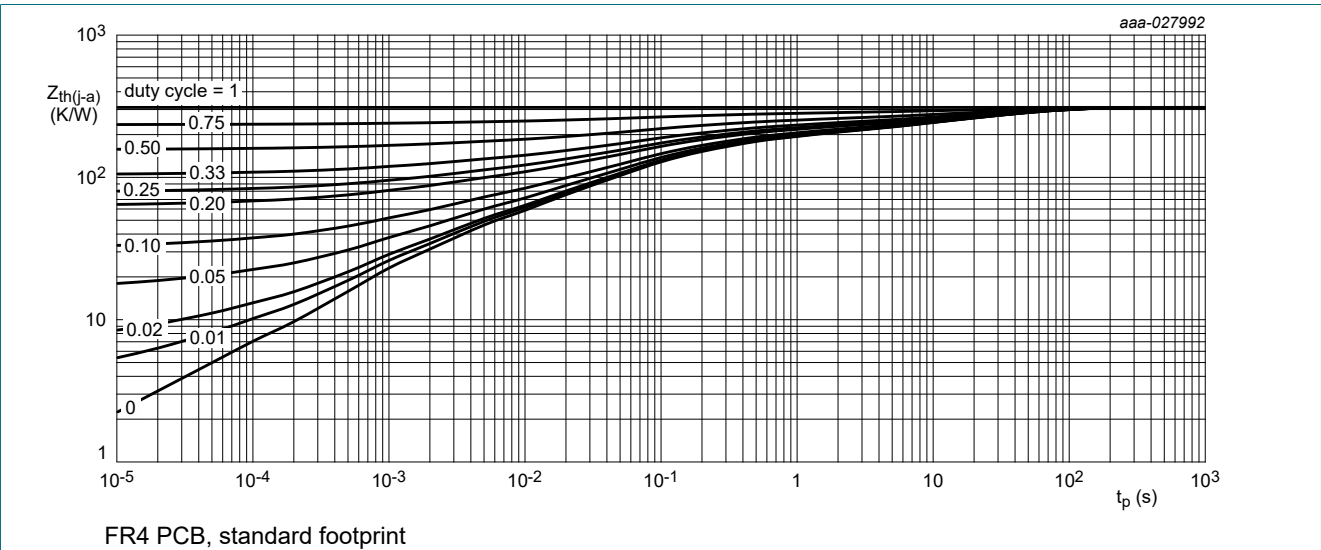


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

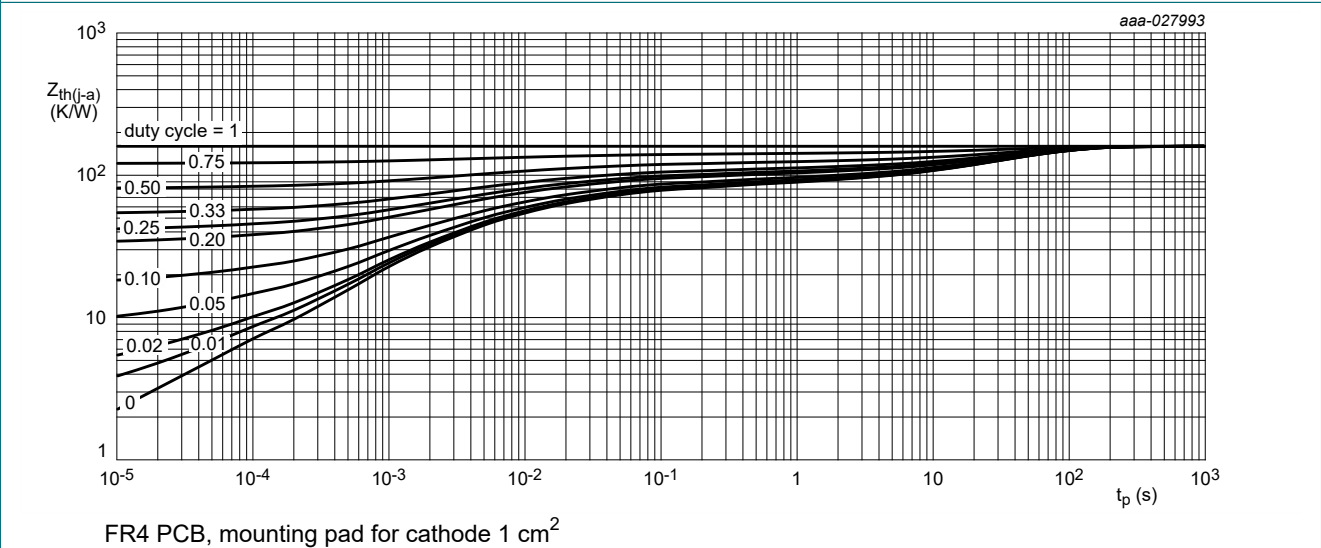
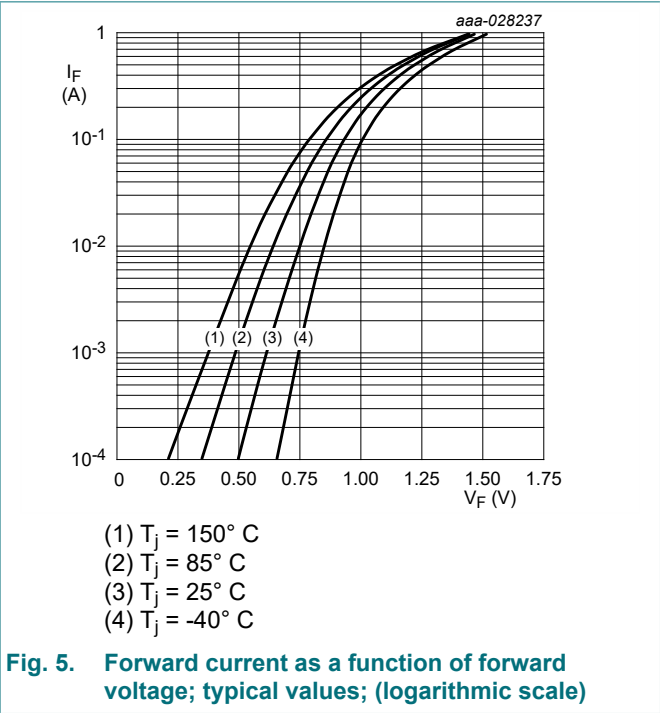
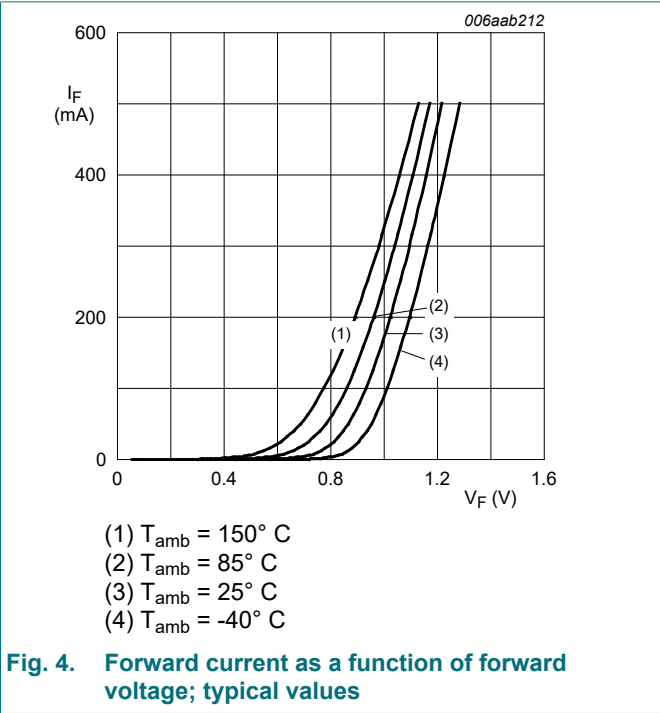


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|-----------------|-----------------------|---|--|-----|-----|------|------|
| V _F | forward voltage | I _F = 100 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | | - | - | 1 | V |
| | | I _F = 200 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C | | - | - | 1.25 | V |
| I _R | reverse current | V _R = 200 V; pulsed; T _j = 25 °C | | - | - | 100 | nA |
| | | V _R = 200 V; pulsed; T _j = 150 °C | | - | - | 100 | μA |
| C _d | diode capacitance | V _R = 0 V; f = 1 MHz; T _j = 25 °C | | - | - | 2 | pF |
| t _{rr} | reverse recovery time | I _F = 30 mA; I _R = 30 mA; R _L = 100 Ω; I _{R(meas)} = 3 mA; T _j = 25 °C | | - | - | 50 | ns |



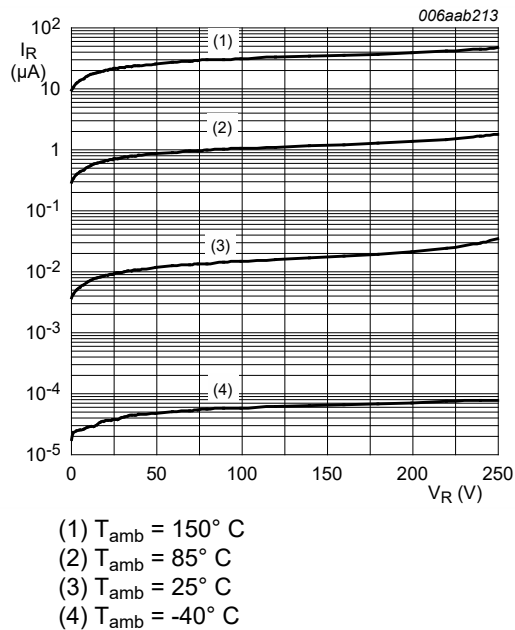


Fig. 6. Reverse current as a function of reverse voltage; typical values

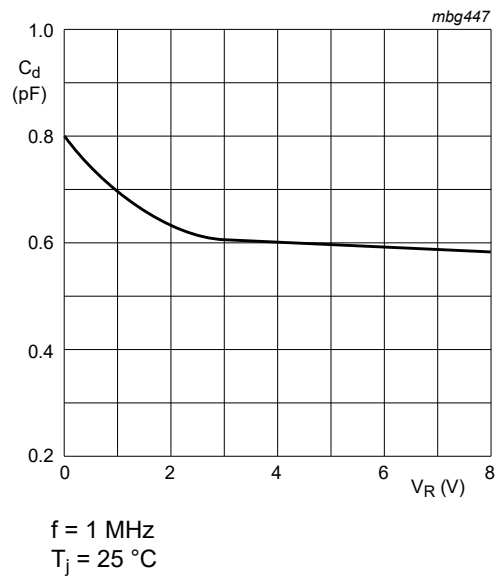


Fig. 7. Diode capacitance as a function of reverse voltage; typical values

11. Test information

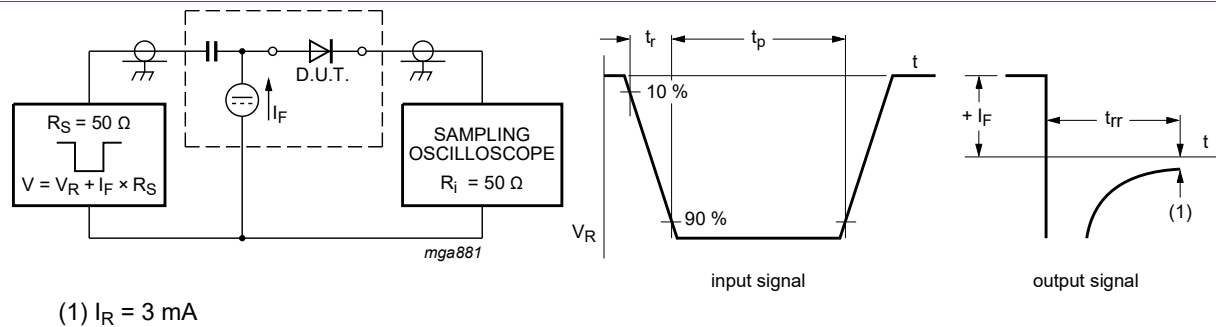
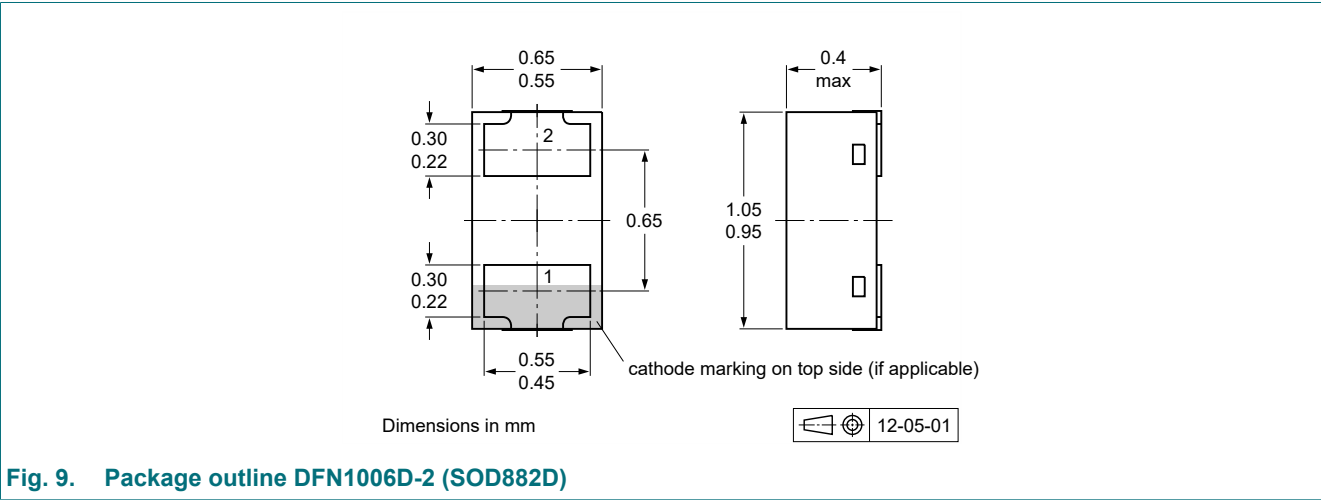


Fig. 8. Reverse recovery time test circuit and waveforms

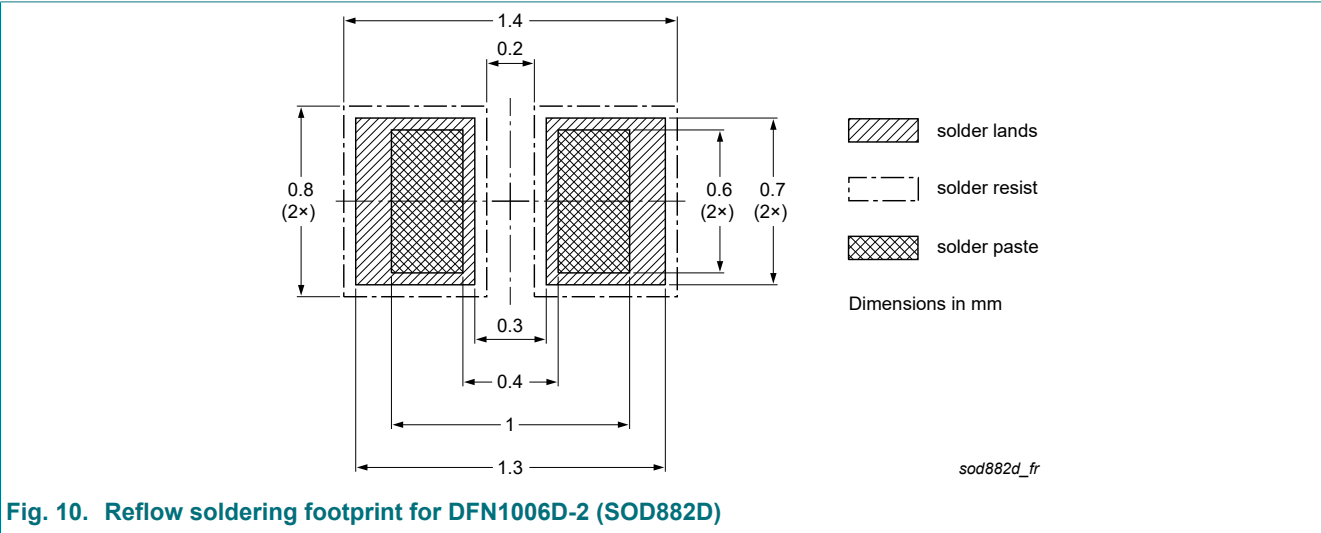
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|--|--------------------|---------------|---------------|
| BAS21LD-Q v.2 | 20250129 | Product data sheet | - | BAS21LD-Q v.1 |
| Modifications: | <ul style="list-style-type: none">Limiting values: I_{FSM} values changedCharacteristics: Fig 8 removed | | | |
| BAS21LD-Q v.1 | 20241202 | Product data sheet | - | - |

15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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- [2] The term 'short data sheet' is explained in section "Definitions".
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