Triple 3-input AND gate Rev. 3 — 12 February 2024

Product data sheet

1. General description

The 74LVC11-Q100 provides three 3-input AND functions.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

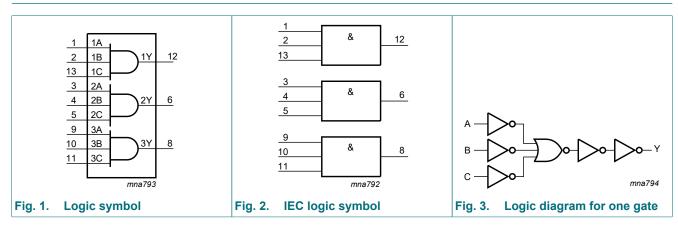
- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and -40 °C to +125 °C
- Wide supply voltage range from 1.2 V to 3.6 V
- Inputs accept voltages up to 5.5 V
- CMOS low power consumption
- Direct interface with TTL levels
- Complies with JEDEC standard:
 - JESD8-7A (1.65 V to 1.95 V)
 - JESD8-5A (2.3 V to 2.7 V)
 - JESD8-C/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V

3. Ordering information

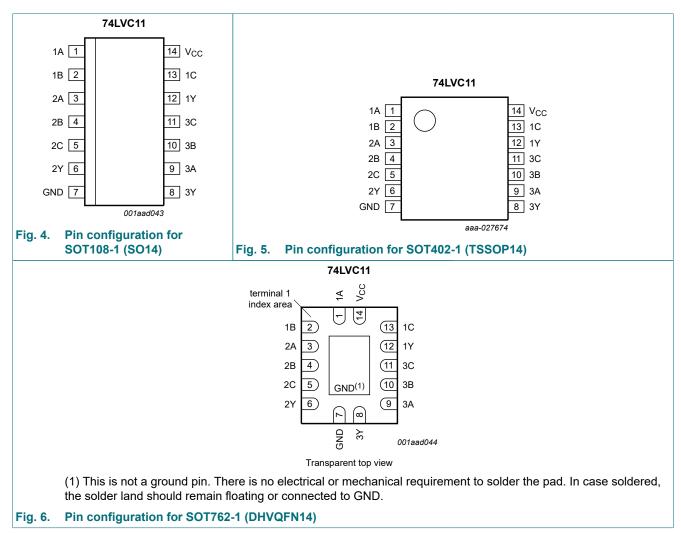
Type number	Package						
	Temperature range	Name	Description	Version			
74LVC11D-Q100	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	<u>SOT108-1</u>			
74LVC11PW-Q100	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	<u>SOT402-1</u>			
74LVC11BQ-Q100	-40 °C to +125 °C	DHVQFN14	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 × 3 × 0.85 mm	<u>SOT762-1</u>			

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4. Functional diagram



5. Pinning information



5.1. Pinning

5.2. Pin description

Table 2. Pin description					
Symbol	Pin	Description			
1A, 2A, 3A	1, 3, 9	data input			
1B, 2B, 3B	2, 4, 10	data input			
1C, 2C, 3C	13, 5, 11	data input			
1Y, 2Y, 3Y	12, 6, 8	data output			
GND	7	ground (0 V)			
V _{CC}	14	supply voltage			

6. Functional description

Table 3. Function selection

H = HIGH voltage level; L = LOW voltage level; X = don't care

Input	Output		
nA	nB	nC	nY
L	Х	Х	L
Х	L	Х	L
X	Х	L	L
Н	Н	Н	Н

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CC}	supply voltage			-0.5	+6.5	V
I _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+6.5	V
I _{OK}	output clamping current	$V_{\rm O}$ > $V_{\rm CC}$ or $V_{\rm O}$ < 0 V		-	±50	mA
Vo	output voltage		[2]	-0.5	V _{CC} + 0.5	V
I _O	output current	$V_{O} = 0 V$ to V_{CC}		-	±50	mA
I _{CC}	supply current			-	100	mA
I _{GND}	ground current			-100	-	mA
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[3]	-	500	mW
T _{stg}	storage temperature			-65	+150	°C

[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

[2] The output voltage ratings may be exceeded if the output current ratings are observed.

[3] For SOT108-1 (SO14) package: P_{tot} derates linearly with 10.1 mW/K above 100 °C.
 For SOT402-1 (TSSOP14) package: P_{tot} derates linearly with 7.3 mW/K above 81 °C.
 For SOT762-1 (DHVQFN14) package: P_{tot} derates linearly with 9.6 mW/K above 98 °C.

8. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		1.65	-	3.6	V
		functional	1.2	-	-	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	-	+125	°C
Δt/ΔV	input transition rise and fall	V _{CC} = 1.65 V to 2.7 V	0	-	20	ns/V
	rate	V _{CC} = 2.7 V to 3.6 V	0	-	10	ns/V

Table 5. Recommended operating conditions

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	°C to +8	85 °C	-40 °C to	Unit	
			Min	Typ[1]	Max	Min	Мах	
VIH	HIGH-level	V _{CC} = 1.2 V	1.08	-	-	1.08	-	V
	input voltage	V _{CC} = 1.65 V to 1.95 V	0.65xV_{CC}	-	-	0.65xV _{CC}	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	1.7	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	2.0	-	V
V _{IL}	LOW-level	V _{CC} = 1.2 V	-	-	0.12	-	0.12	V
	input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	$0.35 \mathrm{xV}_{\mathrm{CC}}$	-	0.35xV _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	output voltage	I_{O} = -100 µA; V_{CC} = 1.65 V to 3.6 V	V _{CC} -0.2	-	-	V _{CC} -0.3	-	V
		I _O = -4 mA; V _{CC} = 1.65 V	1.2	-	-	1.05	-	V
		I _O = -8 mA; V _{CC} = 2.3 V	1.8	-	-	1.65	-	V
		I _O = -12 mA; V _{CC} = 2.7 V	2.2	-	-	2.05	-	V
		I _O = -18 mA; V _{CC} = 3.0 V	2.4	-	-	2.25	-	V
		I _O = -24 mA; V _{CC} = 3.0 V	2.2	-	-	2.0	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	output voltage	I_{O} = 100 µA; V_{CC} = 1.65 V to 3.6 V	-	-	0.2	-	0.3	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.45	-	0.65	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	-	0.6	-	0.8	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.4	-	0.6	V
		I _O = 24 mA; V _{CC} = 3.0 V	-	-	0.55	-	0.8	V
l _l	input leakage current	V _{CC} = 3.6 V; V _I = 5.5 V or GND	-	±0.1	±5	-	±20	μA
I _{CC}	supply current	V_{CC} = 3.6 V; V_{I} = V_{CC} or GND; I_{O} = 0 A	-	0.1	10	-	40	μA
ΔI _{CC}	additional supply current	per input pin; V_{CC} = 2.7 V to 3.6 V; V _I = V _{CC} - 0.6 V; I _O = 0 A	-	5	500	-	5000	μA

Triple 3-input AND gate

Symbol	Parameter	Conditions	-40 °C to +85 °C -40 °C to +12		+125 °C	Unit		
			Min	Typ <mark>[1]</mark>	Мах	Min	Мах	
CI	input capacitance	$V_{CC} = 0 V$ to 3.6 V; $V_I = GND$ to V_{CC}	-	5.0	-	-	-	pF

[1] All typical values are measured at V_{CC} = 3.3 V (unless stated otherwise) and T_{amb} = 25 °C.

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Fig. 8.

Symbol	mbol Parameter Conditions		-40 °C to +85 °C			-40 °C to	+125 °C	Unit
			Min	Typ[1]	Мах	Min	Мах	1
t _{pd}	propagation delay	nA, nB, nC to nY; see Fig. 7 [2]						
		V _{CC} = 1.2 V	-	14.0	-	-	-	ns
		V _{CC} = 1.65 V to 1.95 V	1.0	4.7	12.2	1.0	14.1	ns
		V_{CC} = 2.3 V to 2.7 V	1.5	2.8	6.9	1.5	8.0	ns
		V _{CC} = 2.7 V	1.5	2.9	7.0	1.5	8.1	ns
		V _{CC} = 3.0 V to 3.6 V	1.5	2.5	6.2	1.5	7.2	ns
C _{PD}	power dissipation	per gate; $V_I = GND$ to V_{CC} [3]						
	capacitance	V _{CC} = 1.65 V to 1.95 V	-	3.1	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	6.2	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	9.0	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.2 V, 1.8 V, 2.5 V, 2.7 V, and 3.3 V respectively.

[1] f_{pd} is the same as f_{PLH} and f_{PHL} . [2] f_{pd} is the same as f_{PLH} and f_{PHL} . [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W). $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_0)$ where:

 f_i = input frequency in MHz; f_o = output frequency in MHz

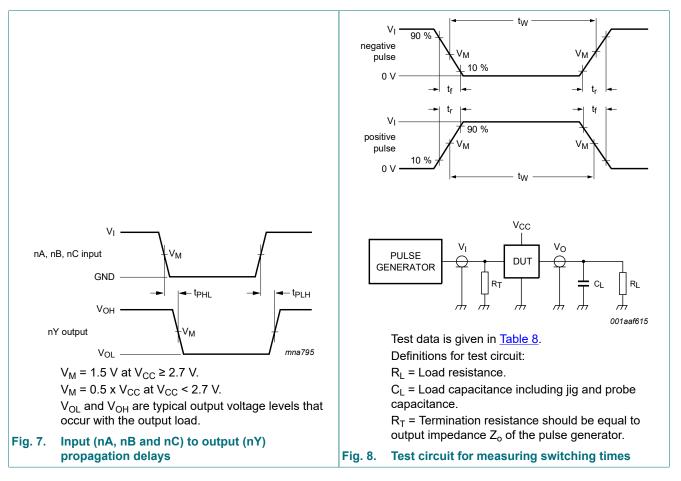
C_L = output load capacitance in pF

V_{CC} = supply voltage in Volts

N = number of inputs switching

 $\Sigma(C_L \times V_{CC}^2 \times f_o) = \text{sum of the outputs}$

Triple 3-input AND gate



10.1. Waveforms and test circuit



Supply voltage	Input	Input		
	VI	t _r , t _f	CL	RL
1.2 V	V _{CC}	≤ 2 ns	30 pF	1 kΩ
1.65 V to 1.95 V	V _{CC}	≤ 2 ns	30 pF	1 kΩ
2.3 V to 2.7 V	V _{CC}	≤ 2 ns	30 pF	500 Ω
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω

Triple 3-input AND gate

11. Package outline

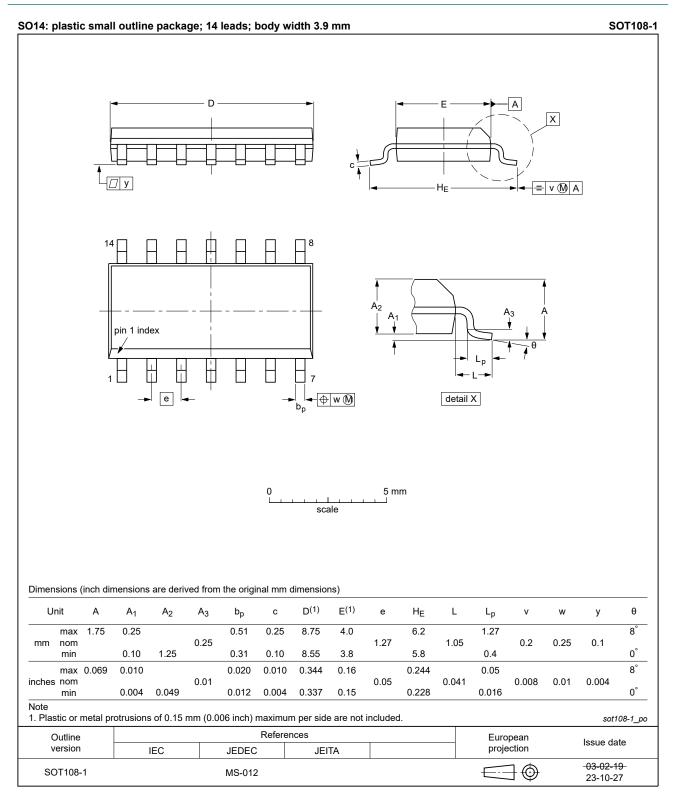


Fig. 9. Package outline SOT108-1 (SO14)

Triple 3-input AND gate

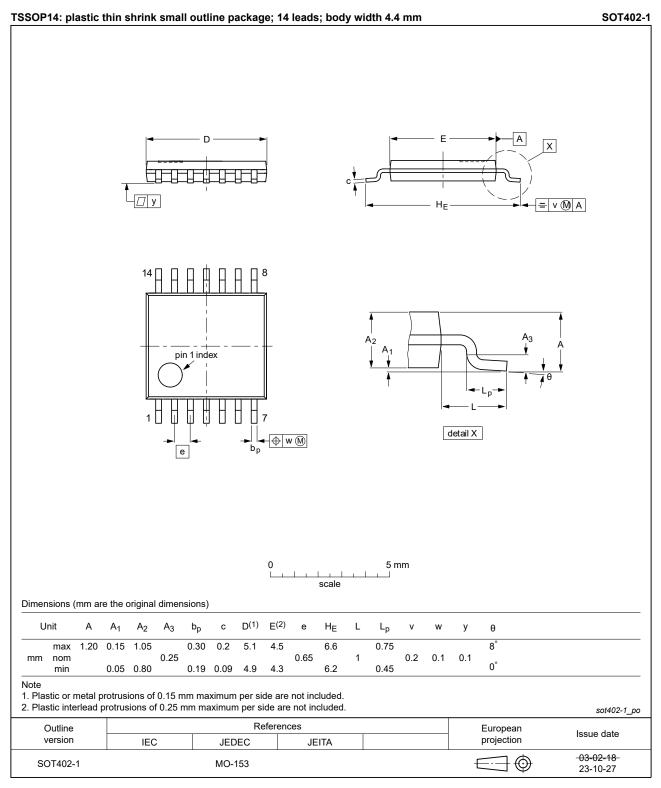


Fig. 10. Package outline SOT402-1 (TSSOP14)

Triple 3-input AND gate

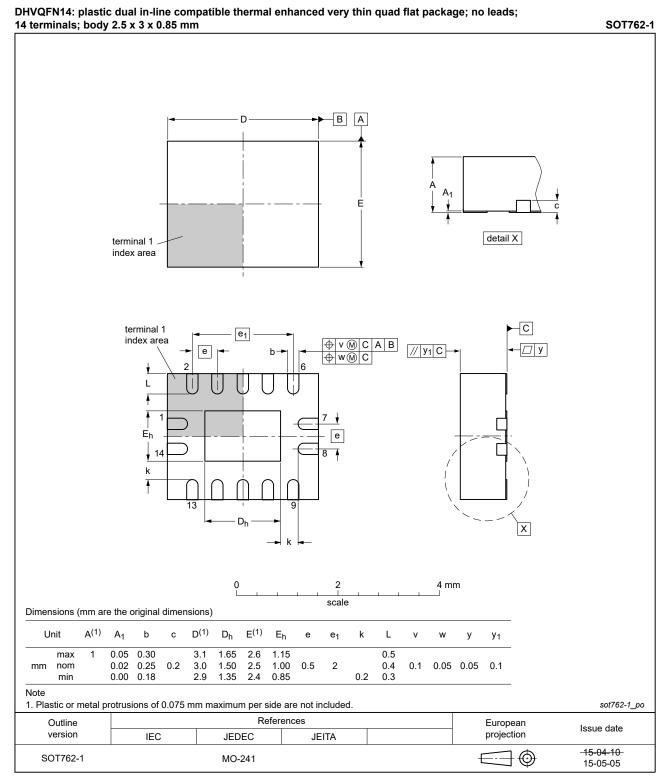


Fig. 11. Package outline SOT762-1 (DHVQFN14)

12. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
TTL	Transistor-Transistor Logic

13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74LVC11_Q100 v.4	20240212	Product data sheet	-	74LVC11_Q100 v.3		
Modifications:	• Fig. 9, Fig. 10: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153.					
74LVC11_Q100 v.3	20230802	Product data sheet	-	74LVC11_Q100 v.2		
Modifications:	• <u>Section 2</u> : ESD sp	pecification updated accord	ing to the latest JEDE	C standard.		
74LVC11_Q100 v.2	20200113	Product data sheet	-	74LVC11_Q100 v.1		
Modifications:	 <u>Table 4</u>: Derating values for P_{tot} total power dissipation updated. Added type number 74LVC11BQ-Q100 (SOT762-1/DHVQFN14) 					
74LVC11 Q100 v.1	20180307	Product data sheet	-	-		

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description	1
2. Features and benefits	1
3. Ordering information	1
4. Functional diagram	2
5. Pinning information	2
5.1. Pinning	2
5.2. Pin description	3
6. Functional description	3
7. Limiting values	3
8. Recommended operating conditions	4
9. Static characteristics	4
10. Dynamic characteristics	5
10.1. Waveforms and test circuit	6
11. Package outline	7
12. Abbreviations	10
13. Revision history	10
14. Legal information	11

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