

74LV245A

Octal bus transceiver; 3-state

Rev. 3 — 4 July 2024

Product data sheet

1. General description

The 74LV245A is an 8-bit transceiver with 3-state outputs. The device features an output enable (\overline{OE}) and send/receive (DIR) for direction control. A HIGH on \overline{OE} causes the outputs to assume a high-impedance OFF-state.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

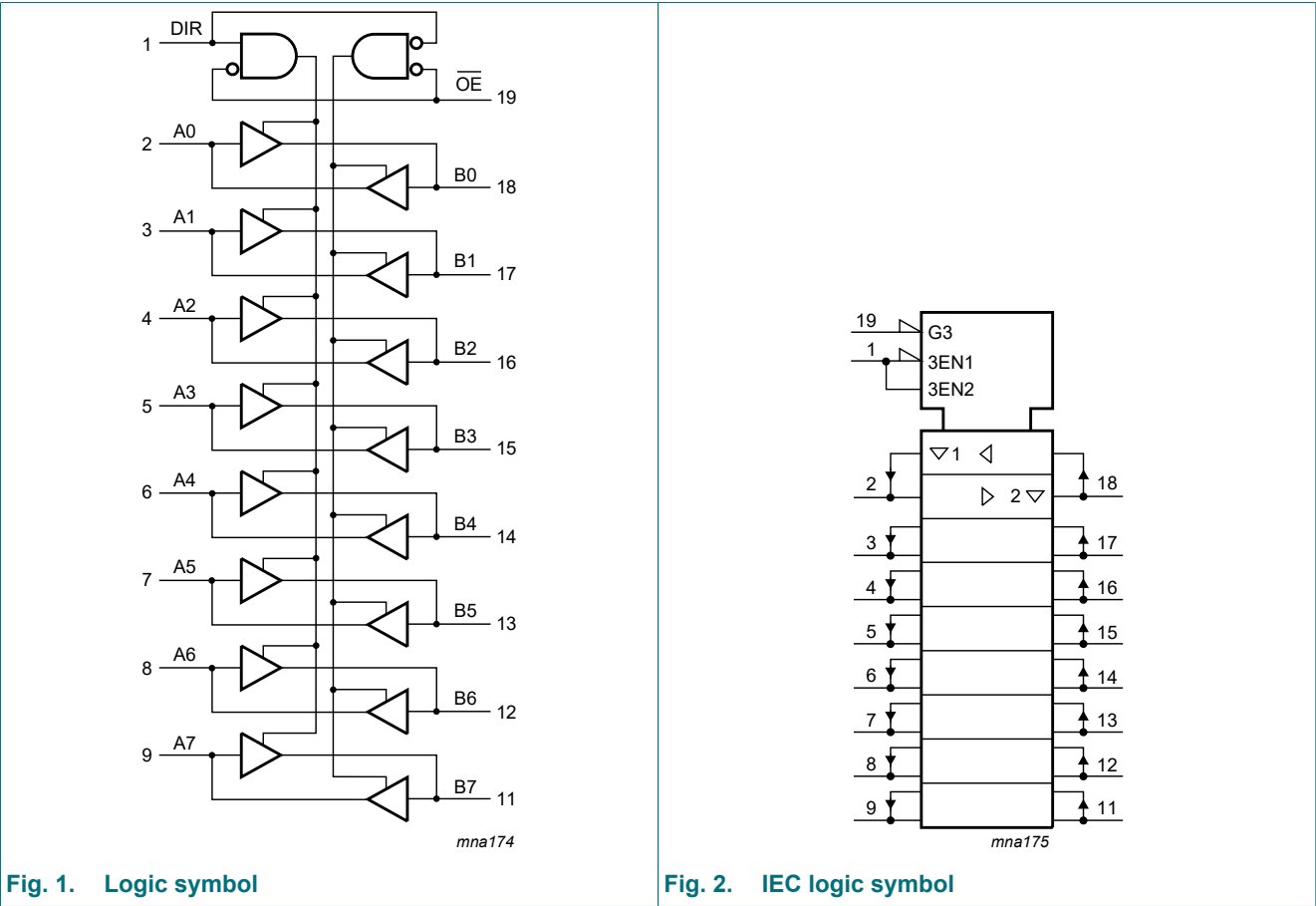
- Wide supply voltage range from 2.0 V to 5.5 V
- Maximum t_{pd} of 6.5 ns at 5 V
- Typical $V_{OL(p)} < 0.8$ V at $V_{CC} = 3.3$ V, $T_{amb} = 25$ °C
- Typical $V_{OH(v)} > 2.3$ V at $V_{CC} = 3.3$ V, $T_{amb} = 25$ °C
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 3000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 2000 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

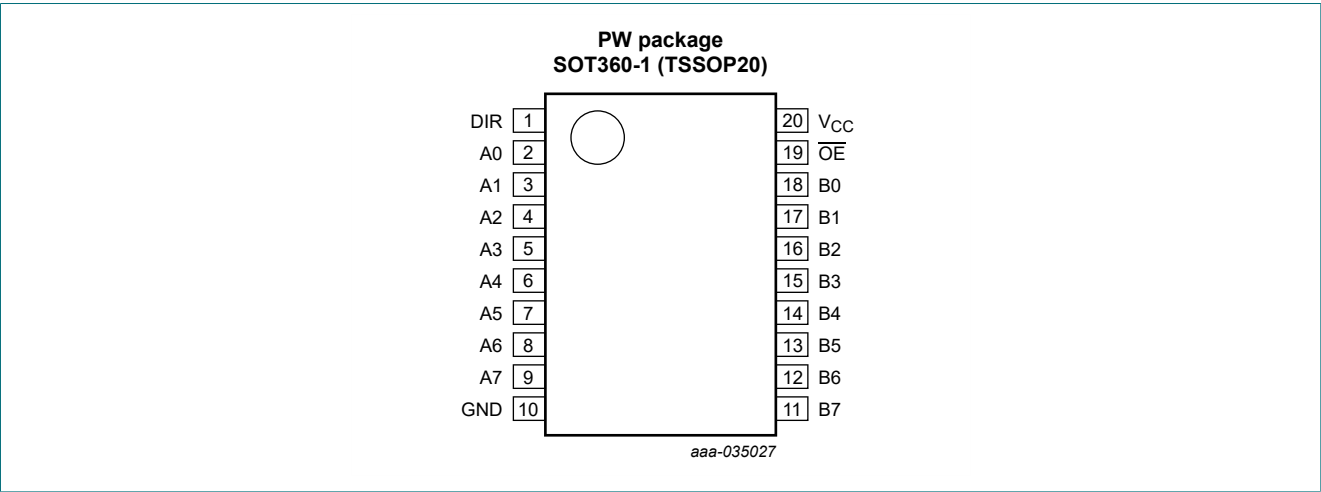
Type number	Package			
	Temperature range	Name	Description	Version
74LV245APW	-40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1

4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
DIR	1	direction control
A0, A1, A2, A3, A4, A5, A6, A7	2, 3, 4, 5, 6, 7, 8, 9	data input/output
GND	10	ground (0 V)
B0, B1, B2, B3, B4, B5, B6, B7	18, 17, 16, 15, 14, 13, 12, 11	data input/output
OE	19	output enable input (active LOW)
V _{CC}	20	supply voltage

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

Input		Input/output	
OE	DIR	An	Bn
L	L	A = B	input
L	H	input	B = A
H	X	Z	Z

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
V _I	input voltage	[1]	-0.5	+7.0	V
V _O	output voltage	active mode [2][3]	-0.5	V _{CC} + 0.5	V
		power-down or 3-state mode [2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V	-20	-	mA
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
I _O	output current	V _O = 0 V to V _{CC}	-	±35	mA
I _{CC}	supply current		-	70	mA
I _{GND}	ground current		-70	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C [4]	-	500	mW

[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.
[2] If the output current ratings are observed, the output voltage ratings may be exceeded.
[3] This value is limited to 7.0 V maximum.
[4] For SOT360-1 (TSSOP20) package: P_{tot} derates linearly with 10.0 mW/K above 100 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		2.0	5.5	V
V _I	input voltage		0	5.5	V
V _O	output voltage	active mode	0	V _{CC}	V
		power-down or 3-state mode	0	5.5	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.3 V to 2.7 V	-	200	ns/V
		V _{CC} = 3.0 V to 3.6 V	-	100	ns/V
		V _{CC} = 4.5 V to 5.5 V	-	20	ns/V

9. Static characteristics

Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level input voltage	V _{CC} = 2 V	1.5	-	-	1.5	-	1.5	-	V
		V _{CC} = 2.3 V to 2.7 V	0.7V _{CC}	-	-	0.7V _{CC}	-	0.7V _{CC}	-	V
		V _{CC} = 3.0 V to 3.6 V	0.7V _{CC}	-	-	0.7V _{CC}	-	0.7V _{CC}	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7V _{CC}	-	-	0.7V _{CC}	-	0.7V _{CC}	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2 V	-	-	0.5	-	0.5	-	0.5	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.3V _{CC}	-	0.3V _{CC}	-	0.3V _{CC}	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.3V _{CC}	-	0.3V _{CC}	-	0.3V _{CC}	V
		V _{CC} = 4.5 V to 5.5 V	-	-	0.3V _{CC}	-	0.3V _{CC}	-	0.3V _{CC}	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}								V
		V _{CC} = 2.0 V to 5.5 V; I _O = -50 μA	V _{CC} -0.1	-	-	V _{CC} -0.1	-	V _{CC} -0.1	-	V
		V _{CC} = 2.3 V; I _O = -2 mA	2	-	-	2	-	2	-	V
		V _{CC} = 3.0 V; I _O = -8 mA	2.58	-	-	2.48	-	2.48	-	V
		V _{CC} = 4.5 V; I _O = -16 mA	3.94	-	-	3.8	-	3.8	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}								
		V _{CC} = 2.0 V to 5.5 V; I _O = 50 μA	-	-	0.1	-	0.1	-	0.1	V
		V _{CC} = 2.3 V; I _O = 2 mA	-	-	0.4	-	0.4	-	0.4	V
		V _{CC} = 3.0 V; I _O = 8 mA	-	-	0.36	-	0.44	-	0.44	V
		V _{CC} = 4.5 V; I _O = 16 mA	-	-	0.44	-	0.55	-	0.55	V
I _{OZ}	OFF-state output current	V _{CC} = 5.5 V; V _I = V _{IH} or V _{IL} ; V _O = GND to 5.5 V	-	-	±0.25	-	±2.5	-	±2.5	μA
I _{OFF}	power-off leakage current	V _I or V _O = GND to 5.5 V; V _{CC} = 0 V	-	-	0.5	-	5	-	5	μA

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
I _I	input leakage current	V _I = V _{CC} or GND; V _{CC} = 0 V to 5.5 V	-	-	±0.1	-	±1	-	±1	µA
I _{CC}	supply current	V _I = V _{CC} or GND; I _O = 0 A; V _{CC} = 5.5 V	-	-	2	-	20	-	20	µA

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit, see Fig. 5.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
t _{pd}	propagation delay	An to Bn or Bn to An; see Fig. 3 [2]								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	5.2	13	1	15	1	17	ns
		C _L = 50 pF	-	7.2	15.9	1	18	1	21	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	4.0	8.4	1	10	1	11	ns
		C _L = 50 pF	-	5.6	11.9	1	13.5	1	14.5	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.1	5.5	1	6.5	1	7	ns
t _{en}	enable time	OE to An or OE to Bn; see Fig. 4 [2]								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	6.5	19.9	1	22	1	24	ns
		C _L = 50 pF	-	8.6	22.7	1	26	1	28	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	4.9	13.2	1	15.5	1	16.5	ns
		C _L = 50 pF	-	6.6	16.7	1	19	1	20	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.7	8.5	1	10	1	10.5	ns
t _{dis}	disable time	OE to An or OE to Bn; see Fig. 4 [2]								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	6.8	18.1	1	20	1	22	ns
		C _L = 50 pF	-	11.4	23.1	1	25	1	27	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.4	16.5	1	19.5	1	20.5	ns
		C _L = 50 pF	-	8.8	19.8	1	22	1	23	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	4.2	12.8	1	14.2	1	14.7	ns
		C _L = 50 pF	-	6.5	14.7	1	16	1	16.5	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
t _{sk(o)}	output skew time	C _L = 50 pF								
		V _{CC} = 2.3 V to 2.7 V	-	-	2	-	2	-	2	ns
		V _{CC} = 3.0 V to 3.6 V	-	-	1.5	-	1.5	-	1.5	ns
		V _{CC} = 4.5 V to 5.5 V	-	-	1	-	1	-	1	ns
C _I	input capacitance	V _I = V _{CC} or GND; V _{CC} = 3.3 V	-	2	6	-	6	-	6	pF
C _{I/O}	input/output capacitance	V _O = V _{CC} or GND; V _{CC} = 3.3 V	-	5.5	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	per buffer; C _L = 50 pF; f = 10 MHz; V _I = GND to V _{CC} [3]								
		V _{CC} = 3.3 V	-	9.5	-	-	-	-	-	pF
		V _{CC} = 5.0 V	-	10.4	-	-	-	-	-	pF

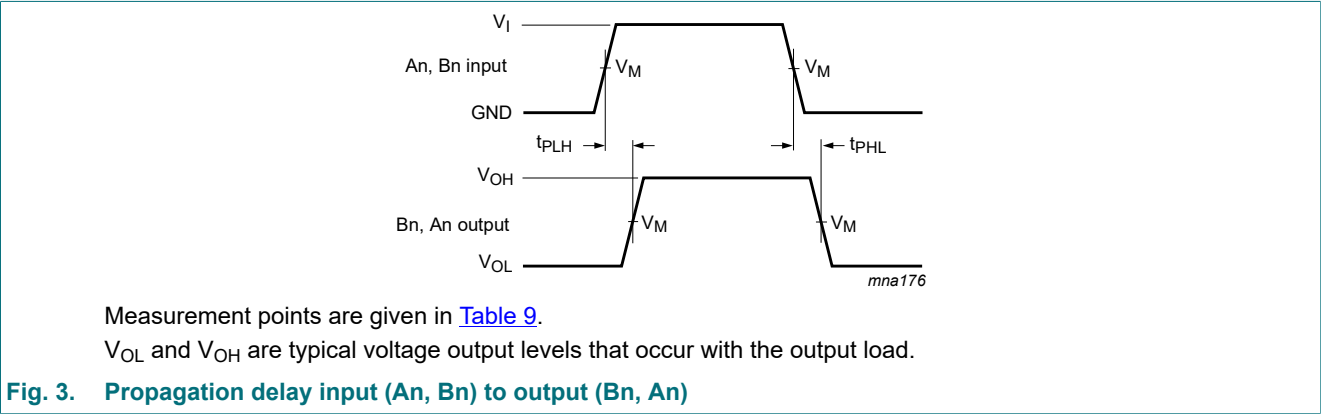
- [1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 2.5 V, 3.3 V, and 5 V respectively, unless otherwise specified.
- [2] t_{pd} is the same as t_{PLH} and t_{PHL}.
t_{en} is the same as t_{PZL} and t_{PZH}.
t_{dis} is the same as t_{PLZ} and t_{PHZ}.
- [3] C_{PD} is used to determine the dynamic power dissipation P_D (μW).
 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:
f_i = input frequency in MHz;
f_o = output frequency in MHz;
C_L = output load capacitance in pF;
V_{CC} = supply voltage in V.

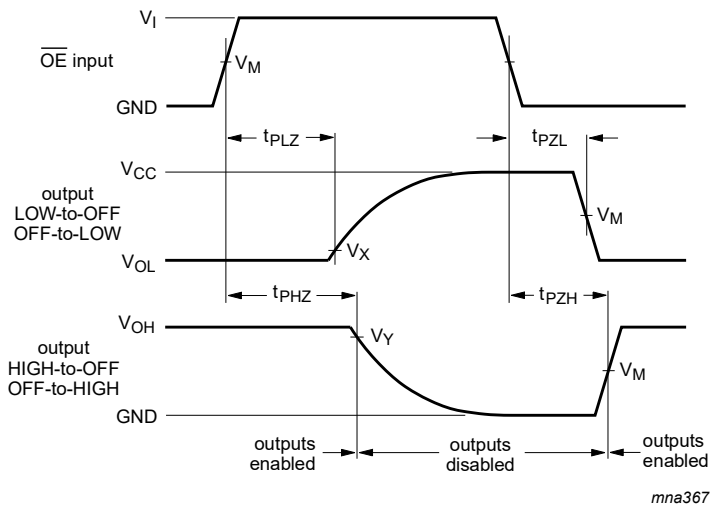
Table 8. Noise characteristics

GND = 0 V. For test circuit, see Fig. 5.

Symbol	Parameter	Conditions	T _{amb} = 25 °C			Unit
			Min	Typ	Max	
V _{CC} = 3.3 V; C _L = 50 pF						
V _{OL(p)}	LOW-level output voltage (peak)		-	0.3	0.8	V
V _{OL(v)}	LOW-level output voltage (valley)		-0.8	-0.2	-	V
V _{OH(v)}	HIGH-level output voltage (valley)		-	2.9	-	V
V _{IH(AC)}	AC HIGH-level input voltage	dynamic	2.31	-	-	V
V _{IL(AC)}	AC LOW-level input voltage	dynamic	-	-	0.99	V

10.1. Waveforms and test circuit



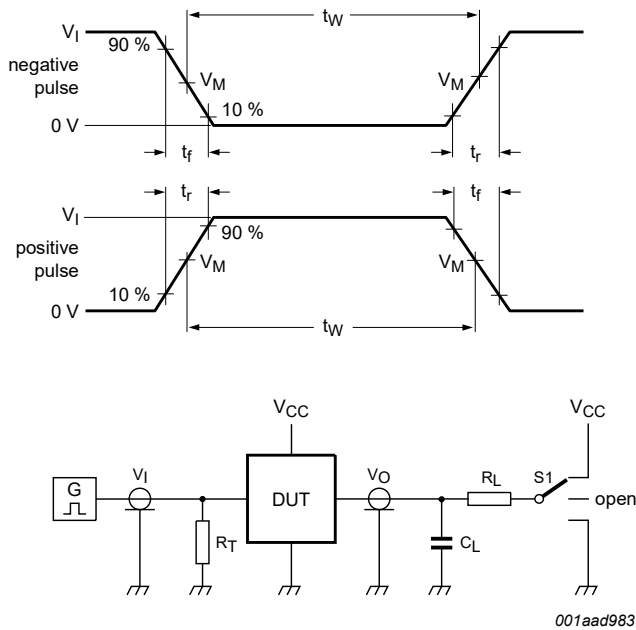


Measurement points are given in [Table 9](#).
 V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Fig. 4. Enable and disable times

Table 9. Measurement points

Input	Output		
V_M	V_M	V_X	V_Y
$0.5V_{CC}$	$0.5V_{CC}$	$V_{OL} + 0.3\text{ V}$	$V_{OH} - 0.3\text{ V}$



Test data is given in [Table 10](#).
Definitions test circuit:
 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator;
 C_L = Load capacitance including jig and probe capacitance;
 R_L = Load resistor;
 $S1$ = Test selection switch.

Fig. 5. Test circuit for measuring switching times

Table 10. Test data

Input		Load		S1 position		
V _I	t _r , t _f	C _L	R _L	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
GND to V _{CC}	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

11. Package outline

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1

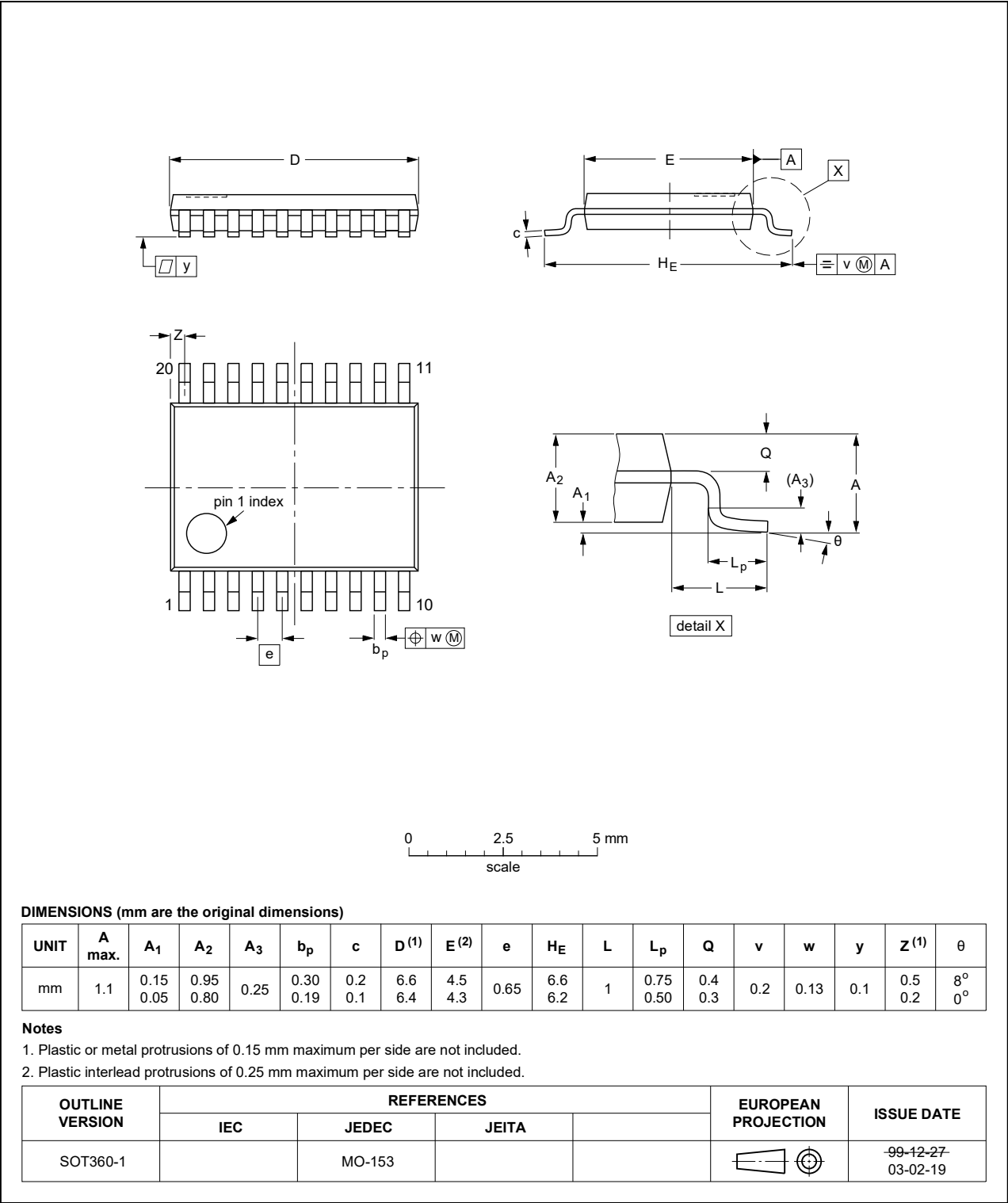


Fig. 6. Package outline SOT360-1 (TSSOP20)

12. Abbreviations

Table 11. Abbreviations

Acronym	Description
ANSI	American National Standards Institute
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
ESDA	ElectroStatic Discharge Association
HBM	Human Body Model
JEDEC	Joint Electron Device Engineering Council

13. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74LV245A v.3	20240704	Product data sheet	-	74LV245A v.2
Modifications:	<ul style="list-style-type: none">Section 2: ESD specification updated according to the latest JEDEC standard.The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.			
74LV245A v.2	20161103	Product data sheet	-	74LV245A v.1
Modifications:	<ul style="list-style-type: none">Type number 74LV245ABQ removed.			
74LV245A v.1	20160610	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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