Single bus switch Rev. 7.1 — 12 February 2022

### 1. General description

The 74CBTLV1G125 is a single high-speed line switch. The switch is disabled when the output enable  $(\overline{OE})$  input is high.

To ensure the high-impedance OFF-state during power-up or power-down, tie  $\overline{OE}$  to the V<sub>CC</sub> through a pull-up resistor. The current-sinking capability of the driver determines the minimum value of the resistor.

Schmitt-trigger action at control inputs makes the circuit tolerant of slower input rise and fall times. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Supply voltage range from 2.3 V to 3.6 V
- Overvoltage tolerant control inputs to 3.6 V
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- 5 Ω switch connection between two ports
- · Rail to rail switching on data I/O ports
- CMOS low power consumption
- Latch-up performance meets requirements of JESD78 Class I
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- · Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

# ne<mark>x</mark>peria

### 3. Ordering information

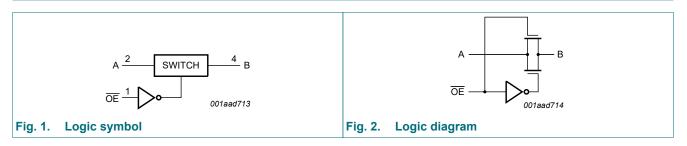
Type number	Package							
	Temperature range	Name	Description	Version				
74CBTLV1G125GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1				
74CBTLV1G125GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753				
74CBTLV1G125GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886				
74CBTLV1G125GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115				
74CBTLV1G125GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202				

### 4. Marking

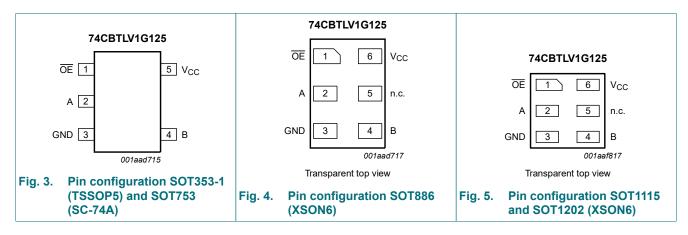
Table 2. Marking					
Type number	Marking code [1]				
74CBTLV1G125GW	bM				
74CBTLV1G125GV	b25				
74CBTLV1G125GM	bM				
74CBTLV1G125GN	bM				
74CBTLV1G125GS	bM				

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

### 5. Functional diagram



### 6. Pinning information



### 6.1. Pinning

### 6.2. Pin description

#### Table 3. Pin description

Symbol	Pin		Description
	SOT353-1 and SOT753	SOT886, SOT1115 and SOT1202	
ŌE	1	1	output enable input $\overline{OE}$ (active LOW)
A	2	2	data input or output A
GND	3	3	ground (0 V)
В	4	4	data input or output B
n.c.	-	5	not connected
V <sub>CC</sub>	5	6	supply voltage

### 7. Functional description

#### Table 4. Function table

*H* = *HIGH* voltage level; *L* = *LOW* voltage level.

Output enable input OE	Function switch
L	ON-state
Н	OFF-state

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
VI	input voltage	[1]	-0.5	+4.6	V
V <sub>SW</sub>	switch voltage	enable and disable mode	-0.5	V <sub>CC</sub> + 0.5	V
I <sub>IK</sub>	input clamping current	V <sub>I/O</sub> < -0.5 V	-50	-	mA
I <sub>SK</sub>	switch clamping current	$V_{\rm I}$ < -0.5 V or $V_{\rm I}$ > $V_{\rm CC}$ + 0.5 V	-	±50	mA
I <sub>SW</sub>	switch current	$V_{SW} = 0 V \text{ to } V_{CC}$	-	±128	mA
I <sub>CC</sub>	supply current		-	+50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to +125 \ ^{\circ}C$ [2]	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT753 (SC-74A) package: P<sub>tot</sub> derates linearly with 3.8 mW/K above 85 °C.

For SOT886 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71 °C. For SOT1202 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

# 9. Recommended operating conditions

### Table 6 Percommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		2.3	-	3.6	V
VI	input voltage		0	-	3.6	V
V <sub>SW</sub>	switch voltage	enable and disable mode	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 2.3 \text{ V to } 3.6 \text{ V}$ [1]	0	-	20	ns/V

[1] Applies to control signal levels.

## 10. Static characteristics

#### **Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T <sub>amb</sub> = -4	40 °C to +85 °C					
VIH	HIGH-level input voltage	V <sub>CC</sub> = 2.3 V to 2.7 V		-	-	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	0.8	V
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to $V_{CC}$ ; $V_{CC}$ = 3.6 V	-	-	±1.0	μA
I <sub>S(OFF)</sub>	OFF-state leakage current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = V_{CC} \text{ - GND};$ $V_{CC} = 3.6 \text{ V}; \text{ see } \underline{Fig. 6}$	-	±0.1	±5	μA
I <sub>S(ON)</sub>	ON-state leakage current	$V_I = V_{IH} \text{ or } V_{IL}; V_{CC} = 3.6 \text{ V}; \text{ see } \underline{Fig. 7}$	-	±0.1	±5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±10	μA
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 3.6 V	-	-	10	μA
$\Delta I_{CC}$	additional supply current	control input; $V_I = V_{CC} - 0.6 V$ ; $V_{CC} = 3.6 V$ [2]	-	-	300	μA
CI	input capacitance	control input; V <sub>I</sub> = 0 V or 3 V	-	2.5	-	pF
C <sub>sw</sub>	switch capacitance	OFF-state	-	7.0	-	pF
		ON-state	-	10.3	-	pF
T <sub>amb</sub> = -4	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	-	-	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	0.8	V
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to $V_{CC}$ ; $V_{CC}$ = 3.6 V	-	-	±100	μA
I <sub>S(OFF)</sub>	OFF-state leakage current	$V_I = V_{IH} \text{ or } V_{IL}; V_O = V_{CC} - GND;$ $V_{CC} = 3.6 V; \text{ see } Fig. 6$	-	-	±200	μA
I <sub>S(ON)</sub>	ON-state leakage current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 3.6 \text{ V}; \text{ see } \underline{\text{Fig. 7}}$	-	-	±200	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±10	μA
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 3.6 V	-	-	200	μA
$\Delta I_{CC}$	additional supply current	control input; $V_I = V_{CC} - 0.6 V$ ; $V_{CC} = 3.6 V$ [2]	-	-	5000	μA

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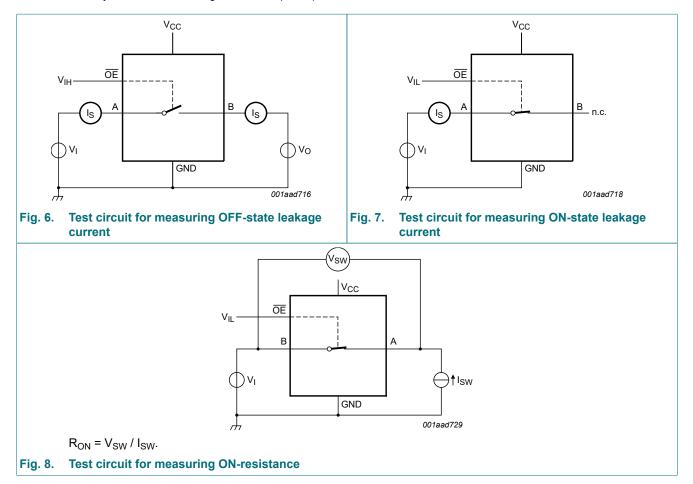
#### Table 8. Resistance RON

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); see test circuit Fig. 8.

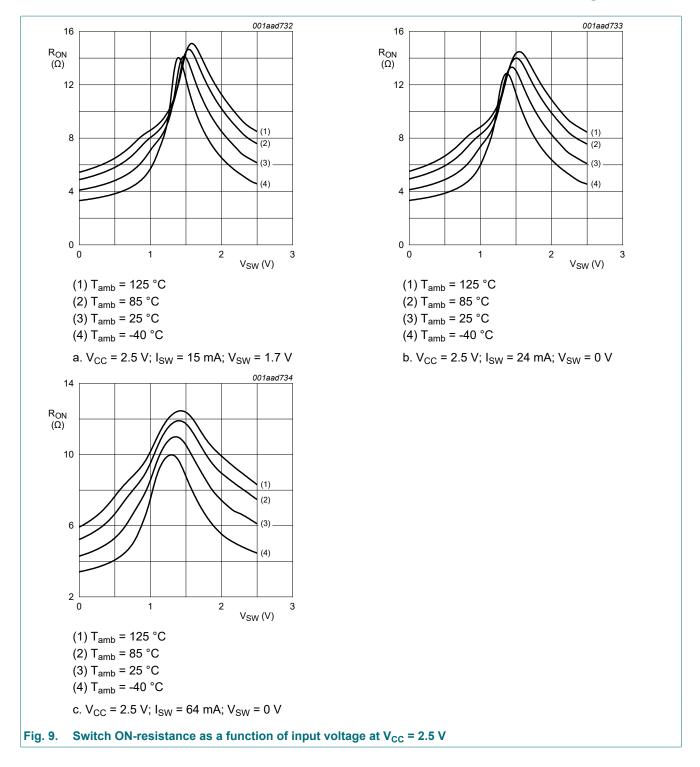
Symbol	Parameter	Conditions	-40 °C to +85 °C		-40 °C to	Unit		
			Min	Typ[1]	Max	Min	Мах	
R <sub>ON</sub>	ON resistance	V <sub>CC</sub> = 2.3 V; see <u>Fig. 9</u> [2]						
		I <sub>SW</sub> = 64 mA; V <sub>I</sub> = 0 V	-	4.7	10	-	15.0	Ω
		I <sub>SW</sub> = 24 mA; V <sub>I</sub> = 0 V	-	4.5	10	-	15.0	Ω
		I <sub>SW</sub> = 15 mA; V <sub>I</sub> = 1.7 V	-	11	25	-	38.0	Ω
		V <sub>CC</sub> = 3.0 V; see <u>Fig. 10</u>						
		I <sub>SW</sub> = 64 mA; V <sub>I</sub> = 0 V	-	4.2	7	-	11.0	Ω
		I <sub>SW</sub> = 24 mA; V <sub>I</sub> = 0 V	-	4.1	7	-	11.0	Ω
		I <sub>SW</sub> = 15 mA; V <sub>I</sub> = 2.4 V	-	7.3	15	-	25.5	Ω

[1] Typical values are measured at  $T_{amb}$  = 25 °C.

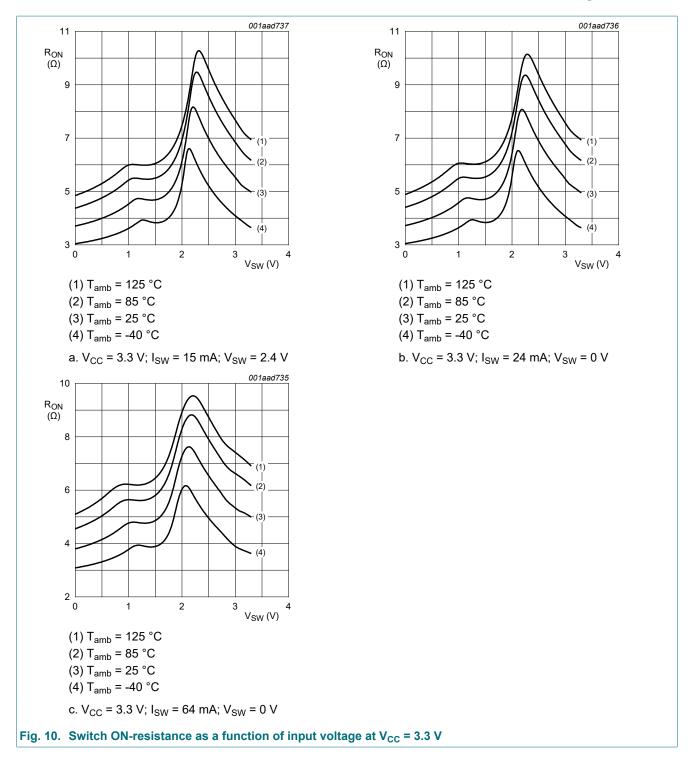
[2] Measured by the voltage drop between the A and B terminals at the indicated current through the switch. ON-state resistance is determined by the lower of the voltages of the two (A or B) terminals.



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### **11. Dynamic characteristics**

#### **Table 9. Dynamic characteristics**

GND = 0 V; see Fig. 13.

Symbol	Parameter	Conditions	-40	°C to +8	5 °C	-40 °C to	o +125 ℃	Unit
			Min	Typ[1]	Мах	Min	Max	
t <sub>pd</sub>	propagation delay	A to B or B to A; [2][3 see Fig. 11; $R_L = \infty \Omega$	]					
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.21	-	0.32	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	0.16	0.25	-	0.39	ns
t <sub>en</sub>	enable time	$\overline{\text{OE}}$ to A or B; see Fig. 12; [4 R <sub>L</sub> = 500 $\Omega$	]					
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	2.50	4.00	1.0	5.00	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	2.05	4.00	1.0	5.00	ns
t <sub>dis</sub>	disable time	$\overline{OE}$ to A or B; see Fig. 12; [8 R <sub>L</sub> = 500 $\Omega$	]					
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	2.80	5.00	1.0	6.30	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	3.40	4.10	1.0	5.40	ns

[1]

All typical values are measured at  $T_{amb}$  = 25 °C and at nominal  $V_{CC}$ . The propagation delay is the calculated RC time constant of the maximum on-state resistance of the switch and the load capacitance, [2] when driven by an ideal voltage source (zero output impedance).

 $t_{\text{pd}}$  is the same as  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$ . [3]

[4]  $t_{\text{en}}$  is the same as  $t_{\text{PZH}}$  and  $t_{\text{PZL}}$ 

 $t_{dis}$  is the same as  $t_{PHZ}$  and  $t_{PLZ}$ . [5]

### 11.1. Waveforms and test circuit

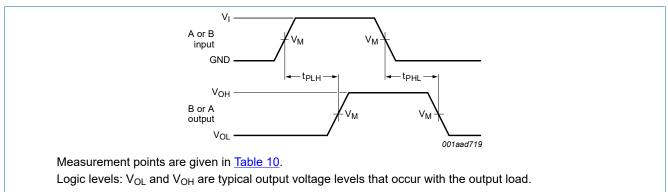
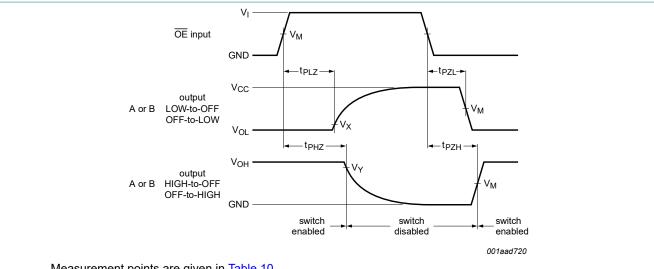


Fig. 11. The data input (A or B) to output (B or A) propagation delays

#### Single bus switch



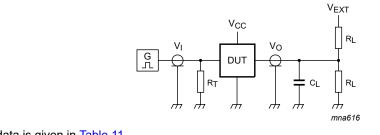
Measurement points are given in <u>Table 10</u>.

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

#### Fig. 12. Enable and disable times

#### Table 10. Measurement points

Supply voltage	Inputs			Output			
V <sub>cc</sub>	V <sub>M</sub>	VI	t <sub>r</sub> = t <sub>f</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>	
2.3 V to 2.7 V	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 2.0 ns	0.5 × V <sub>CC</sub>	V <sub>OL</sub> + 0.15 V	V <sub>OH</sub> - 0.15 V	
3.0 V to 3.6 V	$0.5 \times V_{CC}$	V <sub>CC</sub>	≤ 2.0 ns	0.5 × V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> - 0.3 V	



Test data is given in Table 11.

Definitions for test circuit:

R<sub>L</sub> = Load resistance;

C<sub>L</sub> = Load capacitance including jig and probe capacitance;

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_0$  of the pulse generator;

V<sub>EXT</sub> = Test voltage for switching times.

#### Fig. 13. Test circuit for measuring switching times

#### Table 11. Test data

Supply voltage	Load	V <sub>EXT</sub>				
V <sub>cc</sub>	CL	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>		
2.3 V to 2.7 V	30 pF	open	GND	$2 \times V_{CC}$		
3.0 V to 3.6 V	50 pF	open	GND	$2 \times V_{CC}$		

# 12. Additional dynamic characteristics

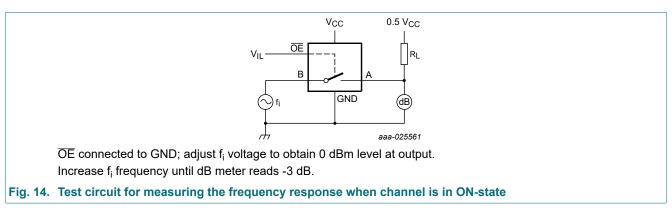
#### Table 12. Additional dynamic characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V);

 $V_I = GND$  or  $V_{CC}$  (unless otherwise specified);  $t_r = t_f \le 2.5$  ns.

Symbol	Parameter	Conditions		T <sub>amb</sub> = 25 °C		
			Min	Тур	Max	
f <sub>(-3dB)</sub>	-3 dB frequency response	$V_{CC} = 3.3 \text{ V}; \text{ R}_{L} = 50 \Omega; \text{ see } \underline{\text{Fig. 14}}$ [1]	-	263	-	MHz

#### [1] $f_i$ is biased at 0.5 × V<sub>CC</sub>.



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### 13. Package outline

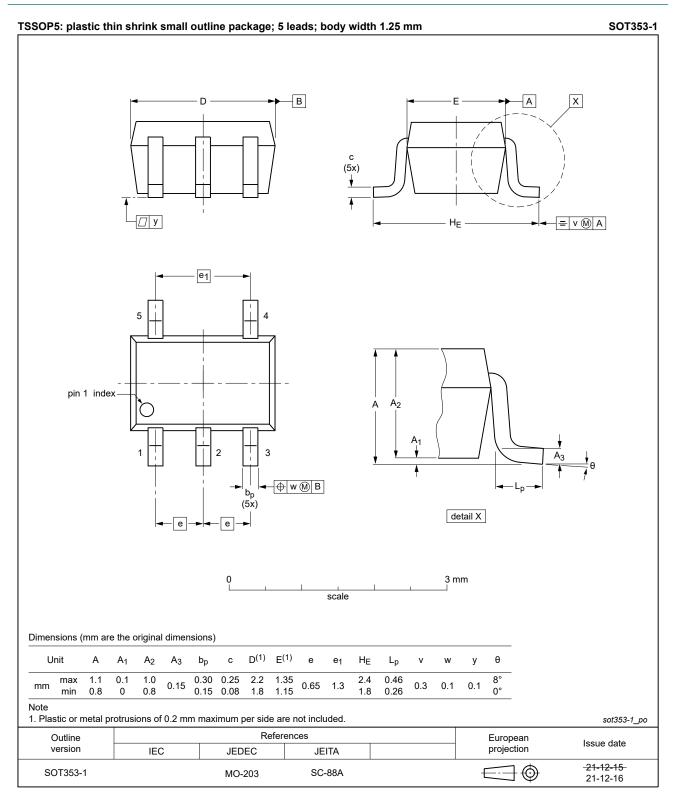


Fig. 15. Package outline SOT353-1 (TSSOP5)

74CBTLV1G125

#### Single bus switch



**SOT753** 

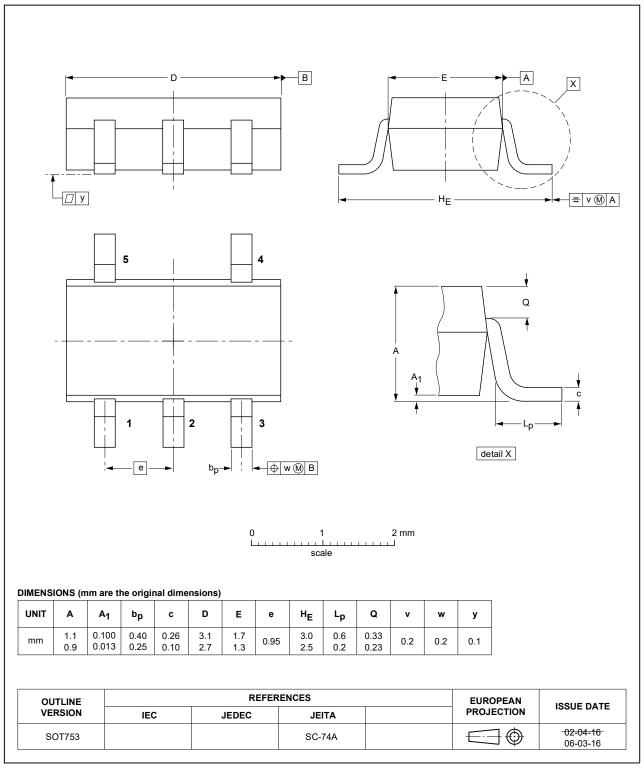


Fig. 16. Package outline SOT753 (SC-74A)

#### Single bus switch

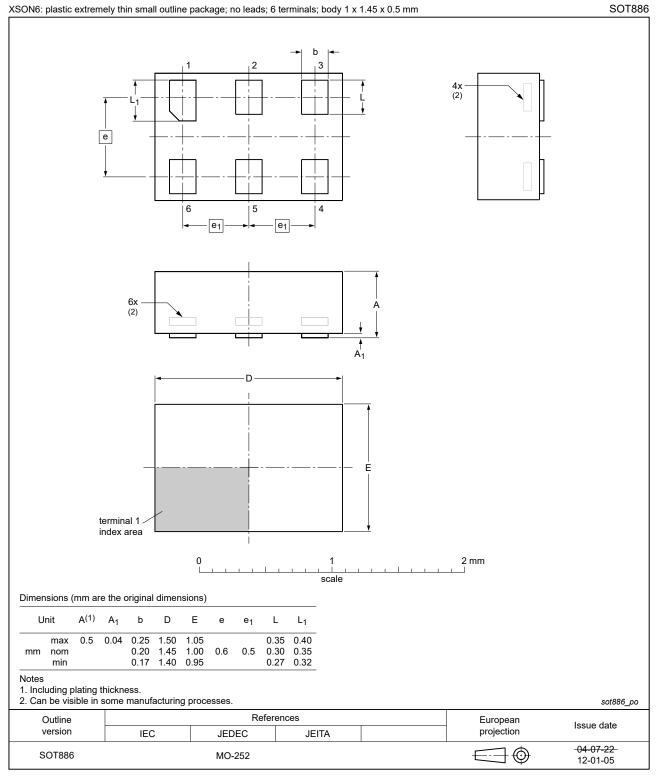
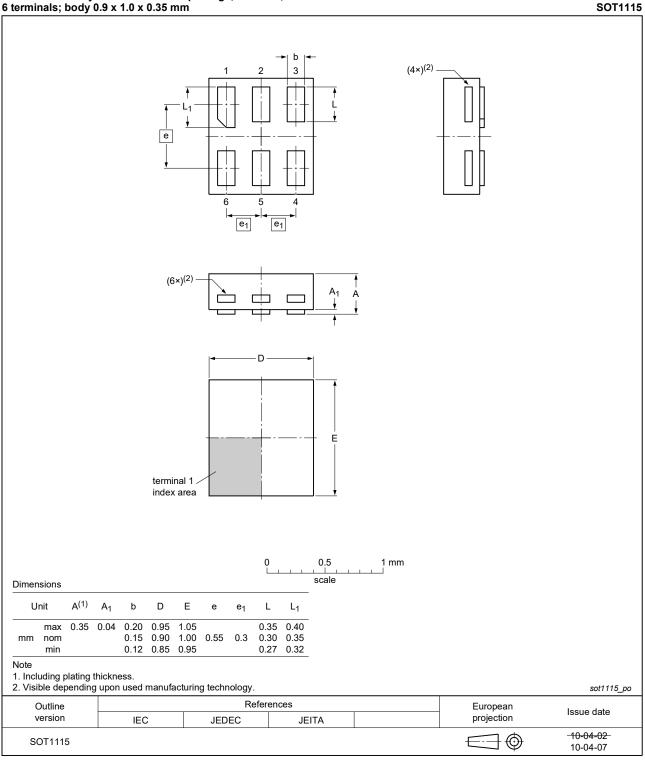


Fig. 17. Package outline SOT886 (XSON6)

#### Single bus switch

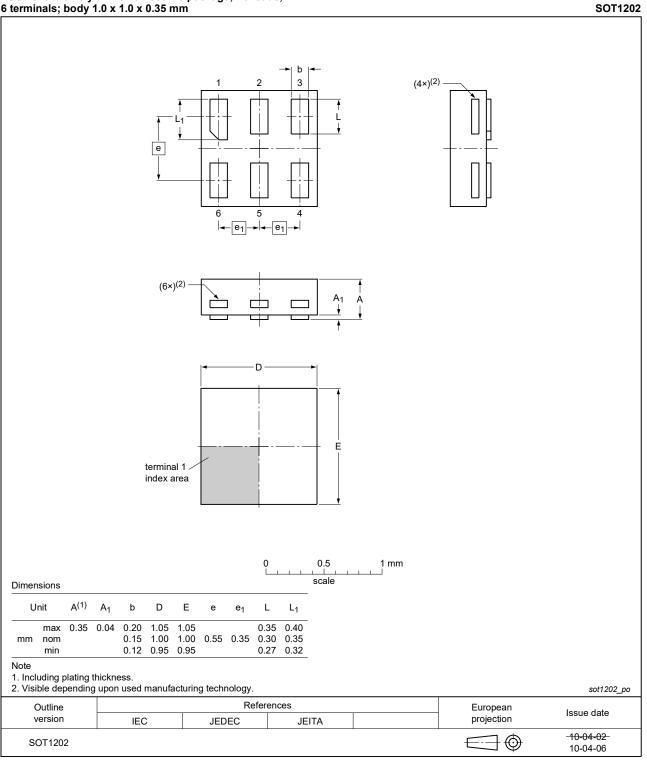
#### XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm





### Single bus switch

XSON6: extremely thin small outline package; no leads;	
6 terminals; body 1.0 x 1.0 x 0.35 mm	





### 14. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

# 15. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74CBTLV1G125 v.7.1	20220212	Product data sheet	-	74CBTLV1G125 v.6		
Modifications:	<u>Fig. 15</u> : Package outline drawing for SOT353-1 (TSSOP5) has changed.					
74CBTLV1G125 v.6	20210104	Product data sheet	-	74CBTLV1G125 v.5		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number 74CBTLV1G125GF (SOT891/XSON6) removed.</li> <li>Fig. 15: Package outline drawing for SOT353-1 (TSSOP5) has changed.</li> <li>Section 8: Derating values for P<sub>tot</sub> total power dissipation updated.</li> <li>Section 1 and Section 2 updated.</li> </ul>					
74CBTLV1G125 v.5	20161110	Product data sheet	-	74CBTLV1G125 v.4		
Modifications:	• <u>Section 12</u> added.					
74CBTLV1G125 v.4	20120905	Product data sheet	-	74CBTLV1G125 v.3		
Modifications:	Package outline drawing of SOT886 (Fig. 17) modified.					
74CBTLV1G125 v.3	20111215	Product data sheet	-	74CBTLV1G125 v.2		
Modifications:	Legal pages updated.					
74CBTLV1G125 v.2	20100729	Product data sheet	-	74CBTLV1G125 v.1		
74CBTLV1G125 v.1	20070223	Product data sheet	-	-		

#### Single bus switch

### 16. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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### Single bus switch

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