# 74AUP2G157

### Low-power 2-input multiplexer

Rev. 10 — 26 July 2023

**Product data sheet** 

### 1. General description

The 74AUP2G157 is a single 2-input multiplexer which select data from two data inputs (I0 and I1) under control of a common data select input (S). The state of the common data select input determines the particular register from which the data comes. The output  $(Y, \overline{Y})$  presents the selected data in the true (non-inverted) and complement form. The enable input  $(\overline{E})$  is active LOW. When  $\overline{E}$  is HIGH, the output Y is forced LOW and the output  $\overline{Y}$  is forced HIGH regardless of all other input conditions.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- · High noise immunity
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8-B (2.7 V to 3.6 V)
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- · ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



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# 3. Ordering information

**Table 1. Ordering information** 

Type number	Package	Package							
	Temperature range Name		Description	Version					
74AUP2G157DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1					
74AUP2G157GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm	SOT833-1					
74AUP2G157GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 × 1.0 × 0.35 mm	SOT1116					
74AUP2G157GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1.0 × 0.35 mm	SOT1203					

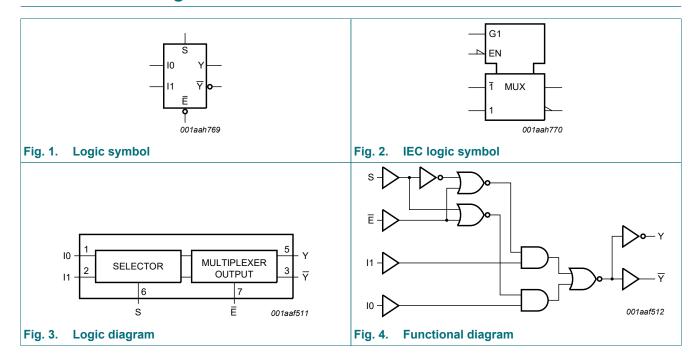
## 4. Marking

Table 2. Marking codes

Type number	Marking code[1]
74AUP2G157DC	a2P
74AUP2G157GT	a2P
74AUP2G157GN	аР
74AUP2G157GS	аР

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

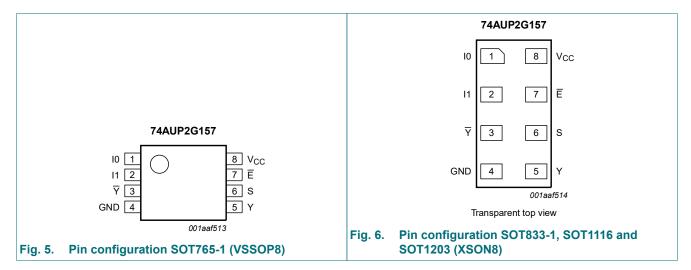
## 5. Functional diagram



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# 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
10	1	data input from source 0
11	2	data input from source 1
Ϋ	3	complement multiplexer output
GND	4	ground (0 V)
Υ	5	true multiplexer output
S	6	data select input
E	7	enable input (active LOW)
V <sub>CC</sub>	8	supply voltage

# 7. Functional description

### **Table 4. Function table**

H = HIGH voltage level; L = LOW voltage level; X = don't care.

Input		Output			
E	S	10	<b>I</b> 1	Υ	Y
Н	Х	Х	Х	L	Н
L	L	L	Х	L	Н
L	L	Н	Х	Н	L
L	Н	Х	L	L	Н
L	Н	Х	Н	Н	L

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## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
lok	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>	-	±20	mA
I <sub>CC</sub>	supply current		-	50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C [2]	-	250	mW

<sup>[1]</sup> The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

## 9. Recommended operating conditions

#### Table 6. Operating conditions

Table 6. Operating conditions									
Symbol	Parameter	Conditions	Min	Max	Unit				
V <sub>CC</sub>	supply voltage		0.8	3.6	V				
VI	input voltage		0	3.6	V				
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V				
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V				
T <sub>amb</sub>	ambient temperature		-40	+125	°C				
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	0	200	ns/V				

<sup>[2]</sup> For SOT765-1 (VSSOP8) package: Ptot derates linearly with 4.9 mW/K above 99 °C.

For SOT833-1 (XSON8) package: Ptot derates linearly with 3.1 mW/K above 68 °C.

For SOT1116 (XSON8) package: Ptot derates linearly with 4.2 mW/K above 90 °C.

For SOT1203 (XSON8) package: Ptot derates linearly with 3.6 mW/K above 81 °C.

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## 10. Static characteristics

#### **Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbo	l Parameter	Conditions		Тур	Max	Unit
T <sub>amb</sub> =	25 °C					'
V <sub>IH</sub>	HIGH-level input	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
	voltage	V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
	voltage	V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	$I_O = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V		-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		$I_{O}$ = -3.1 mA; $V_{CC}$ = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	$I_O = 20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V		-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	μΑ
l <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V to 0.2 V	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.5	μA
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	40	μΑ
Cı	input capacitance	$V_{CC}$ = 0 V to 3.6 V; $V_I$ = GND or $V_{CC}$	-	0.6	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.3	-	pF

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
	voltage	V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
	voltage	V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V		-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		$I_{O}$ = -1.9 mA; $V_{CC}$ = 1.65 V		-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output	$V_I = V_{IH}$ or $V_{IL}$				
	voltage	I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0 \text{ V}$ to 3.6 V; $V_{CC} = 0 \text{ V}$	-	-	±0.5	μΑ
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V to 0.2 V	-	-	±0.6	μΑ
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ [1]	-	-	50	μΑ

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
T <sub>amb</sub> = -	40 °C to +125 °C		'				
V <sub>IH</sub>	HIGH-level input	V <sub>CC</sub> = 0.8 V	0.75V <sub>CC</sub>	-	-	V	
	voltage	V <sub>CC</sub> = 0.9 V to 1.95 V	0.70V <sub>CC</sub>	-	-	V	
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V	
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V	
V <sub>IL</sub>	LOW-level input	V <sub>CC</sub> = 0.8 V	-	-	0.25V <sub>CC</sub>	V	
	voltage	V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30V <sub>CC</sub>	V	
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V	
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V	
V <sub>OH</sub>	HIGH-level output	$V_I = V_{IH}$ or $V_{IL}$					
	voltage	I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V	
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6V <sub>CC</sub>	-	-	V	
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V		0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V	
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V	
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V	
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V	
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V	
V <sub>OL</sub>	LOW-level output	$V_I = V_{IH}$ or $V_{IL}$					
	voltage	I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V	
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33V <sub>CC</sub>	V	
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V	
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V	
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V	
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V	
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V	
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V	
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	μA	
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μA	
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V to 0.2 V	-	-	±0.75	μA	
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μΑ	
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	[1] -	-	75	μA	

<sup>[1]</sup> One input at  $V_{CC}$  - 0.6 V, other input at  $V_{CC}$  or GND.

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# 11. Dynamic characteristics

### **Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9.

Symbol	Parameter	Conditions	Т	<sub>amb</sub> = 25 <sup>c</sup>	C	T <sub>an</sub>	<sub>nb</sub> = o +85 °C	T <sub>amb</sub> = -40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F									
t <sub>pd</sub>	propagation	I0, I1 to Y, ₹; see <u>Fig. 7</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	21.2	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.5	6.1	13.3	2.2	13.8	2.2	13.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.9	4.2	7.8	2.0	8.4	2.0	8.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.7	3.4	6.2	1.6	6.9	1.6	7.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.5	2.7	4.3	1.2	4.9	1.2	5.2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.3	2.4	3.7	1.0	4.0	1.0	4.2	ns
		S to Y, $\overline{Y}$ ; see $\underline{\text{Fig. 7}}$ [2]								
		V <sub>CC</sub> = 0.8 V	-	23.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	6.6	13.8	2.2	14.3	2.2	14.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.9	4.5	8.0	2.1	8.7	2.1	9.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.7	3.6	6.3	1.6	7.0	1.6	7.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	2.8	4.4	1.2	5.0	1.2	5.3	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.3	2.5	3.7	1.0	4.0	1.0	4.2	ns
		$\overline{E}$ to Y, $\overline{Y}$ ; see $\underline{Fig. 8}$ [2]								
		V <sub>CC</sub> = 0.8 V	-	22.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.7	6.4	13.7	2.5	14.3	2.5	14.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.1	4.4	8.0	2.1	8.7	2.1	9.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.8	3.6	6.3	1.6	7.0	1.6	7.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	2.8	4.2	1.4	4.8	1.4	5.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.5	3.6	1.1	3.9	1.1	4.2	ns

Symbol	Parameter	Conditions	Т	T <sub>amb</sub> = 25 °C		T <sub>an</sub>	<sub>nb</sub> = o +85 °C	T <sub>amb</sub> = -40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 10	pF									
t <sub>pd</sub>	propagation	I0, I1 to Y, ₹; see <u>Fig. 7</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	24.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.9	6.9	15.1	2.5	15.6	2.5	15.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	4.8	8.9	2.4	9.6	2.4	10.0	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.1	4.0	7.1	1.9	7.9	1.9	8.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.9	3.2	5.0	1.6	5.7	1.6	6.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	2.9	4.4	1.3	4.7	1.3	5.0	ns
		S to Y, $\overline{Y}$ ; see Fig. 7 [2]								
		V <sub>CC</sub> = 0.8 V	-	27.2	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.0	7.4	15.5	2.6	16.1	2.6	16.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.3	5.1	9.0	2.4	9.8	2.4	10.3	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.1	4.2	7.2	1.9	8.0	1.9	8.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.9	3.4	5.1	1.6	5.7	1.6	6.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	3.0	4.4	1.4	4.7	1.4	5.0	ns
		$\overline{E}$ to Y, $\overline{Y}$ ; see $\underline{Fig. 8}$ [2]								
		V <sub>CC</sub> = 0.8 V	-	25.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.1	7.2	15.5	2.8	16.1	2.8	16.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	5.0	9.0	2.4	9.8	2.4	10.3	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.2	4.1	7.1	1.9	8.0	1.9	8.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.9	3.3	4.9	1.7	5.5	1.7	5.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	3.0	4.2	1.5	4.6	1.5	4.8	ns

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Symbol	Parameter	Conditions	T	<sub>amb</sub> = 25 °	C.	T <sub>an</sub>	<sub>nb</sub> = o +85 °C	T <sub>ar</sub> -40 °C to	<sub>ոь</sub> = o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 15 <sub>I</sub>	pF									
t <sub>pd</sub>	propagation	I0, I1 to Y, ₹; see <u>Fig. 7</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	27.8	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.3	7.7	16.8	2.8	17.4	2.8	17.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	5.4	9.8	2.7	10.6	2.7	11.2	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.4	4.4	7.8	2.2	8.7	2.2	9.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.2	3.7	5.6	1.9	6.4	1.9	6.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	3.4	4.9	1.6	5.3	1.6	5.6	ns
		S to Y, $\overline{Y}$ ; see Fig. 7 [2]								
		V <sub>CC</sub> = 0.8 V	-	30.7	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.3	8.2	17.2	2.9	17.9	2.9	18.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	5.7	10.0	2.7	10.9	2.7	11.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.4	4.7	7.9	2.2	8.9	2.2	9.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.2	3.8	5.7	1.9	6.5	1.9	6.8	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	3.5	5.0	1.6	5.4	1.6	5.7	ns
		$\overline{E}$ to Y, $\overline{Y}$ ; see $\underline{Fig. 8}$ [2]								
		V <sub>CC</sub> = 0.8 V	-	29.1	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.5	8.0	17.2	3.1	17.9	3.1	18.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.8	5.6	9.9	2.7	10.9	2.7	11.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.4	4.6	7.9	2.2	8.9	2.2	9.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.2	3.8	5.5	2.0	6.2	2.0	6.6	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	3.4	4.7	1.8	5.1	1.8	5.4	ns

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### Low-power 2-input multiplexer

Symbol Parameter		Conditions	T <sub>amb</sub> = 25 °C		T <sub>amb</sub> = -40 °C to +85 °C		T <sub>amb</sub> = -40 °C to +125 °C		Unit	
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 30	pF				•					,
t <sub>pd</sub>	propagation	I0, I1 to Y, ₹; see <u>Fig. 7</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	35.4	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.3	9.8	21.6	3.7	22.5	3.7	22.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.3	6.9	12.4	3.4	13.6	3.4	14.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.1	5.7	10.0	2.8	11.3	2.8	11.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.9	4.8	7.2	2.6	8.2	2.6	8.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.8	4.4	6.4	2.3	6.9	2.3	7.3	ns
		S to Y, $\overline{Y}$ ; see $\underline{\text{Fig. 7}}$ [2]								
		V <sub>CC</sub> = 0.8 V	-	38.8	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.4	10.5	22.0	3.7	23.0	3.7	23.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.3	7.2	12.6	3.5	13.9	3.5	14.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.1	5.9	10.1	2.8	11.4	2.8	12.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.9	4.9	7.3	2.6	8.3	2.6	8.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.7	4.5	6.4	2.3	6.9	2.3	7.3	ns
		$\overline{E}$ to Y, $\overline{Y}$ ; see $\underline{Fig. 8}$ [2]								
		V <sub>CC</sub> = 0.8 V	-	36.8	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.4	10.1	22.1	3.9	23.0	3.9	23.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.6	7.1	12.6	3.5	13.8	3.5	14.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.1	5.8	10.0	2.8	11.3	2.8	12.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.9	4.9	7.1	2.7	8.0	2.7	8.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.7	4.5	6.2	2.4	6.7	2.4	7.0	ns
C <sub>L</sub> = 5 p	F, 10 pF, 15 p	F and 30 pF			•					•
C <sub>PD</sub>	power dissipation capacitance	$f_i = 1 \text{ MHz};$ [3] $V_I = \text{GND to } V_{CC}$								
		V <sub>CC</sub> = 0.8 V	-	5.2	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	5.5	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	5.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	6.0	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	6.9	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	7.9	-	-	-	-	-	pF

<sup>[1]</sup> All typical values are measured at nominal  $V_{\text{CC}}$ .

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

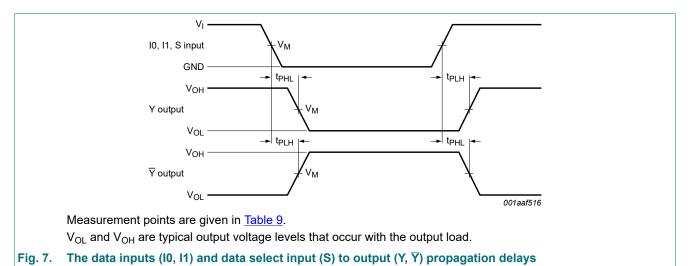
V<sub>CC</sub> = supply voltage in V;

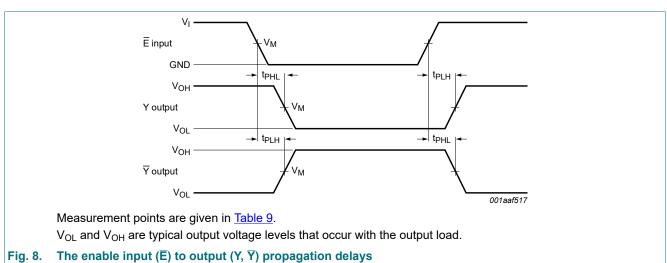
N = number of inputs switching;  $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

 <sup>[2]</sup> t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>
 [3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).
 P<sub>D</sub> = C<sub>PD</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>i</sub> × N + Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) where:
 f<sub>i</sub> = input frequency in MHz;

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### 11.1. Waveforms and test circuit

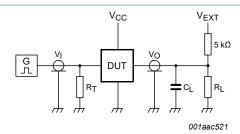




**Table 9. Measurement points** 

Supply voltage	Output	Input			
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	V <sub>I</sub>	$t_r = t_f$	
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	

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Test data is given in Table 10.

Definitions for test circuit:

R<sub>L</sub> = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

V<sub>EXT</sub> = External voltage for measuring switching times.

### Fig. 9. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Load	V <sub>EXT</sub>			
V <sub>CC</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V <sub>CC</sub>

[1] For measuring enable and disable times  $R_L$  = 5 k $\Omega$ . For measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1 M $\Omega$ .

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# 12. Package outline

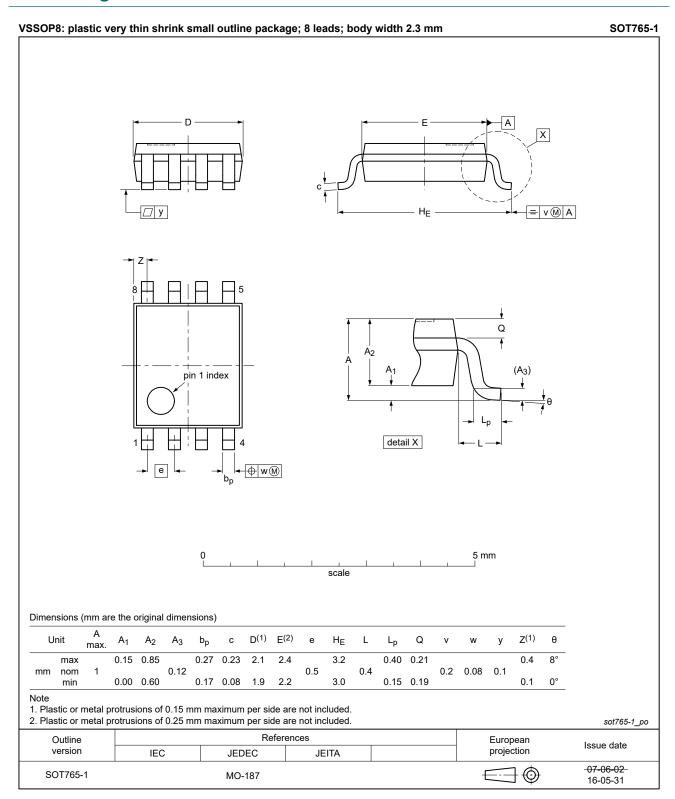


Fig. 10. Package outline SOT765-1 (VSSOP8)

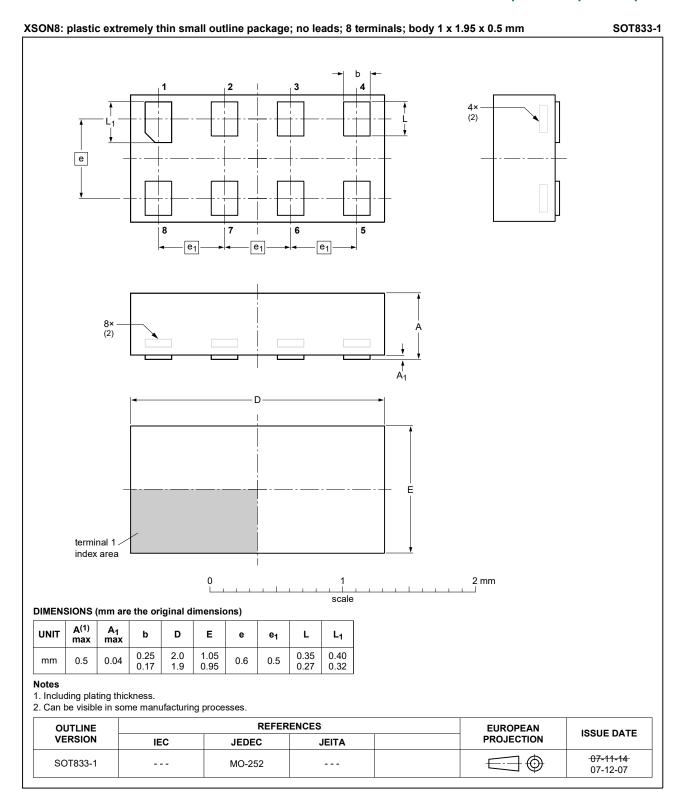


Fig. 11. Package outline SOT833-1 (XSON8)

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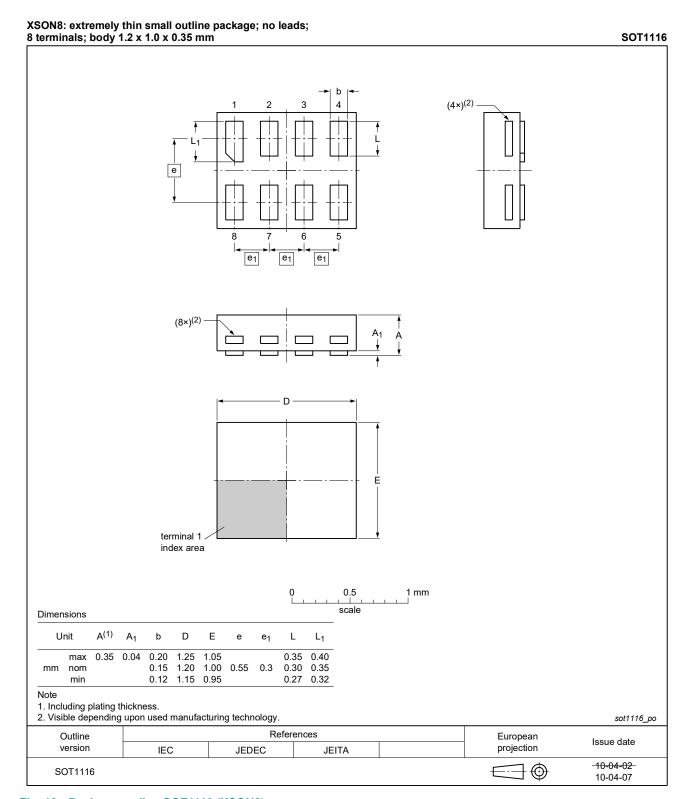


Fig. 12. Package outline SOT1116 (XSON8)

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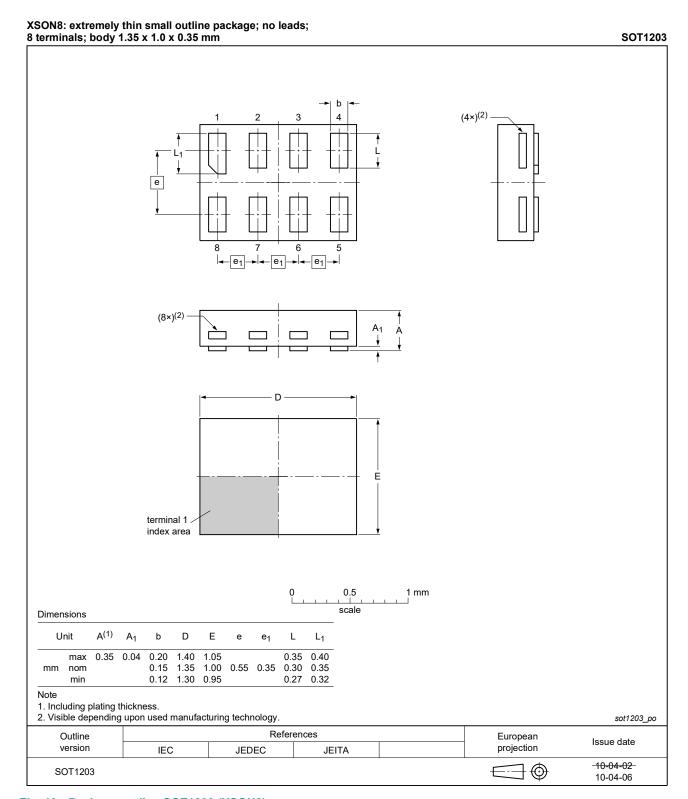


Fig. 13. Package outline SOT1203 (XSON8)

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## 13. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model

# 14. Revision history

### **Table 12. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74AUP2G157 v.10	20230726	Product data sheet	-	74AUP2G157 v.9		
Modifications:	Section 2: Es	SD specification updated a	ccording to the lat	est JEDEC standard.		
74AUP2G157 v.9	20201201	Product data sheet	-	74AUP2G157 v.8		
Modifications:	<ul> <li><u>Section 8</u>: Derating values for P<sub>tot</sub> total power dissipation have been updated.</li> <li>Type numbers 74AUP2G157GF (SOT1089/XSON8) and 74AUP2G157GM (SOT902-1/XQFN8) removed.</li> </ul>					
74AUP2G157 v.8	20190315	Product data sheet	-	74AUP2G157 v.7		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type number 74AUP2G157GD (SOT996-2) removed.</li> <li>Package outline drawing SOT765-1 (VSSOP8) updated.</li> <li>Package outline drawing SOT902-2 (XQFN8) updated.</li> </ul>					
74AUP2G157 v.7	20130118	Product data sheet	-	74AUP2G157 v.6		
Modifications:	For type num	For type number 74AUP2G157GD XSON8U has changed to XSON8.				
74AUP2G157 v.6	20120606	Product data sheet	-	74AUP2G157 v.5		
74AUP2G157 v.5	20111205	Product data sheet	-	74AUP2G157 v.4		
74AUP2G157 v.4	20100730	Product data sheet	-	74AUP2G157 v.3		
74AUP2G157 v.3	20080702	Product data sheet	-	74AUP2G157 v.2		
74AUP2G157 v.2	20080219	Product data sheet	-	74AUP2G157 v.1		
74AUP2G157 v.1	20061006	Product data sheet	-	-		

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### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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