# 74AUP1G332

# Low-power 3-input OR-gate

Rev. 8 — 18 July 2023

**Product data sheet** 

### 1. General description

The 74AUP1G332 is a single 3-input OR gate. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- · High noise immunity
- · Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

# 3. Ordering information

**Table 1. Ordering information** 

Type number	Package									
	Temperature range Name Description									
74AUP1G332GW	-40 °C to +125 °C	TSSOP6	plastic thin shrink small outline package; 6 leads; body width 1.25 mm	SOT363-2						
74AUP1G332GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886						
74AUP1G332GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	<u>SOT1115</u>						
74AUP1G332GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202						



Low-power 3-input OR-gate

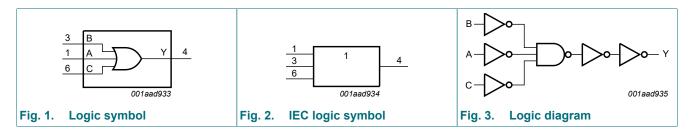
# 4. Marking

#### Table 2. Marking

Type number	Marking code [1]
74AUP1G332GW	aG
74AUP1G332GM	aG
74AUP1G332GN	aG
74AUP1G332GS	aG

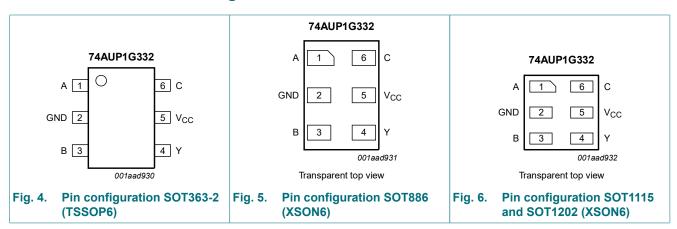
<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 5. Functional diagram



# 6. Pinning information

### 6.1. Pinning



# 6.2. Pin description

**Table 3. Pin description** 

Symbol	Pin	Description
A	1	data input A
GND	2	ground (0 V)
В	3	data input B
Υ	4	data output Y
Vcc	5	supply voltage
С	6	data input C

Low-power 3-input OR-gate

### 7. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$ 

Input	Output		
Α	В	С	Υ
Н	X	X	Н
X	Н	X	Н
X	X	Н	Н
L	L	L	L

# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
VI	input voltage	[1	] -0.5	+4.6	V
lok	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1	] -0.5	+4.6	V
Io	output current	$V_O = 0 \text{ V to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$	-	250	mW

<sup>[1]</sup> The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	0	200	ns/V

<sup>[2]</sup> For SOT363-2 (TSSOP6) package: P<sub>tot</sub> derates linearly with 3.7 mW/K above 83 °C.

For SOT886 (XSON6) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: Ptot derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

Low-power 3-input OR-gate

# 10. Static characteristics

**Table 7. Static characteristics** 

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	25 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 $V$ to 3.6 $V$	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μA
Δl <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.5	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_I$ = $V_{CC}$ - 0.6 V; $I_O$ = 0 A; $V_{CC}$ = 3.3 V; One input at $V_{CC}$ - 0.6 V, other inputs at $V_{CC}$ or GND	-	-	40	μΑ
Cı	input capacitance	$V_{CC}$ = 0 V to 3.6 V; $V_{I}$ = GND or $V_{CC}$	-	8.0	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -4	40 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 $V$ to 3.6 $V$	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		$I_{O}$ = -2.3 mA; $V_{CC}$ = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.5	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μΑ
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_I$ = $V_{CC}$ - 0.6 V; $I_O$ = 0 A; $V_{CC}$ = 3.3 V; One input at $V_{CC}$ - 0.6 V, other inputs at $V_{CC}$ or GND	-	-	50	μΑ

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 $V$ to 3.6 $V$	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		$I_{O}$ = -2.3 mA; $V_{CC}$ = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH}$ or $V_{IL}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.75	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_I$ = $V_{CC}$ - 0.6 V; $I_O$ = 0 A; $V_{CC}$ = 3.3 V; One input at $V_{CC}$ - 0.6 V, other inputs at $V_{CC}$ or GND	-	-	75	μΑ

**Low-power 3-input OR-gate** 

# 11. Dynamic characteristics

### **Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 8.

Symbol	Parameter	Conditions	Ta	<sub>amb</sub> = 25	°C	T <sub>amb</sub> = -40 °C to +85 °C		T <sub>amb</sub> = -40 °C to +125 °C		Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F				•					
t <sub>pd</sub>	propagation	A, B and C to Y; see Fig. 7 [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	17.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.3	5.2	10.2	2.0	10.3	2.0	10.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.7	3.7	6.0	1.9	6.4	1.9	6.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.6	3.0	4.7	1.4	5.2	1.4	5.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.3	3.3	1.2	3.7	1.2	3.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.2	2.1	2.9	1.1	3.1	1.1	3.3	ns
C <sub>L</sub> = 10	pF									
t <sub>pd</sub>	propagation	A, B and C to Y; see Fig. 7 [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	17.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.5	6.1	11.9	2.4	12.0	2.4	12.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	4.3	7.1	2.0	7.3	2.0	7.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.1	3.5	5.4	1.9	5.8	1.9	6.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	2.9	4.0	1.5	4.5	1.5	4.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.5	2.6	3.7	1.4	3.9	1.4	4.1	ns
C <sub>L</sub> = 15	pF									
t <sub>pd</sub>	propagation	A, B and C to Y; see Fig. 7 [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	23.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.9	6.9	13.5	2.7	13.6	2.7	13.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	4.9	7.8	2.4	8.5	2.4	8.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.2	4.0	6.2	2.1	6.8	2.1	7.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.0	3.3	4.7	1.6	5.2	1.6	5.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.9	3.1	4.2	1.7	4.5	1.7	4.8	ns
C <sub>L</sub> = 30	pF				•					
t <sub>pd</sub>	propagation	A, B and C to Y; see Fig. 7 [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	36.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	9.2	17.9	3.5	18.4	3.5	18.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.2	6.4	10.4	3.3	11.4	3.3	11.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.0	5.3	8.3	2.9	9.1	2.9	9.6	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.8	4.4	6.2	1.6	6.7	1.6	7.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.6	4.2	5.5	1.4	6.4	1.4	6.7	ns

74AUP1G332 **Nexperia** 

#### Low-power 3-input OR-gate

Symbol	Parameter	Conditions		<sub>amb</sub> = 25 °	°C	T <sub>am</sub> -40 °C to	<sub>nb</sub> = o +85 °C	T <sub>an</sub> -40 °C to	<sub>nb</sub> = 0 +125 °C	Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F, 10 pF, 15 pF	and 30 pF								
C <sub>PD</sub>	power	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]								
	dissipation capacitance	V <sub>CC</sub> = 0.8 V	-	2.5	-	-	-	-	-	pF
	oapaoitarioc	V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.5	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF

- All typical values are measured at nominal V<sub>CC</sub>.
- $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- $\dot{C}_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

f<sub>i</sub> = input frequency in MHz;

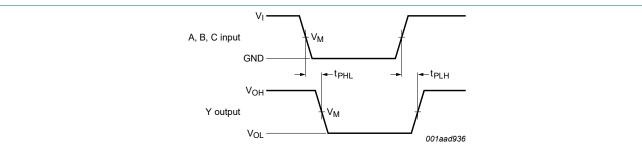
f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;  $\Sigma(C_L \times V_{CC}^2 \times f_0)$  = sum of the outputs.

### 11.1. Waveform and test circuit



Measurement points are given in Table 9.

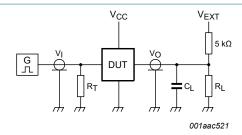
Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage drop that occur with the output load.

Fig. 7. Input A, B and C to output Y propagation delay times

Table 9. Measurement points

Supply voltage	Output	Input				
V <sub>CC</sub>	V <sub>M</sub>	$V_{M}$ $V_{I}$ $t_{r} = t_{f}$				
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns		

### Low-power 3-input OR-gate



Test data is given in Table 10.

Definitions for test circuit:

 $R_L$  = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

 $V_{\text{EXT}}$  = External voltage for measuring switching times.

### Fig. 8. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Load		V <sub>EXT</sub>		
V <sub>CC</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V <sub>CC</sub>

[1] For measuring enable and disable times  $R_L$  = 5 k $\Omega$ . For measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1 M $\Omega$ .

Low-power 3-input OR-gate

# 12. Package outline

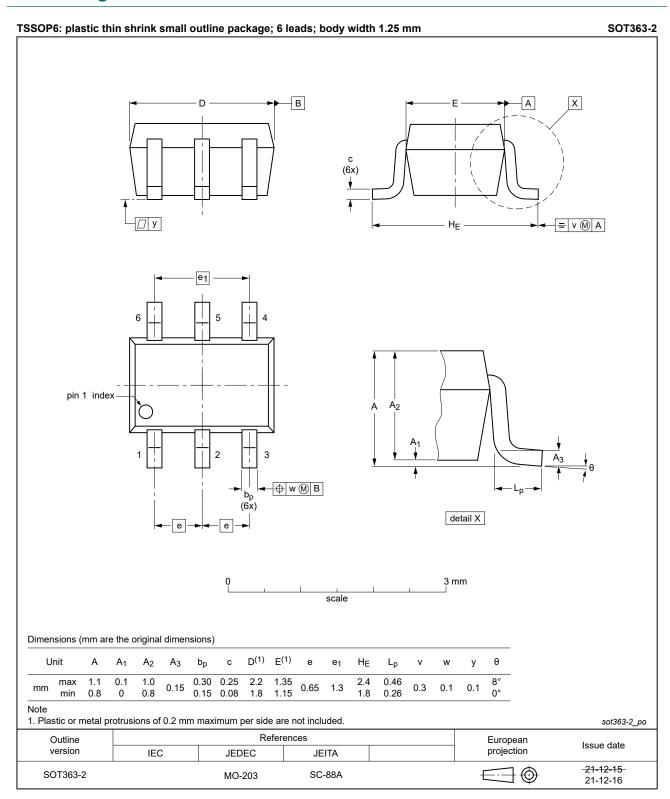


Fig. 9. Package outline SOT363-2 (TSSOP6)

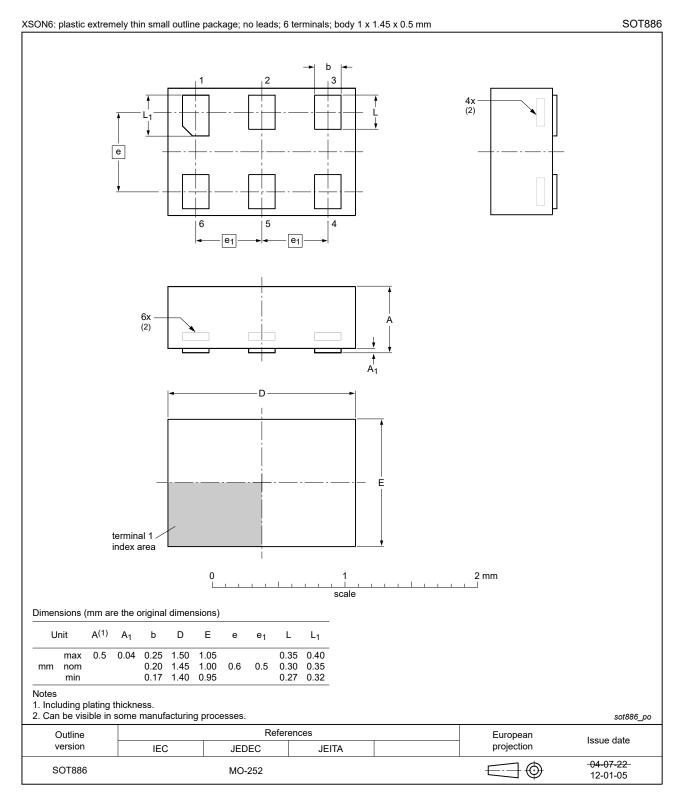


Fig. 10. Package outline SOT886 (XSON6)

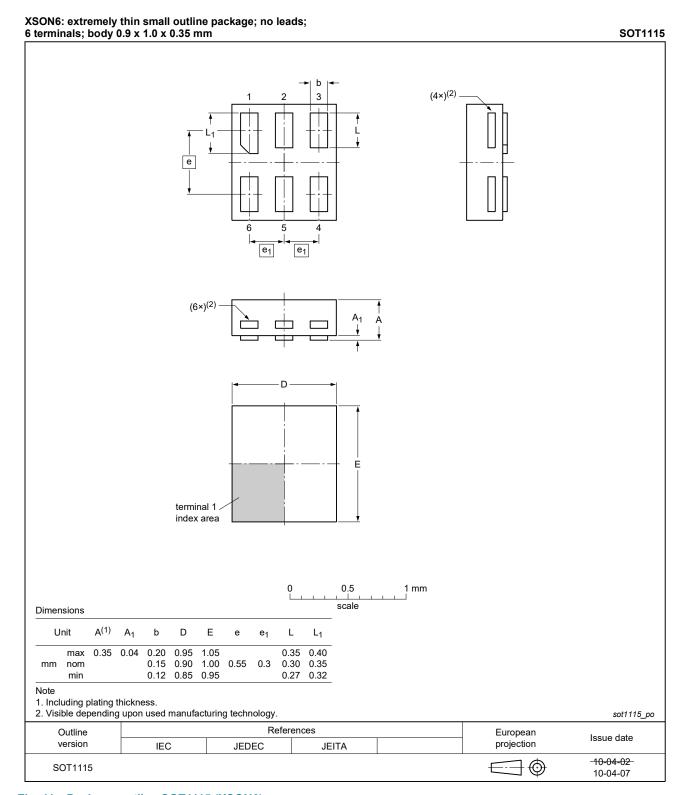


Fig. 11. Package outline SOT1115 (XSON6)

Low-power 3-input OR-gate

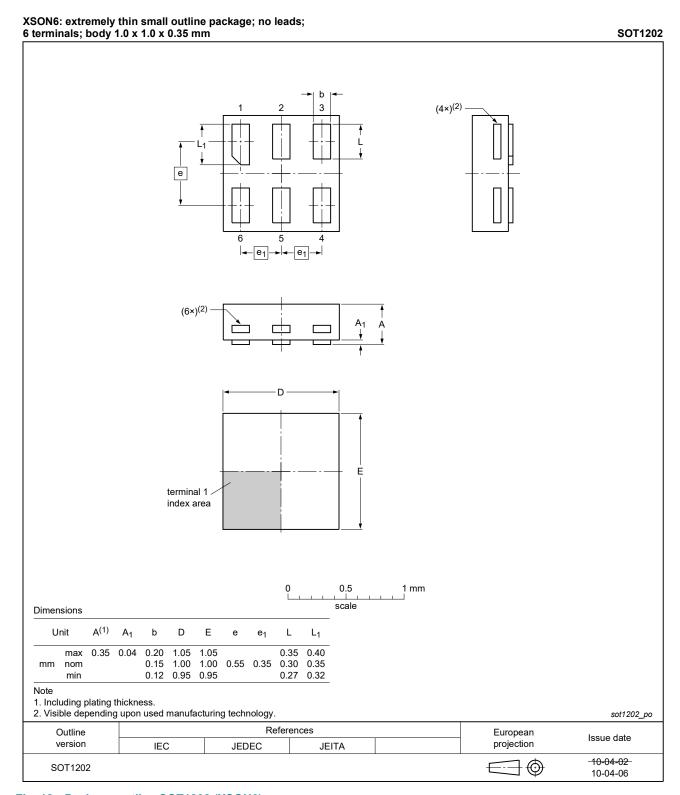


Fig. 12. Package outline SOT1202 (XSON6)

13 / 16

**Low-power 3-input OR-gate** 

### 13. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model

# 14. Revision history

### **Table 12. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G332 v.8	20230718	Product data sheet	-	74AUP1G332 v.7
Modifications:	Section 2: E	SD specification updated a	according to the la	atest JEDEC standard.
74AUP1G332 v.7	20220120	Product data sheet	-	74AUP1G332 v.6
Modifications:		nd <u>Section 2</u> updated. DT363 (SC-88) changed to	SOT363-2 (TSSC	DP6).
74AUP1G332 v.6	20201203	Product data sheet	-	74AUP1G332 v.5
Modifications:	guidelines o Legal texts I Type numbe	of this data sheet has been f Nexperia. nave been adapted to the r er 74AUP1G332GF (SOT8) rating values for P <sub>tot</sub> total p	new company nan 91 / XSON6) remo	ne where appropriate.
74AUP1G332 v.5	20120704	Product data sheet	-	74AUP1G332 v.4
Modifications:	Package ou	tline drawing of SOT886 (F	ig. 10) modified.	
74AUP1G332 v.4	20111125	Product data sheet	-	74AUP1G332 v.3
Modifications:	<ul> <li>Legal pages</li> </ul>	updated.		
74AUP1G332 v.3	20101007	Product data sheet	-	74AUP1G332 v.2
74AUP1G332 v.2	20080229	Product data sheet	-	74AUP1G332 v.1
74AUP1G332 v.1	20061113	Product data sheet	-	-

#### Low-power 3-input OR-gate

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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### Low-power 3-input OR-gate

### **Contents**

1.	General description	. 1
2.	Features and benefits	. 1
3.	Ordering information	1
4.	Marking	. 2
5.	Functional diagram	2
6.	Pinning information	2
6.1.	Pinning	2
6.2	. Pin description	. 2
7.	Functional description	. 3
8.	Limiting values	. 3
9.	Recommended operating conditions	3
10.	Static characteristics	4
11.	Dynamic characteristics	7
11.1	Waveform and test circuit	. 8
12.	Package outline	10
13.	Abbreviations	14
14.	Revision history	14
	Legal information	

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