

# 74AHC08; 74AHCT08

## Quad 2-input AND gate

Rev. 7 — 23 April 2025

Product data sheet

## 1. General description

The 74AHC08; 74AHCT08 are quad 2-input AND gates. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

## 2. Features and benefits

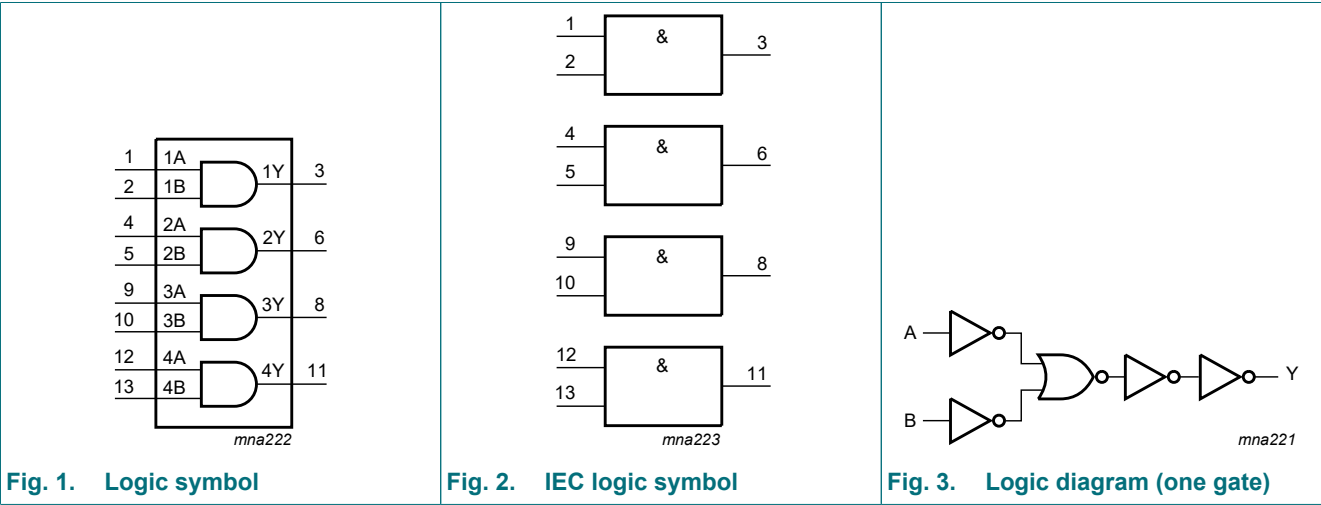
- Wide supply voltage range from 2.0 V to 5.5 V
- Input levels:
  - For 74AHC08: CMOS level
  - For 74AHCT08: TTL level
- Balanced propagation delays
- All inputs have a Schmitt-trigger action
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

## 3. Ordering information

Table 1. Ordering information

| Type number   | Package           |          |  |                           |
|---|-------------------|----------|--|---------------------------|
|   | Temperature range | Name     | Description  | Version                   |
| <a href="#">74AHC08D</a><br><a href="#">74AHCT08D</a>   | -40 °C to +125 °C | SO14     | plastic small outline package; 14 leads;<br>body width 3.9 mm  | <a href="#">SOT108-1</a>  |
| <a href="#">74AHC08PW</a><br><a href="#">74AHCT08PW</a> | -40 °C to +125 °C | TSSOP14  | plastic thin shrink small outline package; 14 leads;<br>body width 4.4 mm  | <a href="#">SOT402-1</a>  |
| <a href="#">74AHC08BQ</a><br><a href="#">74AHCT08BQ</a> | -40 °C to +125 °C | DHVQFN14 | plastic dual in-line compatible thermal<br>enhanced very thin quad flat package; no leads;<br>14 terminals; body 2.5 × 3 × 0.85 mm                                   | <a href="#">SOT762-1</a>  |
| <a href="#">74AHC08BZ</a><br><a href="#">74AHCT08BZ</a> | -40 °C to +125 °C | DHXQFN14 | plastic, leadless dual in-line compatible thermal<br>enhanced extreme thin quad flat package;<br>no leads; 14 terminals; 0.4 mm pitch;<br>body 2 mm × 2 mm × 0.48 mm | <a href="#">SOT8014-1</a> |

4. Functional diagram



5. Pinning information

5.1. Pinning

**D package**  
**SOT108-1 (SO14)**

aaa-036070

**PW package**  
**SOT402-1 (TSSOP14)**

aaa-036071

**BQ package**  
**SOT762-1 (DHVQFN14)**

aaa-036072

Transparent top view

**BZ package**  
**SOT8014-1 (DHXQFN14)**

aaa-041206

Transparent top view

(1) This is not a ground pin. There is no electrical or mechanical requirement to solder the pad. In case soldered, the solder land should remain floating or connected to GND

5.2. Pin description

Table 2. Pin description

| Symbol          | Pin          | Description    |
|-----------------|--------------|----------------|
| 1A, 2A, 3A, 4A  | 1, 4, 9, 12  | data inputs    |
| 1B, 2B, 3B, 4B  | 2, 5, 10, 13 | data inputs    |
| 1Y, 2Y, 3Y, 4Y  | 3, 6, 8, 11  | data outputs   |
| GND             | 7            | ground (0 V)   |
| V <sub>CC</sub> | 14           | supply voltage |

6. Functional description

Table 3. Function selection

H = HIGH voltage level; L = LOW voltage level; X = don't care

| Input |    | Output |
|-------|----|--------|
| nA    | nB | nY     |
| L     | X  | L      |
| X     | L  | L      |
| H     | H  | H      |

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter               | Conditions   | Min  | Max  | Unit |
|------------------|-------------------------|--|------|------|------|
| V <sub>CC</sub>  | supply voltage          |  | -0.5 | +7.0 | V    |
| V <sub>I</sub>   | input voltage           |  | -0.5 | +7.0 | V    |
| I <sub>IK</sub>  | input clamping current  | V <sub>I</sub> < -0.5 V [1]  | -20  | -    | mA   |
| I <sub>OK</sub>  | output clamping current | V <sub>O</sub> < -0.5 V or V <sub>O</sub> > V <sub>CC</sub> + 0.5 V [1]  | -    | ±20  | mA   |
| I <sub>O</sub>   | output current          | V <sub>O</sub> = -0.5 V to (V <sub>CC</sub> + 0.5 V)                     | -    | ±25  | mA   |
| I <sub>CC</sub>  | supply current          |  | -    | 75   | mA   |
| I <sub>GND</sub> | ground current          |  | -75  | -    | mA   |
| T <sub>stg</sub> | storage temperature     |  | -65  | +150 | °C   |
| P <sub>tot</sub> | total power dissipation | SOT108-1; SOT402-1; SOT762-1<br>T <sub>amb</sub> = -40 °C to +125 °C [2] | -    | 500  | mW   |
|                  |                         | SOT8014-1<br>T <sub>amb</sub> = -40 °C to +125 °C [3]                    | -    | 250  | mW   |

- [1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
- [2] For SOT108-1 (SO14) package: P<sub>tot</sub> derates linearly with 10.1 mW/K above 100 °C.  
For SOT402-1 (TSSOP14) package: P<sub>tot</sub> derates linearly with 7.3 mW/K above 81 °C.  
For SOT762-1 (DHVQFN14) package: P<sub>tot</sub> derates linearly with 9.6 mW/K above 98 °C.
- [3] For SOT8014-1 (DHXQFN14) package: P<sub>tot</sub> derates linearly with 8.7 mW/K above 121 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                           | Conditions                      | 74AHC08 |     |                 | 74AHCT08 |     |                 | Unit |
|------------------|-------------------------------------|---------------------------------|---------|-----|-----------------|----------|-----|-----------------|------|
|                  |                                     |                                 | Min     | Typ | Max             | Min      | Typ | Max             |      |
| V <sub>CC</sub>  | supply voltage                      |                                 | 2.0     | 5.0 | 5.5             | 4.5      | 5.0 | 5.5             | V    |
| V <sub>I</sub>   | input voltage                       |                                 | 0       | -   | 5.5             | 0        | -   | 5.5             | V    |
| V <sub>O</sub>   | output voltage                      |                                 | 0       | -   | V <sub>CC</sub> | 0        | -   | V <sub>CC</sub> | V    |
| T <sub>amb</sub> | ambient temperature                 |                                 | -40     | +25 | +125            | -40      | +25 | +125            | °C   |
| Δt/ΔV            | input transition rise and fall rate | V <sub>CC</sub> = 3.3 V ± 0.3 V | -       | -   | 100             | -        | -   | -               | ns/V |
|                  |                                     | V <sub>CC</sub> = 5.0 V ± 0.5 V | -       | -   | 20              | -        | -   | 20              | ns/V |

9. Static characteristics

Table 6. Static characteristics  
Voltages are referenced to GND (ground = 0 V).

| Symbol          | Parameter                 | Conditions   | 25 °C |     |      | -40 °C to +85 °C |      | -40 °C to +125 °C |      | Unit |
|-----------------|---------------------------|--|-------|-----|------|------------------|------|-------------------|------|------|
|                 |                           |  | Min   | Typ | Max  | Min              | Max  | Min               | Max  |      |
| 74AHC08         |                           |  |       |     |      |                  |      |                   |      |      |
| V <sub>IH</sub> | HIGH-level input voltage  | V <sub>CC</sub> = 2.0 V  | 1.5   | -   | -    | 1.5              | -    | 1.5               | -    | V    |
|                 |                           | V <sub>CC</sub> = 3.0 V  | 2.1   | -   | -    | 2.1              | -    | 2.1               | -    | V    |
|                 |                           | V <sub>CC</sub> = 5.5 V  | 3.85  | -   | -    | 3.85             | -    | 3.85              | -    | V    |
| V <sub>IL</sub> | LOW-level input voltage   | V <sub>CC</sub> = 2.0 V  | -     | -   | 0.5  | -                | 0.5  | -                 | 0.5  | V    |
|                 |                           | V <sub>CC</sub> = 3.0 V  | -     | -   | 0.9  | -                | 0.9  | -                 | 0.9  | V    |
|                 |                           | V <sub>CC</sub> = 5.5 V  | -     | -   | 1.65 | -                | 1.65 | -                 | 1.65 | V    |
| V <sub>OH</sub> | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                    |       |     |      |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = -50 µA; V <sub>CC</sub> = 2.0 V                                       | 1.9   | 2.0 | -    | 1.9              | -    | 1.9               | -    | V    |
|                 |                           | I <sub>O</sub> = -50 µA; V <sub>CC</sub> = 3.0 V                                       | 2.9   | 3.0 | -    | 2.9              | -    | 2.9               | -    | V    |
|                 |                           | I <sub>O</sub> = -50 µA; V <sub>CC</sub> = 4.5 V                                       | 4.4   | 4.5 | -    | 4.4              | -    | 4.4               | -    | V    |
|                 |                           | I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V                                      | 2.58  | -   | -    | 2.48             | -    | 2.4               | -    | V    |
|                 |                           | I <sub>O</sub> = -8.0 mA; V <sub>CC</sub> = 4.5 V                                      | 3.94  | -   | -    | 3.8              | -    | 3.7               | -    | V    |
| V <sub>OL</sub> | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                    |       |     |      |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = 50 µA; V <sub>CC</sub> = 2.0 V  | -     | 0   | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 50 µA; V <sub>CC</sub> = 3.0 V  | -     | 0   | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 50 µA; V <sub>CC</sub> = 4.5 V  | -     | 0   | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V                                       | -     | -   | 0.36 | -                | 0.44 | -                 | 0.55 | V    |
|                 |                           | I <sub>O</sub> = 8.0 mA; V <sub>CC</sub> = 4.5 V                                       | -     | -   | 0.36 | -                | 0.44 | -                 | 0.55 | V    |
| I <sub>I</sub>  | input leakage current     | V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V                          | -     | -   | 0.1  | -                | 1.0  | -                 | 2.0  | µA   |
| I <sub>CC</sub> | supply current            | V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 5.5 V | -     | -   | 2.0  | -                | 20   | -                 | 40   | µA   |
| C <sub>I</sub>  | input capacitance         |  | -     | 3.0 | 10   | -                | 10   | -                 | 10   | pF   |
| 74AHCT08        |                           |  |       |     |      |                  |      |                   |      |      |
| V <sub>IH</sub> | HIGH-level input voltage  | V <sub>CC</sub> = 4.5 V to 5.5 V   | 2.0   | -   | -    | 2.0              | -    | 2.0               | -    | V    |
| V <sub>IL</sub> | LOW-level input voltage   | V <sub>CC</sub> = 4.5 V to 5.5 V   | -     | -   | 0.8  | -                | 0.8  | -                 | 0.8  | V    |
| V <sub>OH</sub> | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> ; V <sub>CC</sub> = 4.5 V          |       |     |      |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = -50 µA  | 4.4   | 4.5 | -    | 4.4              | -    | 4.4               | -    | V    |
|                 |                           | I <sub>O</sub> = -8.0 mA   | 3.94  | -   | -    | 3.8              | -    | 3.7               | -    | V    |
| V <sub>OL</sub> | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> ; V <sub>CC</sub> = 4.5 V          |       |     |      |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = 50 µA   | -     | 0   | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 8.0 mA  | -     | -   | 0.36 | -                | 0.44 | -                 | 0.55 | V    |
| I <sub>I</sub>  | input leakage current     | V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V                          | -     | -   | 0.1  | -                | 1.0  | -                 | 2.0  | µA   |
| I <sub>CC</sub> | supply current            | V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 5.5 V | -     | -   | 2.0  | -                | 20   | -                 | 40   | µA   |

| Symbol          | Parameter                 | Conditions  | 25 °C |     |      | -40 °C to +85 °C |     | -40 °C to +125 °C |     | Unit |
|-----------------|---------------------------|---|-------|-----|------|------------------|-----|-------------------|-----|------|
|                 |                           |   | Min   | Typ | Max  | Min              | Max | Min               | Max |      |
| $\Delta I_{CC}$ | additional supply current | per input pin;<br>$V_I = V_{CC} - 2.1\text{ V}$ ; $I_O = 0\text{ A}$ ;<br>other pins at $V_{CC}$ or GND;<br>$V_{CC} = 4.5\text{ V}$ to $5.5\text{ V}$ | -     | -   | 1.35 | -                | 1.5 | -                 | 1.5 | mA   |
| $C_I$           | input capacitance         |   | -     | 3.0 | 10   | -                | 10  | -                 | 10  | pF   |

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V; For test circuit see Fig. 5.

| Symbol          | Parameter                     | Conditions  | 25 °C |        |      | -40 °C to +85 °C |      | -40 °C to +125 °C |      | Unit |
|-----------------|-------------------------------|---|-------|--------|------|------------------|------|-------------------|------|------|
|                 |                               |   | Min   | Typ[1] | Max  | Min              | Max  | Min               | Max  |      |
| 74AHC08         |                               |   |       |        |      |                  |      |                   |      |      |
| t <sub>pd</sub> | propagation delay             | nA, nB to nY; see Fig. 4 [2]  |       |        |      |                  |      |                   |      |      |
|                 |                               | V <sub>CC</sub> = 3.0 V to 3.6 V  |       |        |      |                  |      |                   |      |      |
|                 |                               | C <sub>L</sub> = 15 pF  | -     | 4.0    | 8.8  | 1.0              | 10.5 | 1.0               | 11.0 | ns   |
|                 |                               | C <sub>L</sub> = 50 pF  | -     | 5.6    | 12.3 | 1.0              | 14   | 1.0               | 15.5 | ns   |
|                 |                               | V <sub>CC</sub> = 4.5 V to 5.5 V  |       |        |      |                  |      |                   |      |      |
|                 |                               | C <sub>L</sub> = 15 pF  | -     | 3.0    | 5.9  | 1.0              | 7.0  | 1.0               | 7.5  | ns   |
|                 |                               | C <sub>L</sub> = 50 pF  |       | 4.2    | 7.9  | 1.0              | 9.0  | 1.0               | 10.0 | ns   |
| C <sub>PD</sub> | power dissipation capacitance | C <sub>L</sub> = 50 pF; f <sub>i</sub> = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub> [3] | -     | 10.0   | -    | -                | -    | -                 | -    | pF   |
| 74AHCT08        |                               |   |       |        |      |                  |      |                   |      |      |
| t <sub>pd</sub> | propagation delay             | nA, nB to nY; see Fig. 4 [2]  |       |        |      |                  |      |                   |      |      |
|                 |                               | V <sub>CC</sub> = 4.5 V to 5.5 V  |       |        |      |                  |      |                   |      |      |
|                 |                               | C <sub>L</sub> = 15 pF  | -     | 3.2    | 6.9  | 1.0              | 8.0  | 1.0               | 9.0  | ns   |
|                 |                               | C <sub>L</sub> = 50 pF  | -     | 4.2    | 7.9  | 1.0              | 9.0  | 1.0               | 10.0 | ns   |
| C <sub>PD</sub> | power dissipation capacitance | C <sub>L</sub> = 50 pF; f <sub>i</sub> = 1 MHz; V <sub>I</sub> = GND to V <sub>CC</sub> [3] | -     | 12.0   | -    | -                | -    | -                 | -    | pF   |

[1] Typical values are measured at nominal supply voltage ( $V_{CC} = 3.3\text{ V}$  and  $V_{CC} = 5.0\text{ V}$ ).

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu\text{W}$ ).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$  where:

$f_i$  = input frequency in MHz,  $f_o$  = output frequency in MHz

$C_L$  = output load capacitance in pF

$V_{CC}$  = supply voltage in Volts

$N$  = number of inputs switching

$\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

10.1. Waveform and test circuit

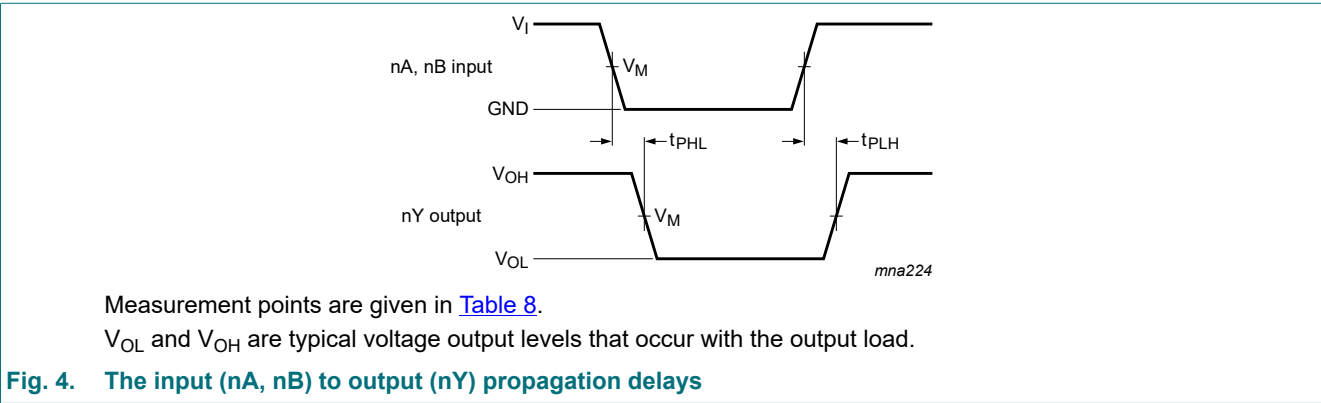


Table 8. Measurement points

| Type     | Input       |  | Output      |
|----------|-------------|--|-------------|
|          | $V_M$       |  | $V_M$       |
| 74AHC08  | $0.5V_{CC}$ |  | $0.5V_{CC}$ |
| 74AHCT08 | 1.5 V       |  | $0.5V_{CC}$ |

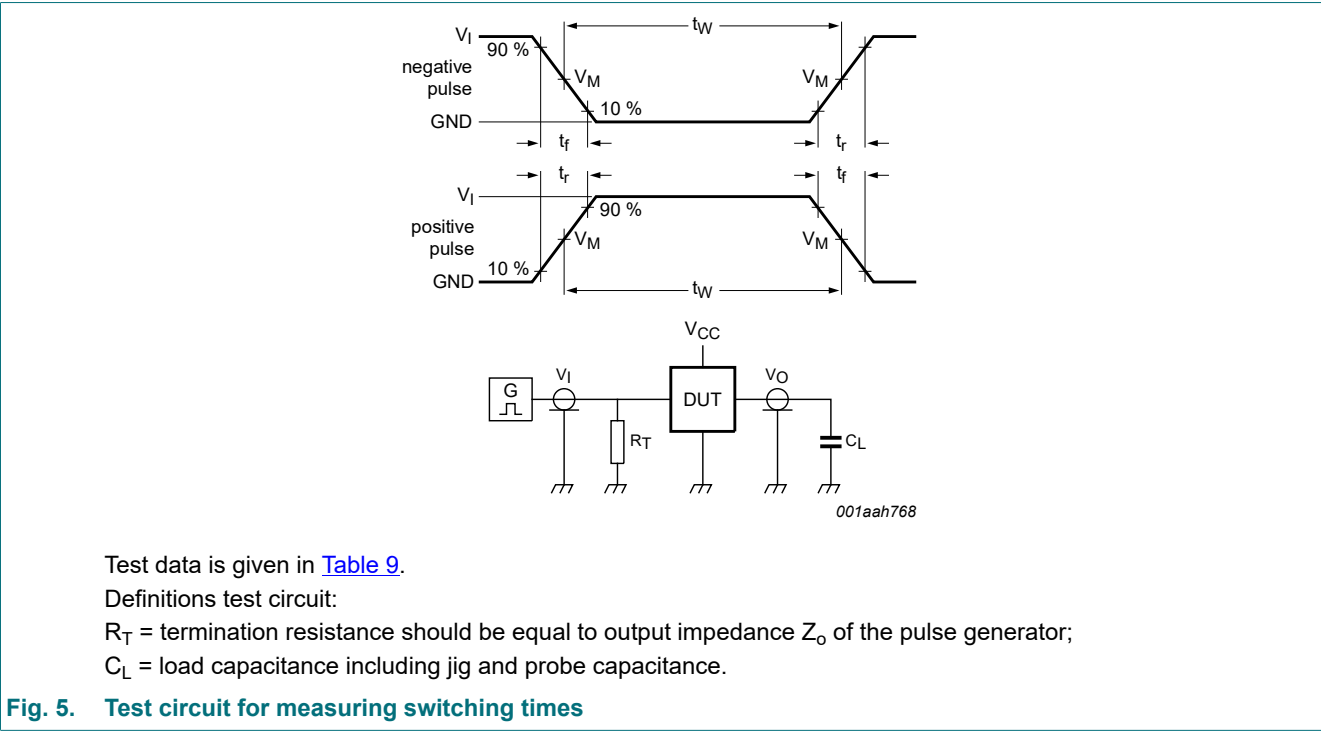


Table 9. Test data

| Type     | Input    |               | Load         | Test               |
|----------|----------|---------------|--------------|--------------------|
|          | $V_I$    | $t_r, t_f$    | $C_L$        |                    |
| 74AHC08  | $V_{CC}$ | $\leq 3.0$ ns | 15 pF, 50 pF | $t_{PLH}, t_{PHL}$ |
| 74AHCT08 | 3.0 V    | $\leq 3.0$ ns | 15 pF, 50 pF | $t_{PLH}, t_{PHL}$ |

11. Package outline

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1

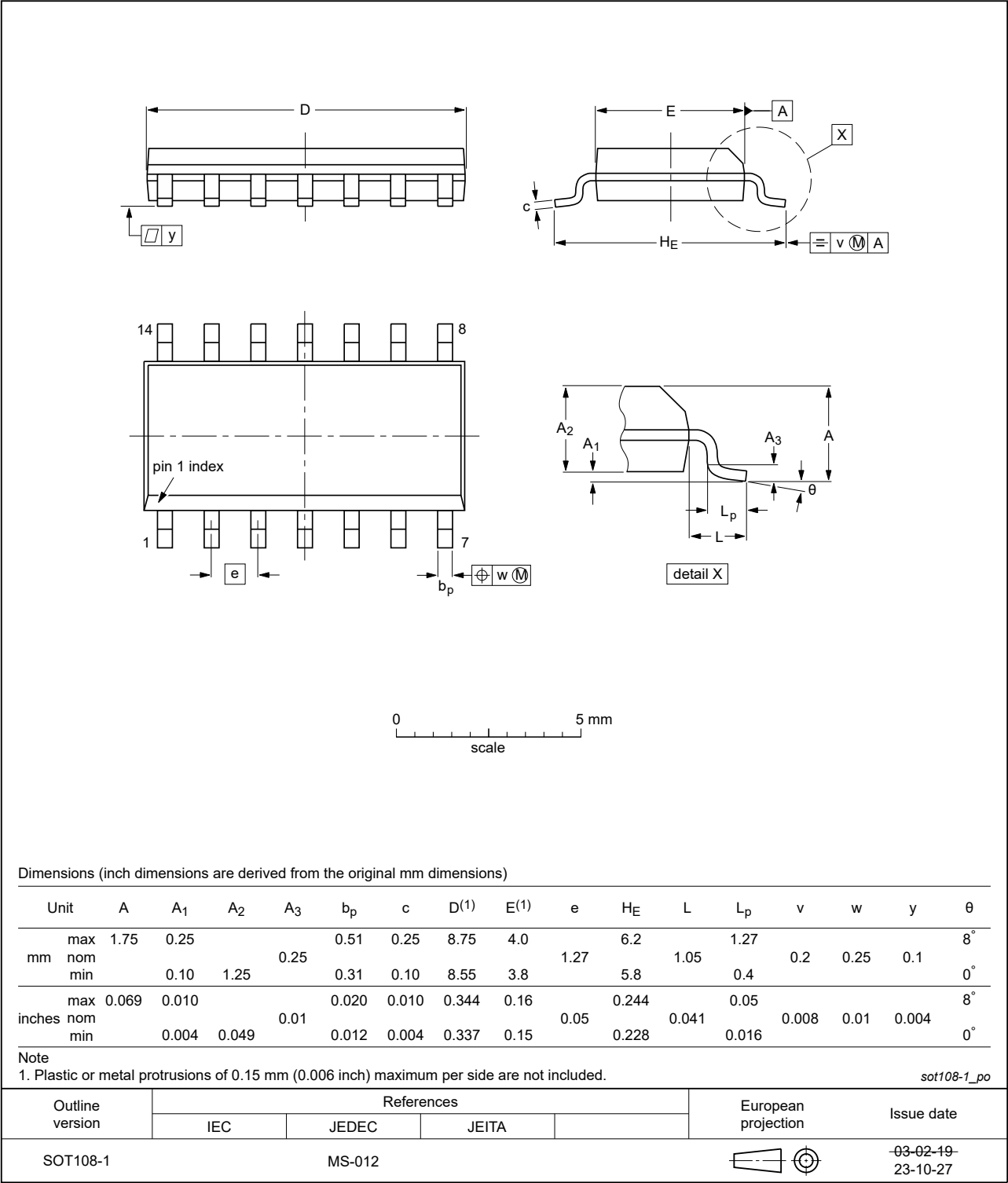


Fig. 6. Package outline SOT108-1 (SO14)



TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1

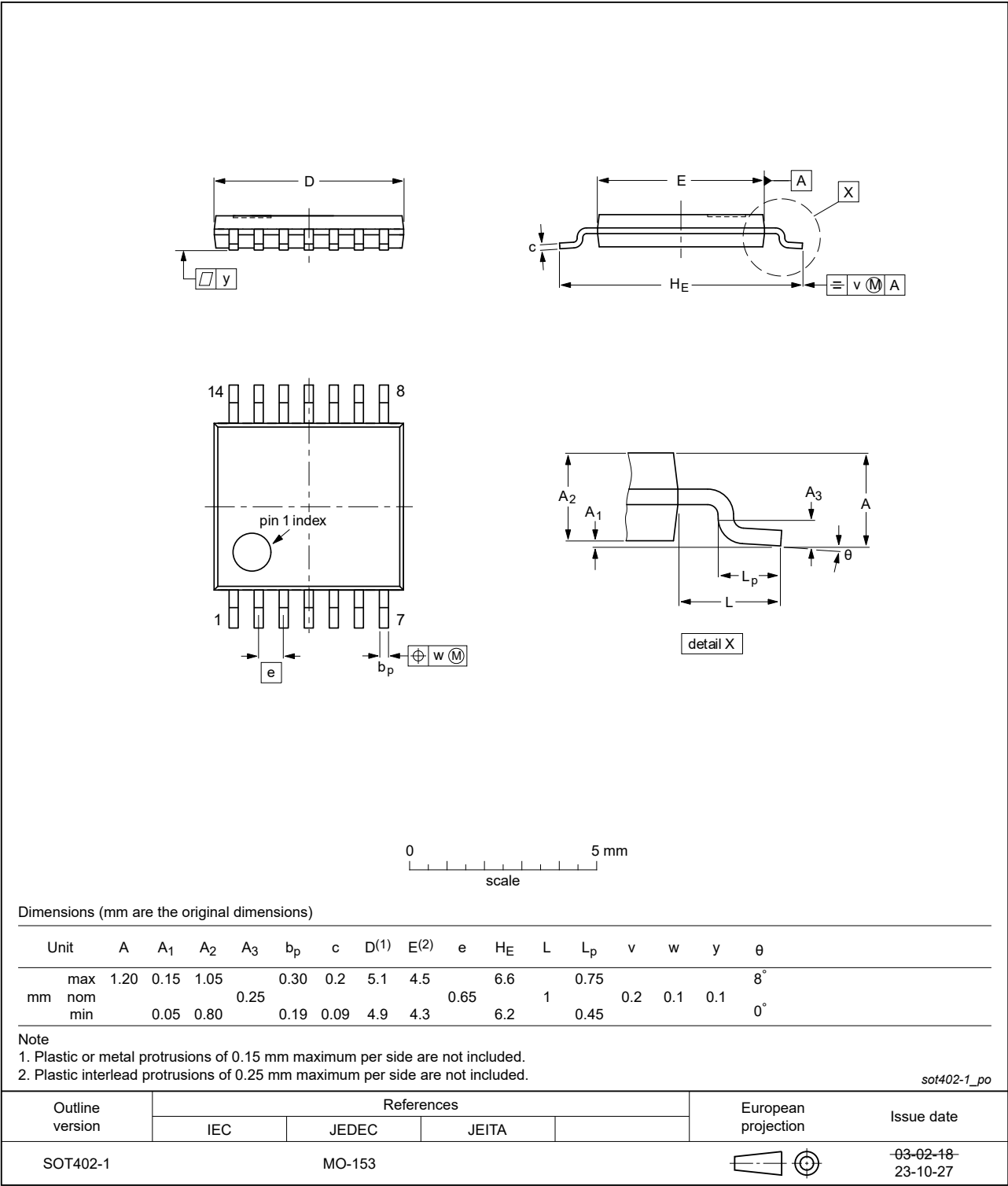


Fig. 7. Package outline SOT402-1 (TSSOP14)

DHVQFN14: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;  
14 terminals; body 2.5 x 3 x 0.85 mm

SOT762-1

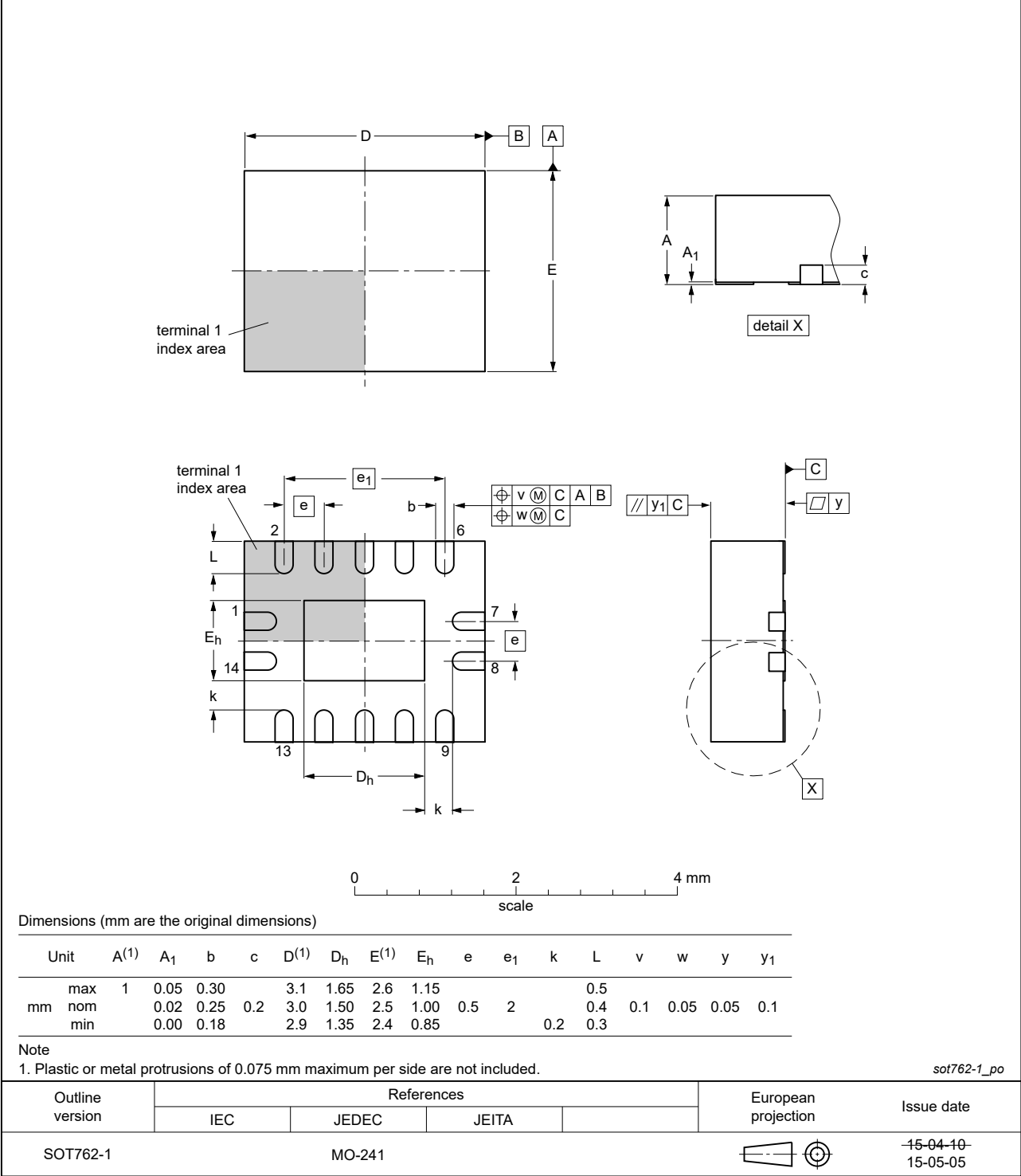


Fig. 8. Package outline SOT762-1 (DHVQFN14)

DHXQFN14: plastic, leadless dual in-line compatible thermal enhanced extreme thin quad flat package;  
no leads; 14 terminals; 0.4 mm pitch; body 2 mm x 2 mm x 0.48 mm

SOT8014-1

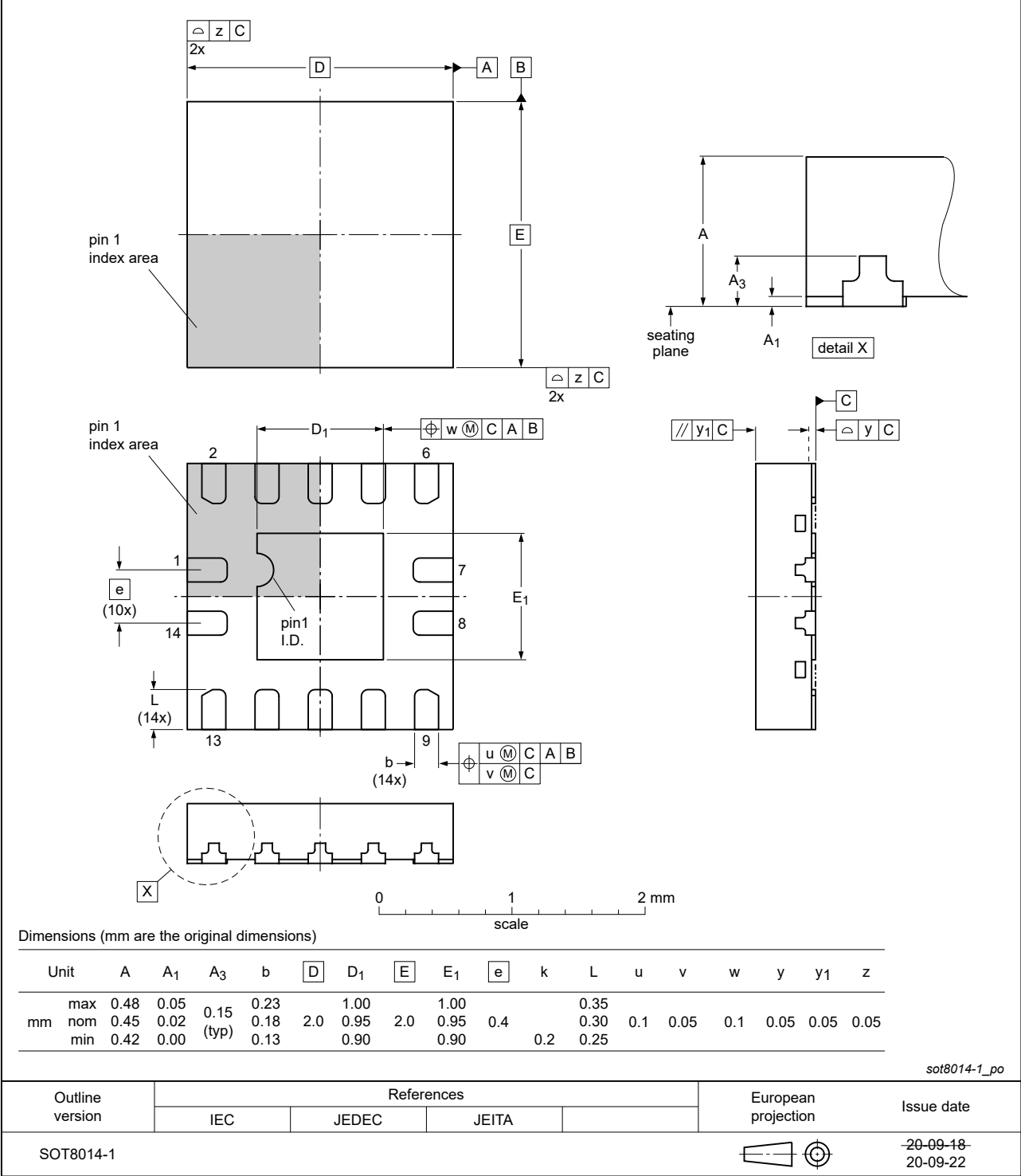


Fig. 9. Package outline SOT8014-1 (DHXQFN14)

12. Abbreviations

Table 10. Abbreviations

| Acronym | Description                               |
|---------|---|
| ANSI    | American National Standards Institute     |
| CDM     | Charged Device Model                      |
| CMOS    | Complementary Metal Oxide Semiconductor   |
| ESD     | ElectroStatic Discharge                   |
| ESDA    | ElectroStatic Discharge Association       |
| HBM     | Human Body Model                          |
| JEDEC   | Joint Electron Device Engineering Council |
| TTL     | Transistor-Transistor Logic               |

13. Revision history

Table 11. Revision history

| Document ID      | Release date   | Data sheet status     | Change notice | Supersedes       |
|------------------|--|-----------------------|---------------|------------------|
| 74AHC_AHCT08 v.7 | 20250423   | Product data sheet    | -             | 74AHC_AHCT08 v.6 |
| Modifications:   | <ul style="list-style-type: none"><li>Type numbers 74AHC08BZ and 74AHCT08BZ (SOT8014-1/DHXQFN14) added.</li></ul>  |                       |               |                  |
| 74AHC_AHCT08 v.6 | 20240205   | Product data sheet    | -             | 74AHC_AHCT08 v.5 |
| Modifications:   | <ul style="list-style-type: none"><li><a href="#">Fig. 6</a>, <a href="#">Fig. 7</a>: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153.</li></ul>  |                       |               |                  |
| 74AHC_AHCT08 v.5 | 20230901   | Product data sheet    | -             | 74AHC_AHCT08 v.4 |
| Modifications:   | <ul style="list-style-type: none"><li><a href="#">Section 2</a>: ESD specification updated according to the latest JEDEC standard.</li></ul>   |                       |               |                  |
| 74AHC_AHCT08 v.4 | 20200526   | Product data sheet    | -             | 74AHC_AHCT08 v.3 |
| Modifications:   | <ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>Legal texts have been adapted to the new company name where appropriate.</li><li><a href="#">Section 1</a> and <a href="#">Section 2</a> updated.</li><li><a href="#">Section 7</a>: Derating values for P<sub>tot</sub> total power dissipation have been updated.</li><li><a href="#">Fig. 5</a>: Test circuit corrected (Errata).</li><li>Package outline drawing of SOT762-1 (<a href="#">Fig. 8</a>) updated.</li></ul> |                       |               |                  |
| 74AHC_AHCT08 v.3 | 20071114   | Product data sheet    | -             | 74AHC_AHCT08 v.2 |
| Modifications:   | <ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>Legal texts have been adapted to the new company name where appropriate.</li><li><a href="#">Section 3</a>: DHVQFN14 package added.</li><li><a href="#">Section 7</a>: derating values added for DHVQFN14 package.</li><li><a href="#">Section 11</a>: outline drawing added for DHVQFN14 package.</li></ul>   |                       |               |                  |
| 74AHC_AHCT08 v.2 | 19990924   | Product specification | -             | 74AHC_AHCT08 v.1 |
| 74AHC_AHCT08 v.1 | 19981218   | Product specification | -             | -                |

## 14. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition  |
|--------------------------------|--------------------|---|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
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