

2N7002 60 V, 300 mA N-channel Trench MOSFET Rev. 7 — 8 September 2011

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Suitable for logic level gate drive sources
- Very fast switching

1.3 Applications

Logic level translators

- Surface-mounted package
- Trench MOSFET technology
- High-speed line drivers

1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 150 °C	-	-	60	V
I _D	drain current	V _{GS} = 10 V; T _{sp} = 25 °C; see <u>Figure 1</u> ; see <u>Figure 3</u>	-	-	300	mA
P _{tot}	total power dissipation	T _{sp} = 25 °C; see <u>Figure 2</u>	-	-	0.83	W
Static cha	aracteristics					
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_D = 500 mA; T_j = 25 °C; see <u>Figure 6</u> ; see <u>Figure 8</u>	-	2.8	5	Ω

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	S	source		
3	D	drain		G (FA)
			SOT23 (TO-236AB)	mbb076 S



3. Ordering information

Table 3. Orderin	g information		
Type number	Package		
	Name	Description	Version
2N7002	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4.Marking codes

Type number	Marking code ^[1]
2N7002	12%

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

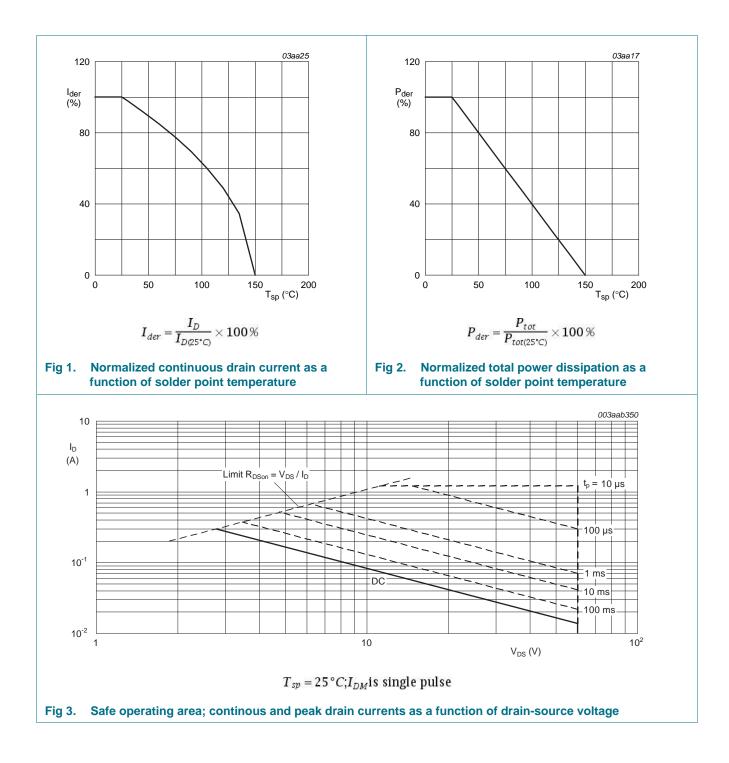
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage	25 °C ≤ T _j ≤ 150 °C	-	60	V
V _{DGR}	drain-gate voltage	25 °C \leq T _j \leq 150 °C; R _{GS} = 20 k Ω	-	60	V
V _{GS}	gate-source voltage		-30	30	V
V _{GSM}	peak gate-source voltage	pulsed; $t_p \le 50 \ \mu s$; $\delta = 0.25$	-40	40	V
I _D	drain current	V _{GS} = 10 V; T _{sp} = 25 °C; see <u>Figure 1</u> ; see <u>Figure 3</u>	-	300	mA
		V_{GS} = 10 V; T_{sp} = 100 °C; see <u>Figure 1</u>	-	190	mA
I _{DM}	peak drain current	pulsed; t _p ≤ 10 µs; T _{sp} = 25 °C; see <u>Figure 3</u>	-	1.2	A
P _{tot}	total power dissipation	T _{sp} = 25 °C; see <u>Figure 2</u>	-	0.83	W
Tj	junction temperature		-65	150	°C
T _{stg}	storage temperature		-65	150	°C
Source-drai	in diode				
I _S	source current	T _{sp} = 25 °C	-	300	mA
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{sp} = 25 \ ^{\circ}C$	-	1.2	А

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6. Thermal characteristics

Symbol	Par	ameter	Conditio	ons		Min	Тур	Max	Unit
R _{th(j-a)}	(j-a) thermal resistance from junction to ambient			Mounted on a printed-circuit board; minimum footprint; vertical in still air				350	K/W
R _{th(j-sp)}		rmal resistance n junction to sol nt	see <u>Figu</u> der	<u>re 4</u>		-	-	150	K/W
10 ³								003aab351	
10									
Z _{th(j-sp)}									
(K/W)									
10 ²									
10	δ=0.5								
	0.2								
	-0.1					P		$\delta = \frac{t_p}{T}$	
10	0.05						_		
	0.02								
		single pulse						┦─┗╴╞	
1							► t _p I ← I ← T →	↓ t	
	0 ⁻⁵	10 ⁻⁴	10 ⁻³	10 ⁻²	10 ⁻¹	1	t _p (s)	10	
Fig 4.	Fransient	thermal imped	ance from iur	iction to solder p	oint as a functio	n of pulse (

Table 6. Thermal characteristics

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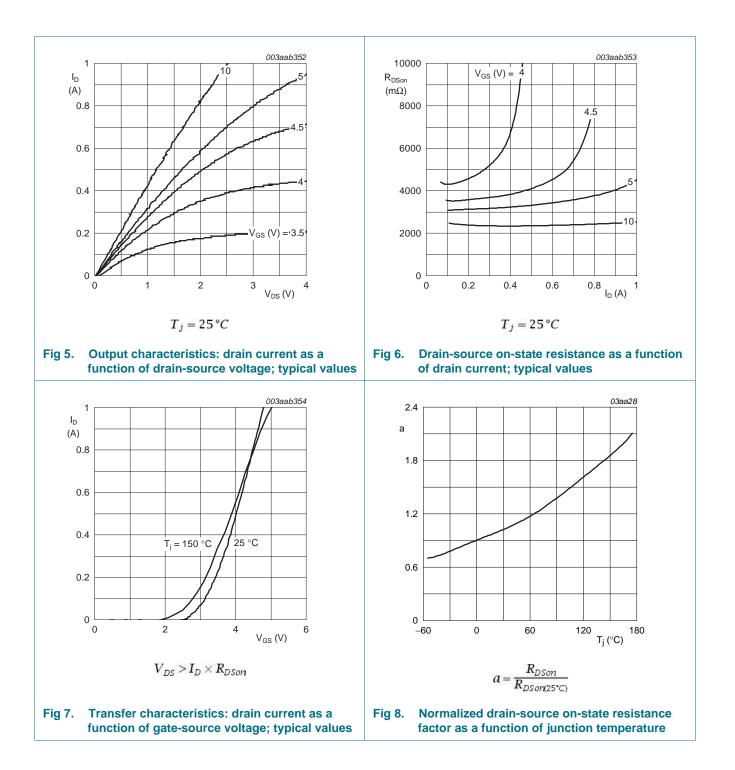
7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
V _{(BR)DSS}	drain-source	$I_D = 10 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^\circ C$	60	-	-	V
	breakdown voltage	$I_D = 10 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = -55 \ ^\circ C$	55	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 0.25 mA; V _{DS} = V _{GS} ; T _j = 25 °C; see <u>Figure 9</u> ; see <u>Figure 10</u>	1	2	2.5	V
		I _D = 0.25 mA; V _{DS} = V _{GS} ; T _j = 150 °C; see <u>Figure 9</u> ; see <u>Figure 10</u>	0.6	-	-	V
		$I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ see <u>Figure 9</u> ; see <u>Figure 10</u>	-	-	2.75	V
I _{DSS}	drain leakage current	V _{DS} = 48 V; V _{GS} = 0 V; T _j = 25 °C	-	0.01	1	μA
		V _{DS} = 48 V; V _{GS} = 0 V; T _j = 150 °C	-	-	10	μA
I _{GSS}	gate leakage current	V _{GS} = 15 V; V _{DS} = 0 V; T _j = 25 °C	-	10	100	nA
		$V_{GS} = -15 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	10	100	nA
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I _D = 500 mA; T _j = 25 °C; see <u>Figure 6</u> ; see <u>Figure 8</u>	-	2.8	5	Ω
		V_{GS} = 10 V; I _D = 500 mA; T _j = 150 °C; see <u>Figure 6</u> ; see <u>Figure 8</u>	-	-	9.25	Ω
		V _{GS} = 4.5 V; I _D = 75 mA; T _j = 25 °C; see <u>Figure 6</u> ; see <u>Figure 8</u>	-	3.8	5.3	Ω
Dynamic	characteristics					
C _{iss}	input capacitance	$V_{DS} = 10 \text{ V}; \text{ f} = 1 \text{ MHz}; \text{ V}_{GS} = 0 \text{ V};$	-	31	50	pF
C _{oss}	output capacitance	$T_j = 25 \ ^{\circ}C$	-	6.8	30	pF
C _{rss}	reverse transfer capacitance		-	3.5	10	pF
t _{on}	turn-on time	V_{GS} = 10 V; V_{DS} = 50 V; R_{L} = 250 Ω ;	-	2.5	10	ns
t _{off}	turn-off time	$R_{G(ext)} = 50 \ \Omega; \ R_{GS} = 50 \ \Omega$	-	11	15	ns
Source-d	rain diode					
V _{SD}	source-drain voltage	I _S = 300 mA; V _{GS} = 0 V; T _j = 25 °C; see <u>Figure 11</u>	-	0.85	1.5	V
Q _r	recovered charge	$V_{GS} = 0 V; I_S = 300 mA;$	-	30	-	nC
t _{rr}	reverse recovery time	dl _S /dt = -100 A/µs	-	30	-	ns

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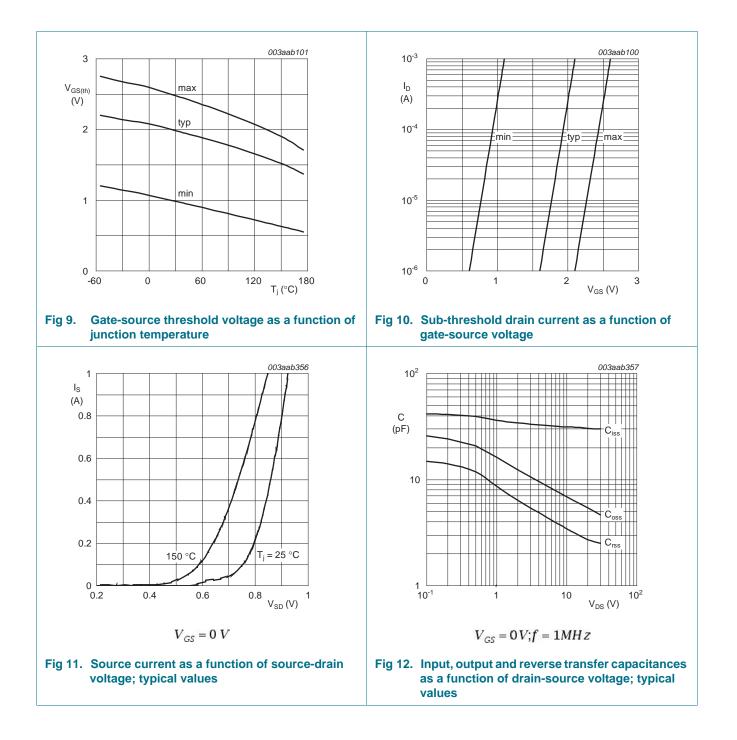
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8. Package outline

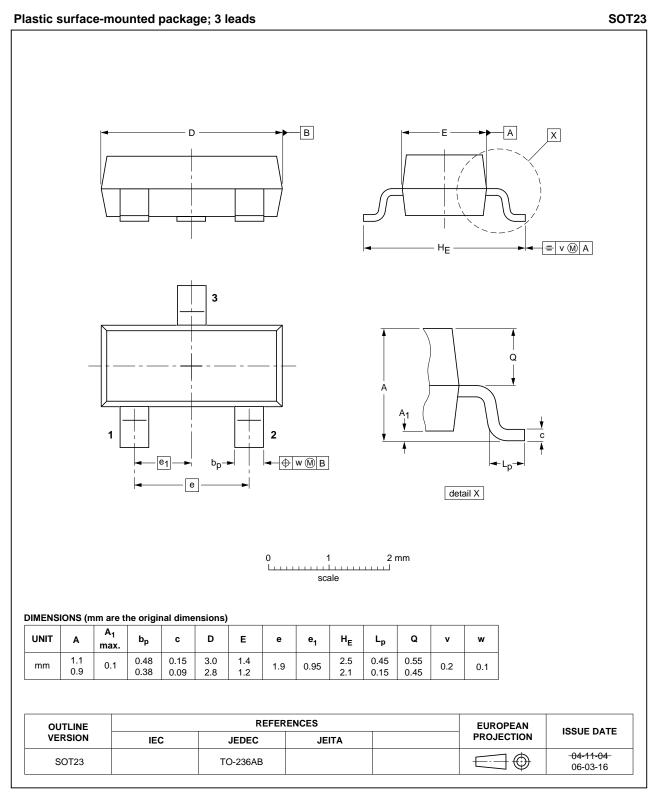
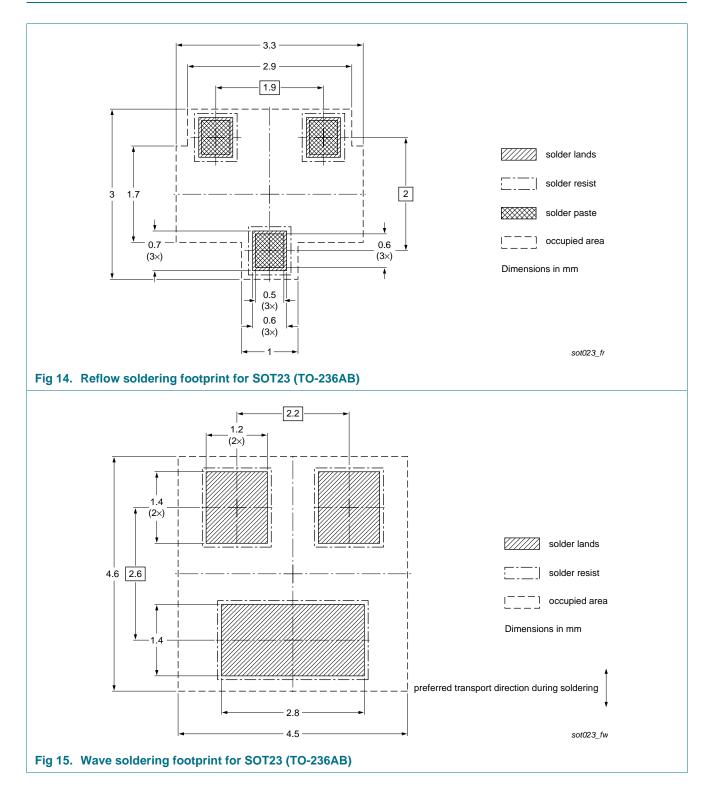


Fig 13. Package outline SOT23 (TO-236AB)

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9. Soldering



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10. Revision history

Table 8. Revisio	on history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
2N7002 v.7	20110908	Product data sheet	-	2N7002 v.6
Modifications:	 The format of t of NXP Semice 	his data sheet has been red onductors.	esigned to comply with	the new identity guidelines
	 Legal texts have 	ve been adapted to the new	company name where	appropriate.
2N7002 v.6	20060428	Product data sheet		2N7002 v.5
2N7002 v.5	20051115	Product data sheet		2N7002 v.4
2N7002 v.4	20050426	Product data sheet		2N7002 v.3
2N7002 v.3	20000727	Product specification	HZG336	2N7002 v.2
2N7002 v.2	19970617	Product specification		2N7002 v.1
2N7002 v.1	19901031	Product specification	-	-

11. Legal information

11.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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